

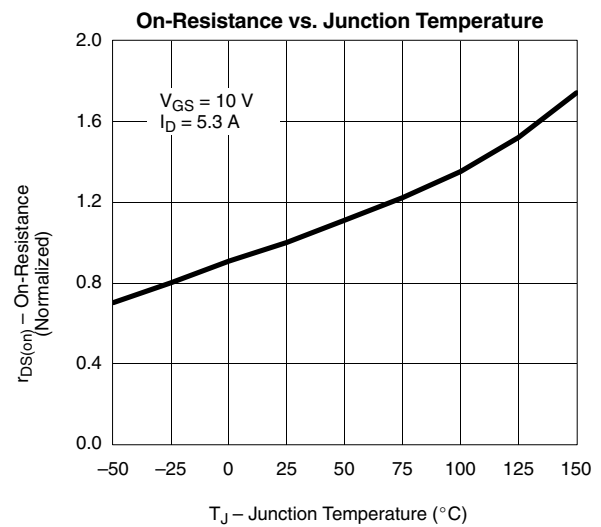
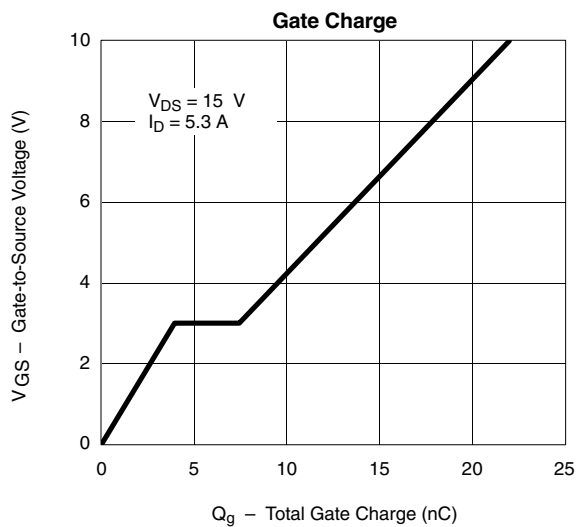
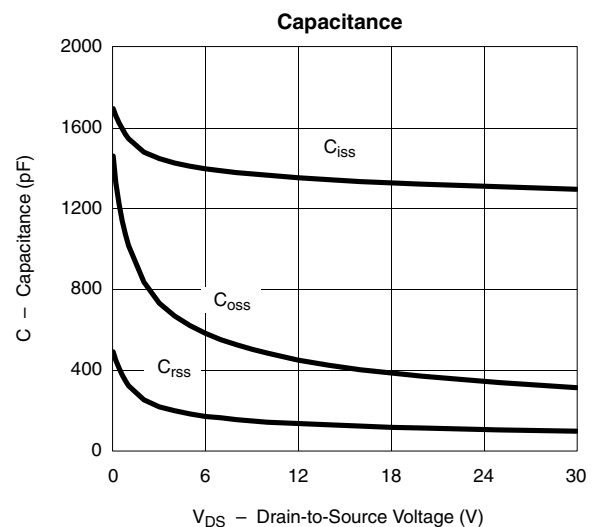
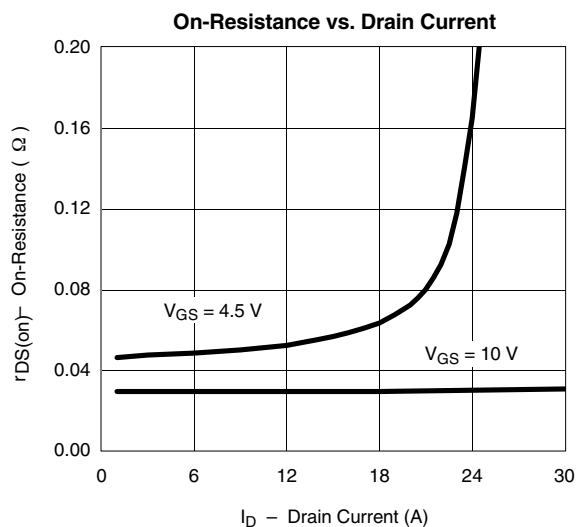
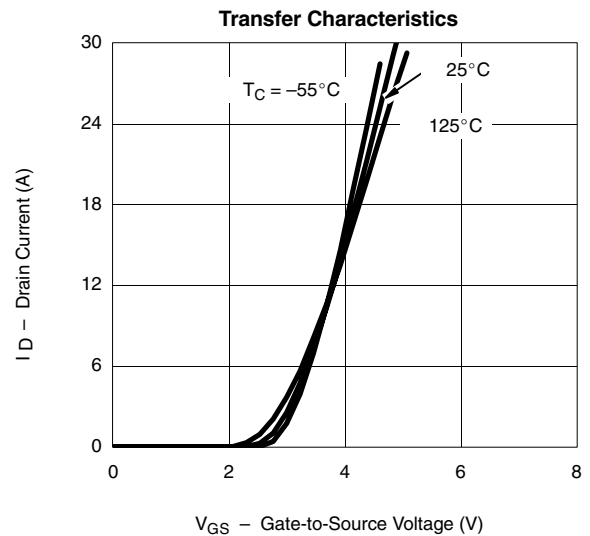
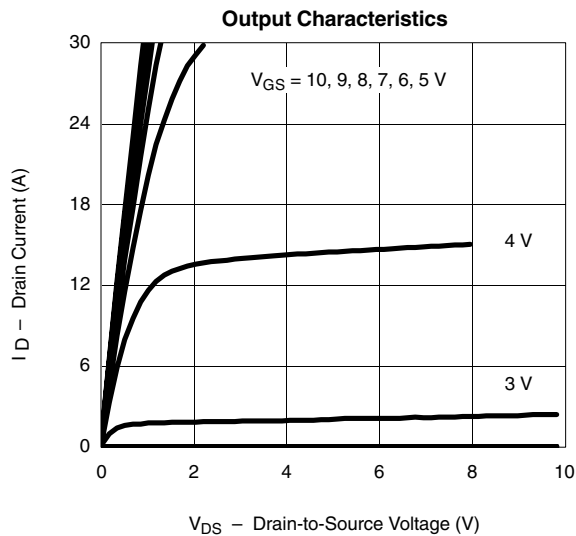
SPECIFICATIONS (T _J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ ^A	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-1.0			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -30 V, V _{GS} = 0 V, T _J = 70 °C			-25	
On-State Drain Current ^b	I _{D(on)}	V _{DS} ≤ -5 V, V _{GS} = -10 V	-30			A
		V _{DS} ≤ -5 V, V _{GS} = -4.5 V	-7			
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = -10 V, I _D = -5.3 A		0.029	0.040	Ω
		V _{GS} = -4.5 V, I _D = -2.0 A		0.047	0.070	
Forward Transconductance ^b	g _{fs}	V _{DS} = -15 V, I _D = -5.3 A		9.3		S
Diode Forward Voltage ^b	V _{SD}	I _S = -2.3 A, V _{GS} = 0 V		-0.78	-1.2	V
Dynamic^a						
Total Gate Charge	Q _g	V _{DS} = -15 V, V _{GS} = -10 V, I _D = -5.3 A		22	35	nC
Gate-Source Charge	Q _{gs}			3.95		
Gate-Drain Charge	Q _{gd}			3.5		
Gate Resistance	R _g			4.5	6.1	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = -15 V, R _L = 15 Ω I _D ≅ -1 A, V _{GEN} = -10 V, R _G = 6 Ω		11.5	20	ns
Rise Time	t _r			12	20	
Turn-Off Delay Time	t _{d(off)}			38	55	
Fall Time	t _f			15	25	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = -2.3 A, di/dt = 100 A/μs		50	80	

Notes

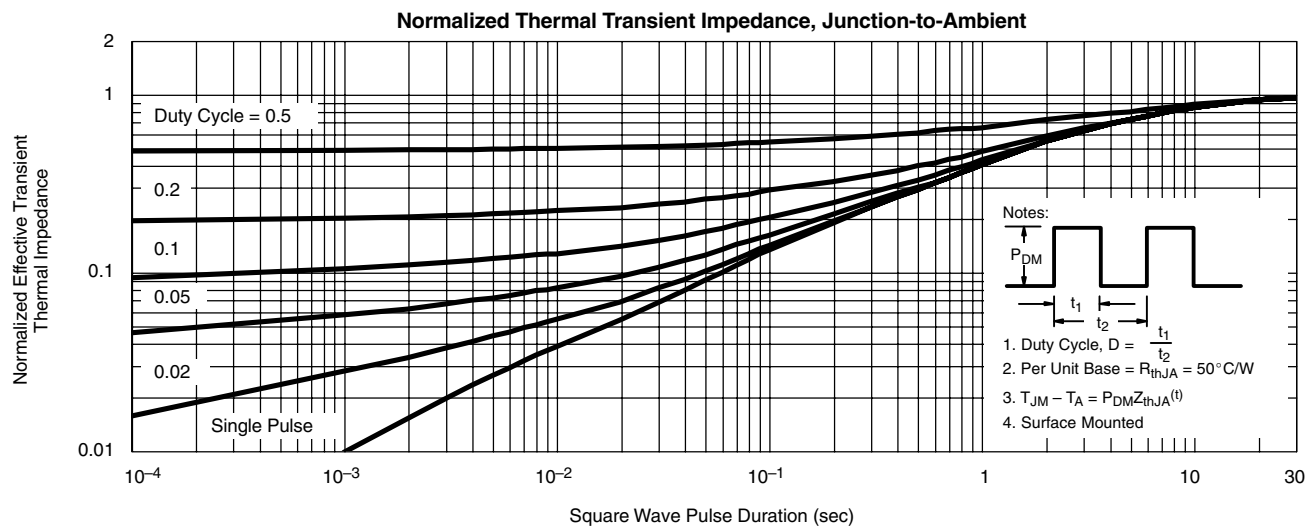
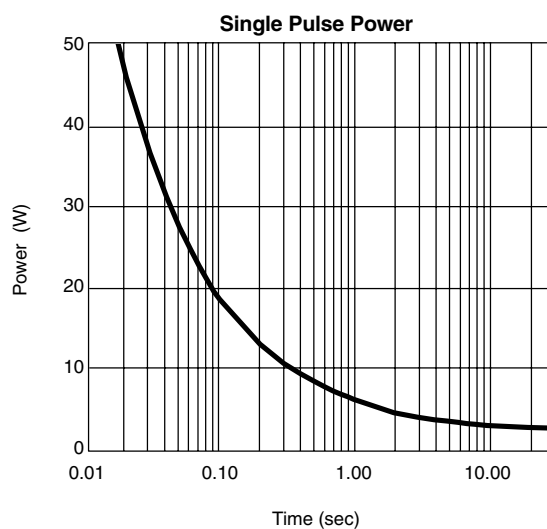
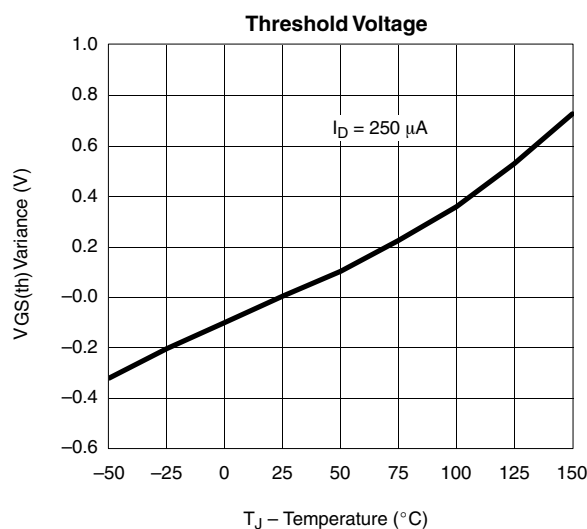
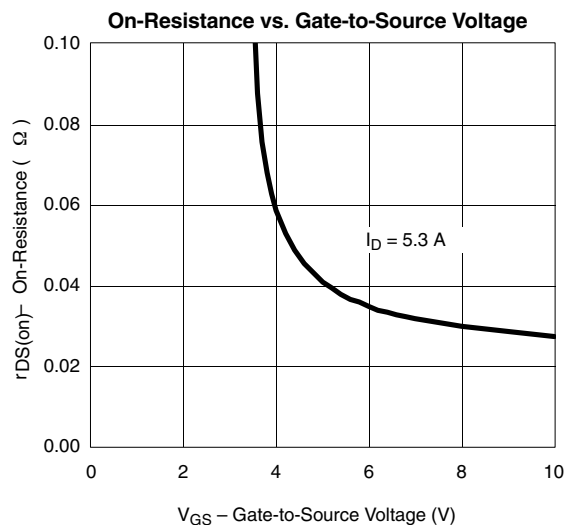
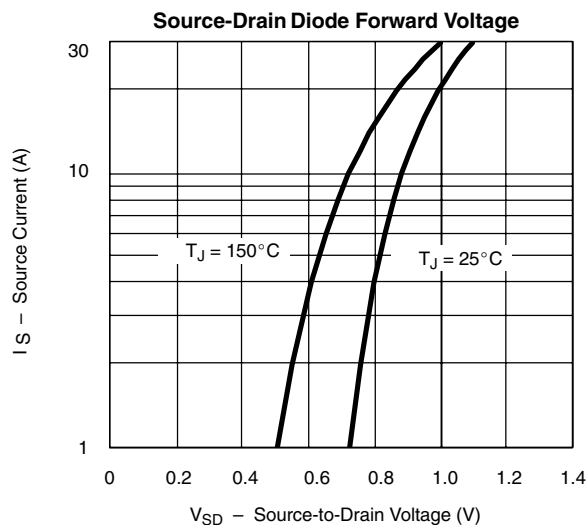
- Guaranteed by design, not subject to production testing.
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.



TYPICAL CHARACTERISTICS (25°C UNLESS OTHERWISE NOTED)



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