# Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	- 1.0		- 3	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA
		$V_{DS}$ = - 30 V, $V_{GS}$ = 0 V, $T_J$ = 85 °C			- 5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le$ - 5 V, $V_{GS}$ = - 10 V	- 25			А
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 6.2 A		0.028	0.035	Ω
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5.0 A		0.042	0.053	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -6.2 \text{ A}$		14		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V
Dynamic <sup>b</sup>						
Total Gate Charge	Qg	$V_{DS}$ = - 15 V, $V_{GS}$ = - 10 V, $I_D$ = - 6.2 A		23	35	nC
Gate-Source Charge	Q <sub>gs</sub>			3.6		
Gate-Drain Charge	Q <sub>gd</sub>			6		
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DD}$ = - 15 V, R <sub>L</sub> = 15 $\Omega$ I <sub>D</sub> $\cong$ - 1 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 6 $\Omega$		10	15	ns
Rise Time	t <sub>r</sub>			10	15	
Turn-Off Delay Time	t <sub>d(off)</sub>			71	110	
Fall Time	t <sub>f</sub>			45	70	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/μs		45	70	

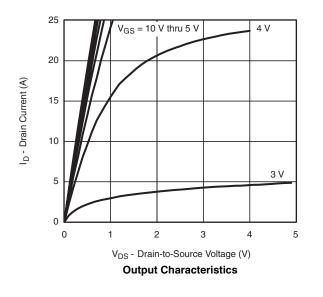
Notes:

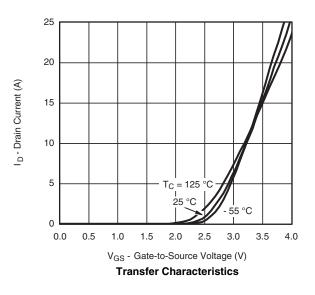
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

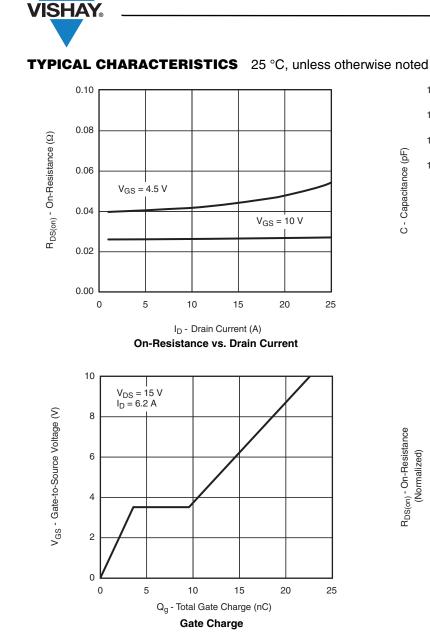
b. Guaranteed by design, not subject to production testing.

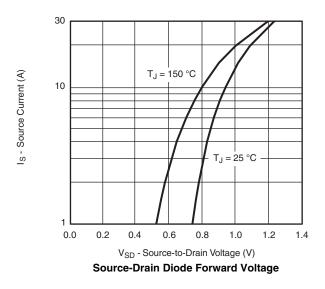
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

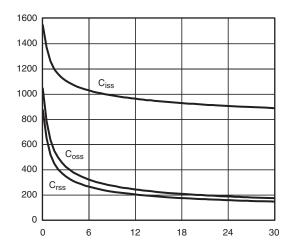
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







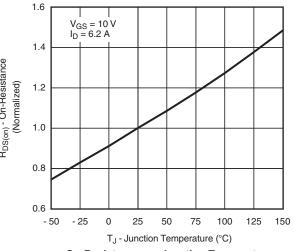




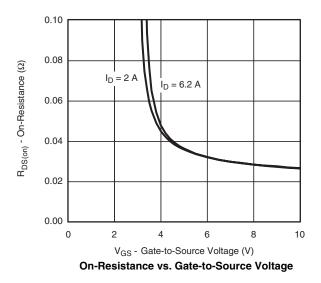
Si3483DV

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V<sub>DS</sub> - Drain-to-Source Voltage (V) Capacitance



**On-Resistance vs. Junction Temperature** 

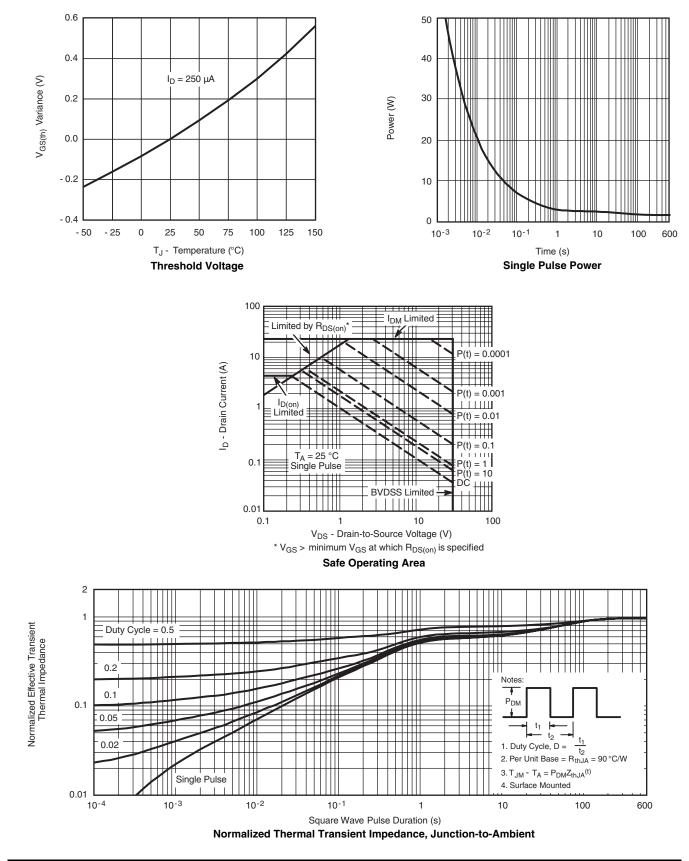


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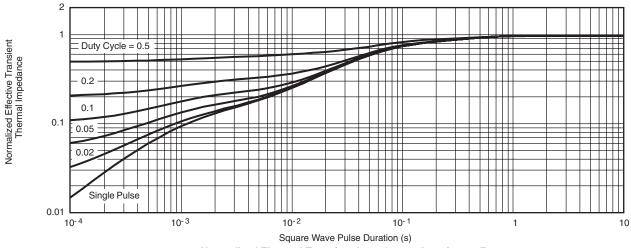




Si3483DV

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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?72078">www.vishay.com/ppg?72078</a>.



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