

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		33		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 2.8		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1		2.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 20	μA
		V _{DS} = 0 V, V _{GS} = ± 4.5 V			± 1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 85 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = ≥ 5 V, V _{GS} = 10 V	1			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 0.5 A		0.540	0.650	Ω
		V _{GS} = 4.5 V, I _D = 0.2 A		0.640	0.770	
Forward Transconductance	g _{fs}	V _{DS} = 10 V, I _D = 0.5 A		1		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		16		pF
Output Capacitance	C _{oss}			8		
Reverse Transfer Capacitance	C _{rss}			4		
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 0.5 A		1	2	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 0.5 A		0.5	1	
Gate-Drain Charge	Q _{gd}			0.20		
Gate Resistance	R _g	f = 1 MHz		50		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 37.5 Ω I _D = 0.38 A, V _{GEN} = 4.5 V, R _g = 1 Ω		8	16	ns
Rise Time	t _r			10	20	
Turn-Off Delay Time	t _{d(off)}			9	18	
Fall Time	t _f			8	16	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 37.5 Ω I _D = 0.38 A, V _{GEN} = 10 V, R _g = 1 Ω		2	4	
Rise Time	t _r			9	18	
Turn-Off Delay Time	t _{d(off)}			7	14	
Fall Time	t _f			8	16	
Drain-Source Body Diode Characteristics						
Pulse Diode Forward Current ^a	I _{SM}				1	A
Body Diode Voltage	V _{SD}	I _S = 0.38 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 0.38 A, dI/dt = 100 A/μs		9	18	ns
Body Diode Reverse Recovery Charge	Q _{rr}			2	4	nC
Reverse Recovery Fall Time	t _a			5		ns
Reverse Recovery Rise Time	t _b			4		

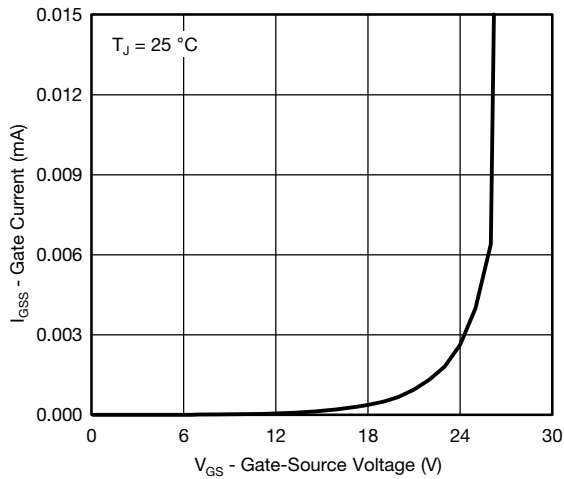
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

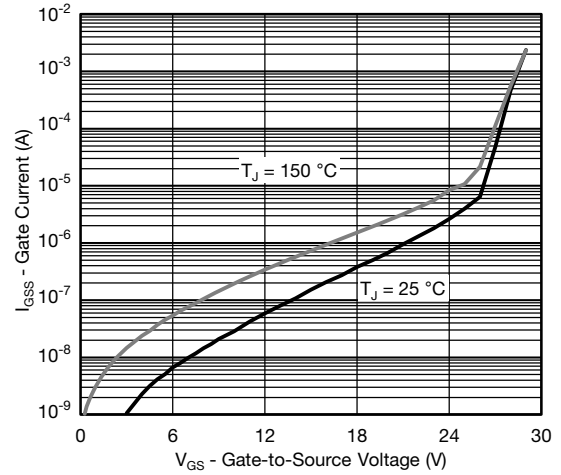
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

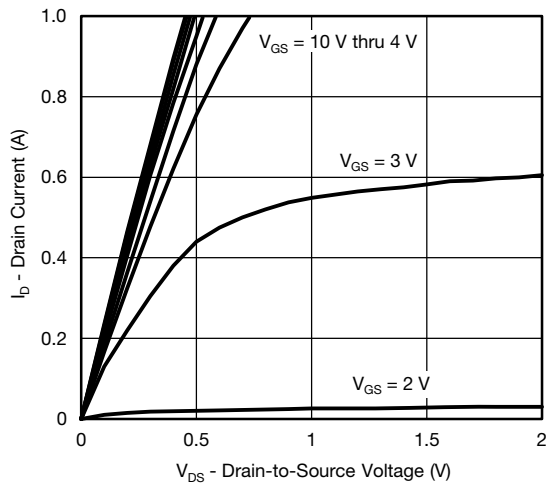
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



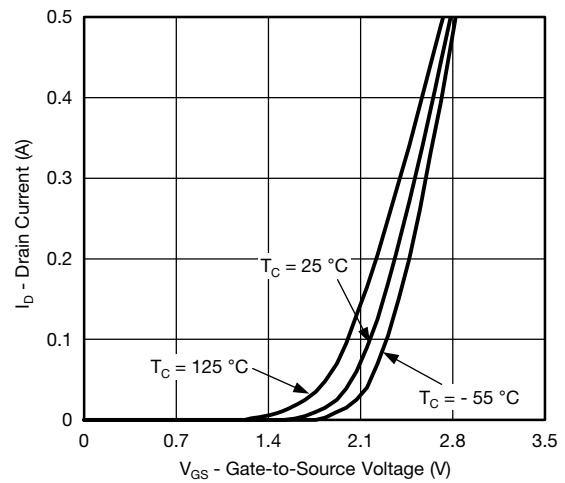
Gate Current vs. Gate-Source Voltage



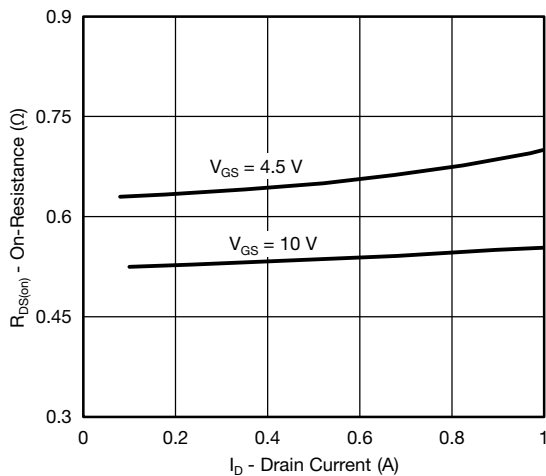
Gate Current vs. Gate-Source Voltage



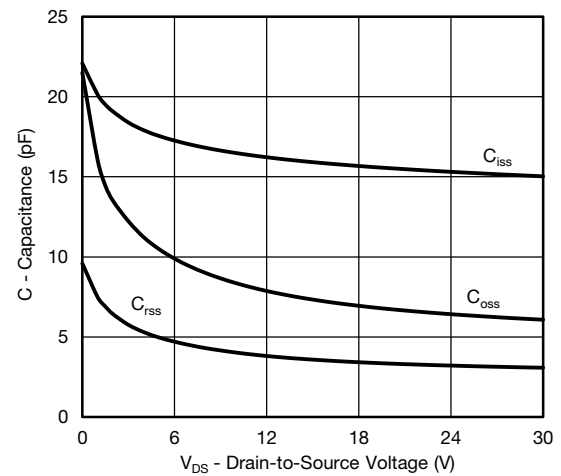
Output Characteristics



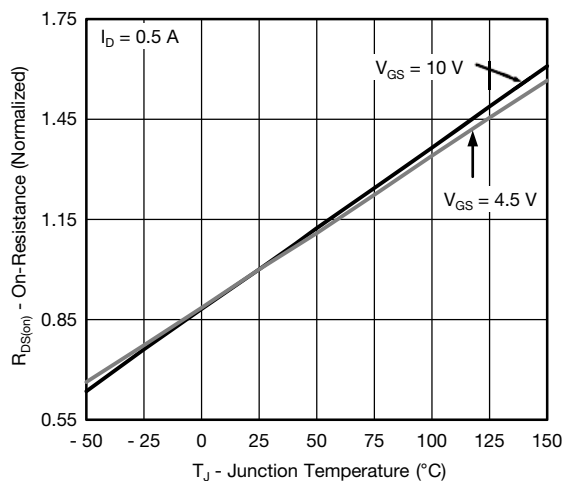
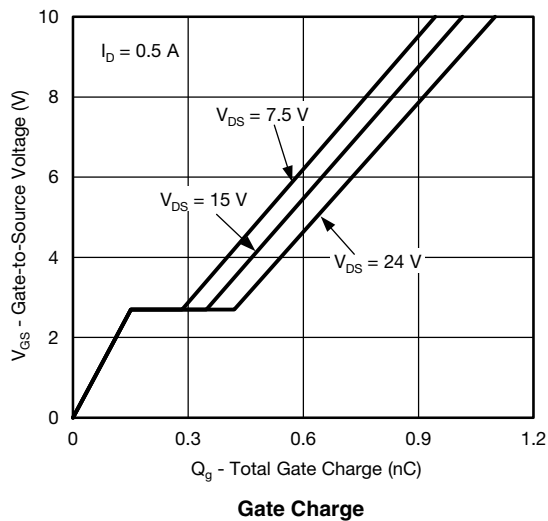
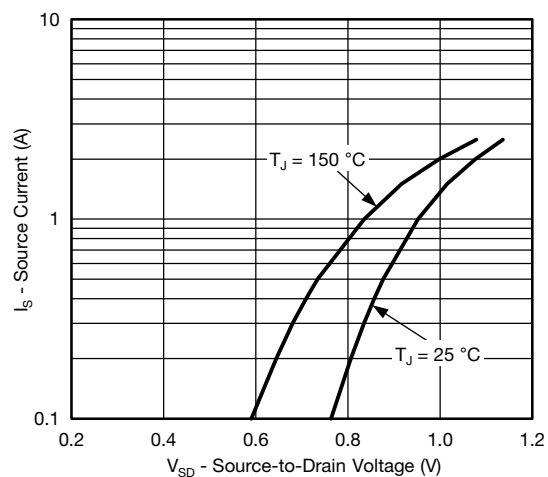
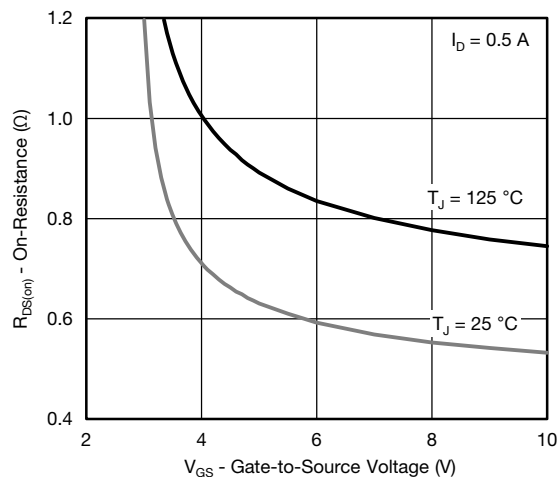
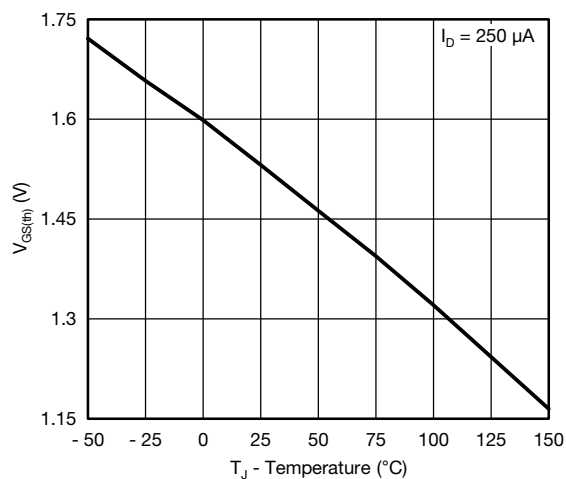
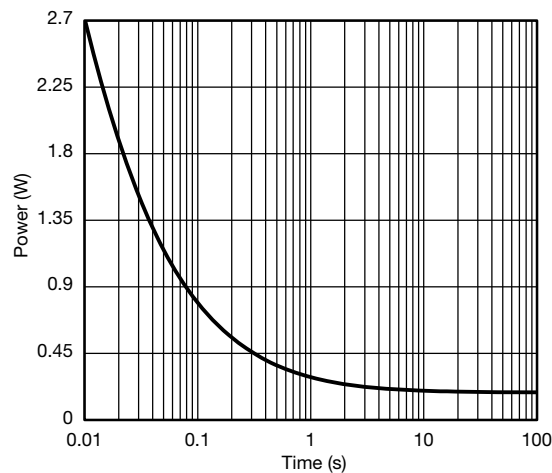
Transfer Characteristics



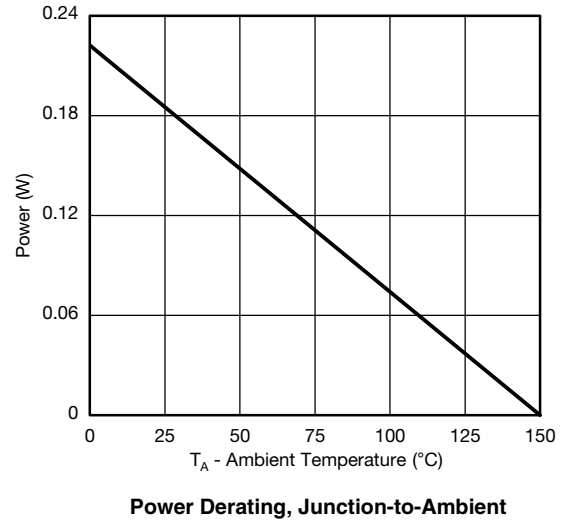
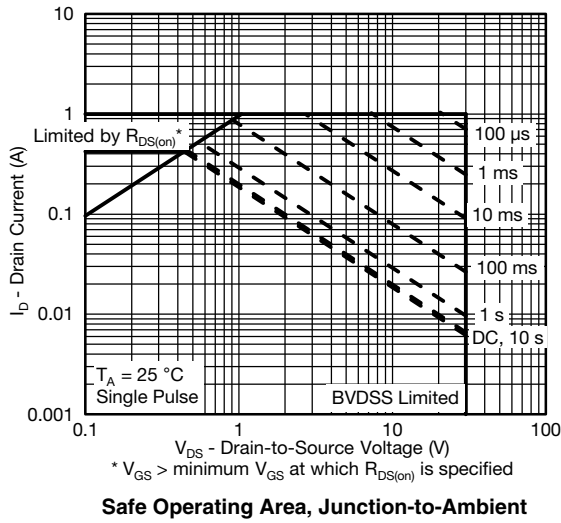
On-Resistance vs. Drain Current



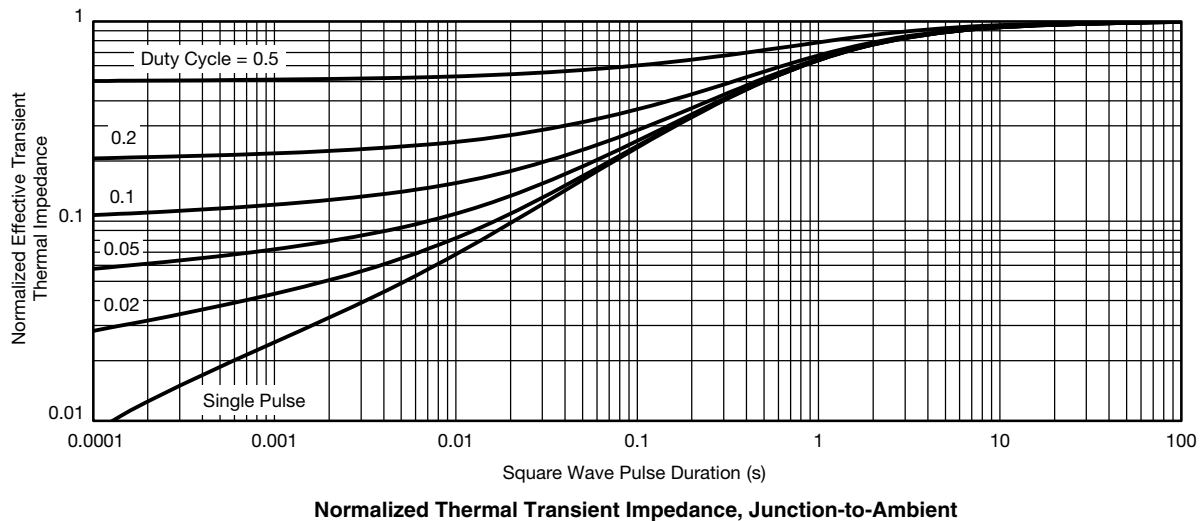
Capacitance

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**On-Resistance vs. Junction Temperature****Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power, Junction-to-Ambient**

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

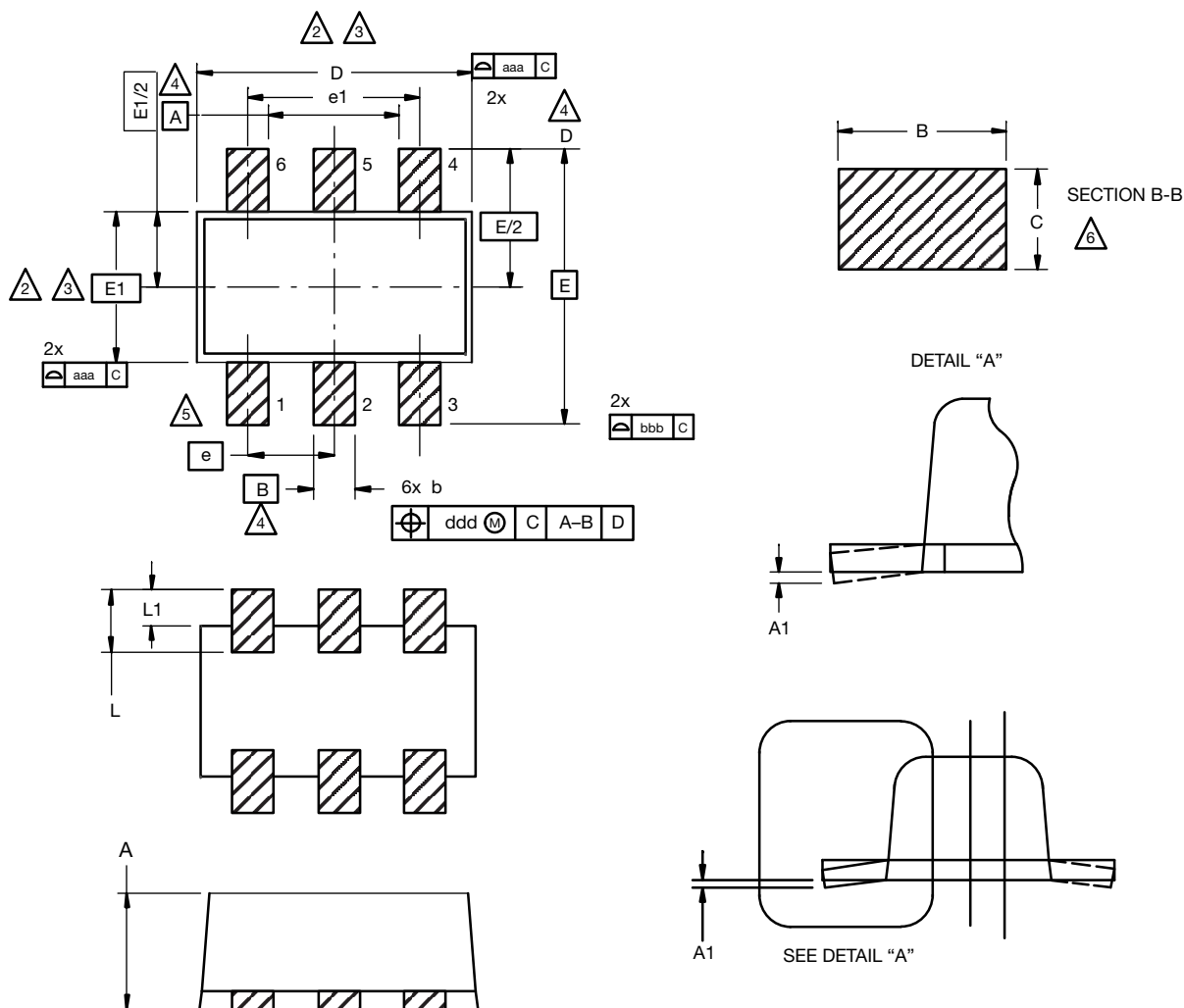


* The power dissipation P_D is based on $T_{J(max)} = 150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



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SC-89 6-Leads (SOT-563F)



Notes

1. Dimensions in millimeters.

1 Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.

2 Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.

3 Datums A, B and D to be determined 0.10 mm from the lead tip.

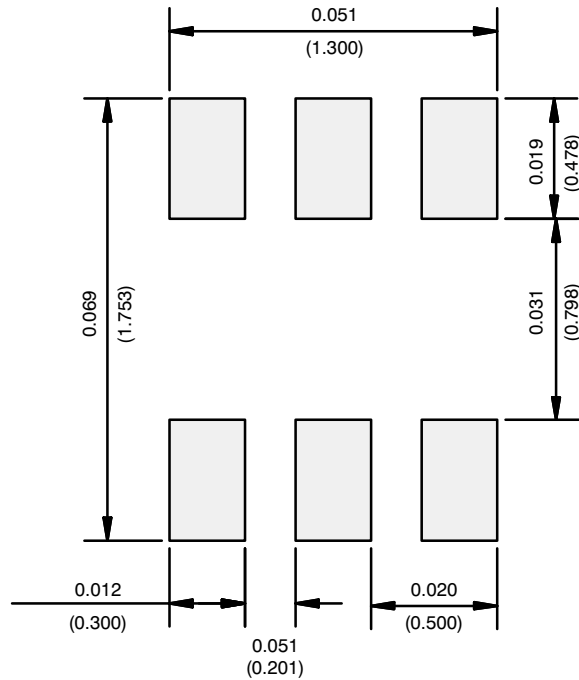
4 Terminal numbers are shown for reference only.

5 These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.56	0.58	0.60
A1	0	0.02	0.10
b	0.15	0.22	0.30
c	0.10	0.14	0.18
D	1.50	1.60	1.70
E	1.50	1.60	1.70
E1	1.15	1.20	1.25
e	0.45	0.50	0.55
e1	0.95	1.00	1.05
L	0.25	0.35	0.50
L1	0.10	0.20	0.30

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DWG: 5880

RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

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