

## Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.  
 For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	200	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage (Note 4)	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	140	V
Average Rectified Output Current @ T <sub>L</sub> = 140°C	I <sub>O</sub>	3.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	75	A

## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Total Capacitance (Note 5)	C <sub>T</sub>	45	pF
Typical Thermal Resistance, Junction to Lead (Note 6)	R <sub>θJL</sub>	11	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +175	°C

## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Forward Voltage @ I <sub>F</sub> = 3.0A, T <sub>J</sub> = 25°C @ I <sub>F</sub> = 3.0A, T <sub>J</sub> = 150°C	V <sub>FM</sub>	0.875 0.71	V
Peak Reverse Current @ T <sub>J</sub> = 25°C at Rated DC Blocking Voltage (Note 4) @ T <sub>J</sub> = 150°C	I <sub>RM</sub>	5.0 100	μA
Reverse Recovery Time (Note 7)	t <sub>rr</sub>	25	ns
Maximum Forward Recovery Time (Note 8)	t <sub>fr</sub>	25	ns

- Notes:
4. Short duration pulse test used to minimize self-heating effect.
  5. Measured at 1.0MHz and applied reverse voltage of 0V DC.
  6. Unit mounted on PC board with 5.0 mm<sup>2</sup> (0.013 mm thick) copper pads as heat sink.
  7. Measured with I<sub>F</sub> = 0.5A, I<sub>R</sub> = 1.0A, I<sub>rr</sub> = 0.25A. See Figure 5.
  8. Measured with I<sub>F</sub> = 1.0A, di/dt = 100A/μS, Recovery to 1.0V.

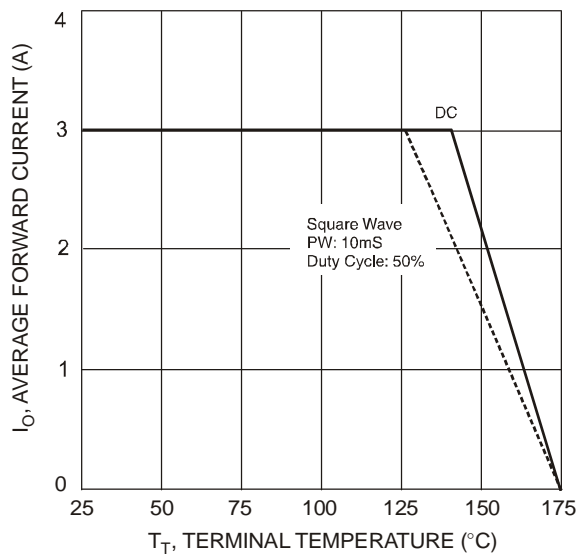


Fig. 1 Forward Current Derating Curve

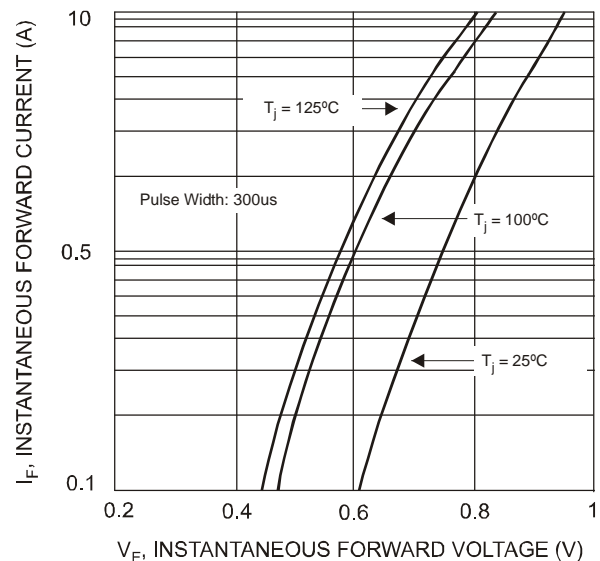


Fig. 2 Typical Forward Characteristics

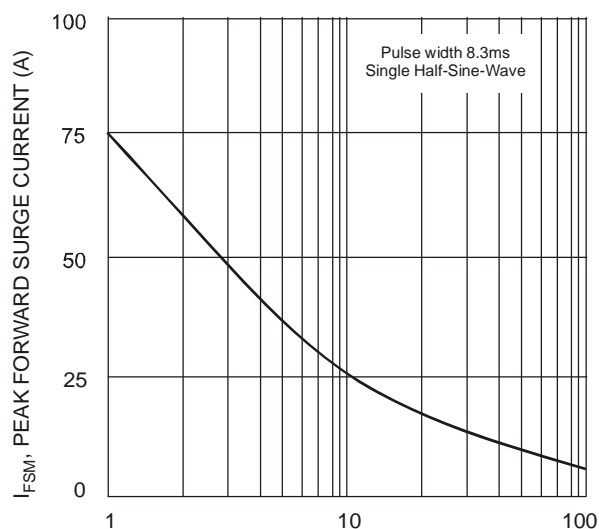


Fig. 3 Surge Current Derating Curve

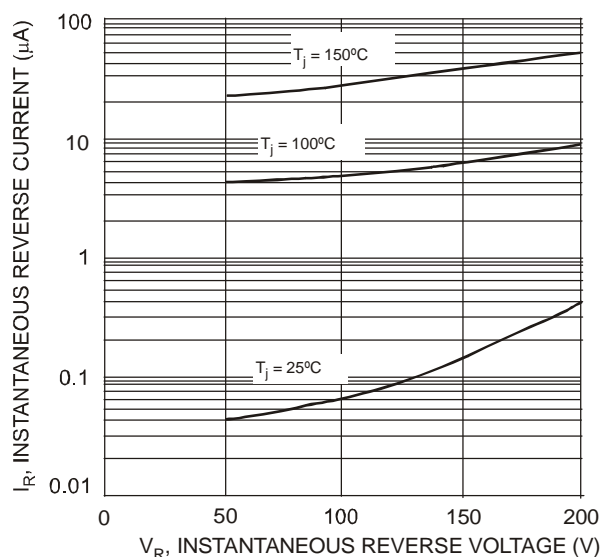
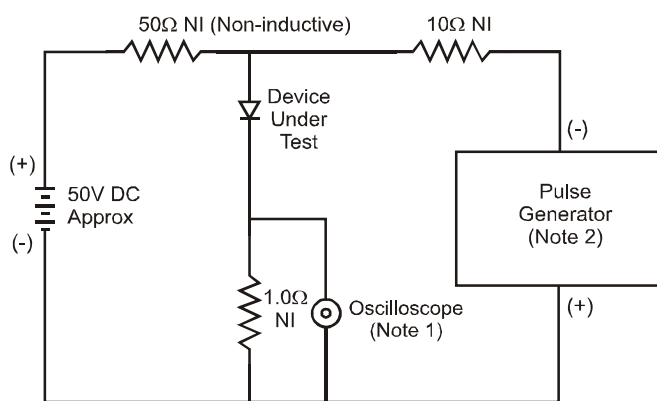
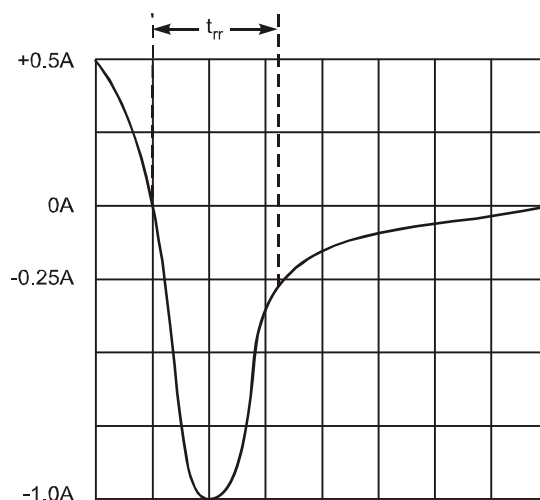


Fig. 4 Typical Reverse Characteristics



- Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
  2. Rise Time = 10ns max. Input Impedance = 50Ω.



Set time base for 50/100 ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

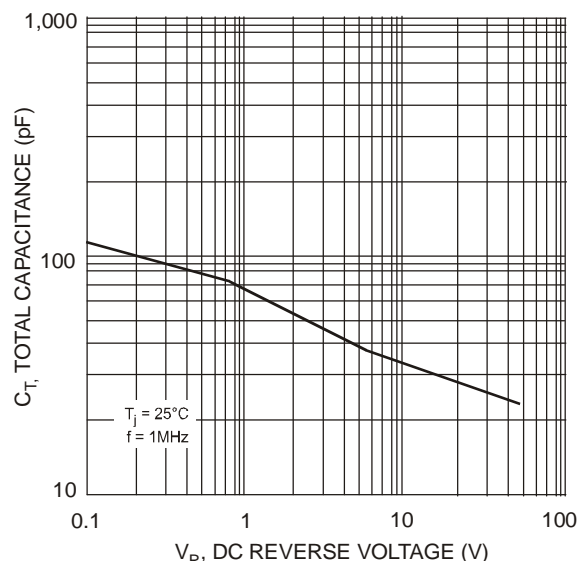
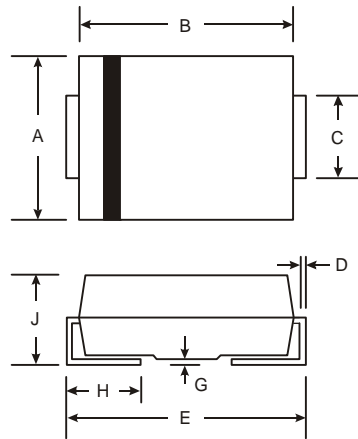


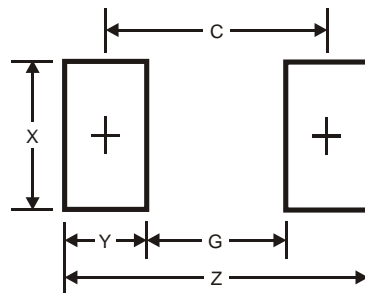
Fig. 6 Typical Total Capacitance

## Package Outline Dimensions



SMC		
Dim	Min	Max
A	5.59	6.22
B	6.60	7.11
C	2.75	3.18
D	0.15	0.31
E	7.75	8.13
G	0.10	0.20
H	0.76	1.52
J	2.00	2.50
All Dimensions in mm		

## Suggested Pad Layout



Dimensions	Value (in mm)
Z	9.3
G	4.4
X	3.3
Y	2.5
C	6.8

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