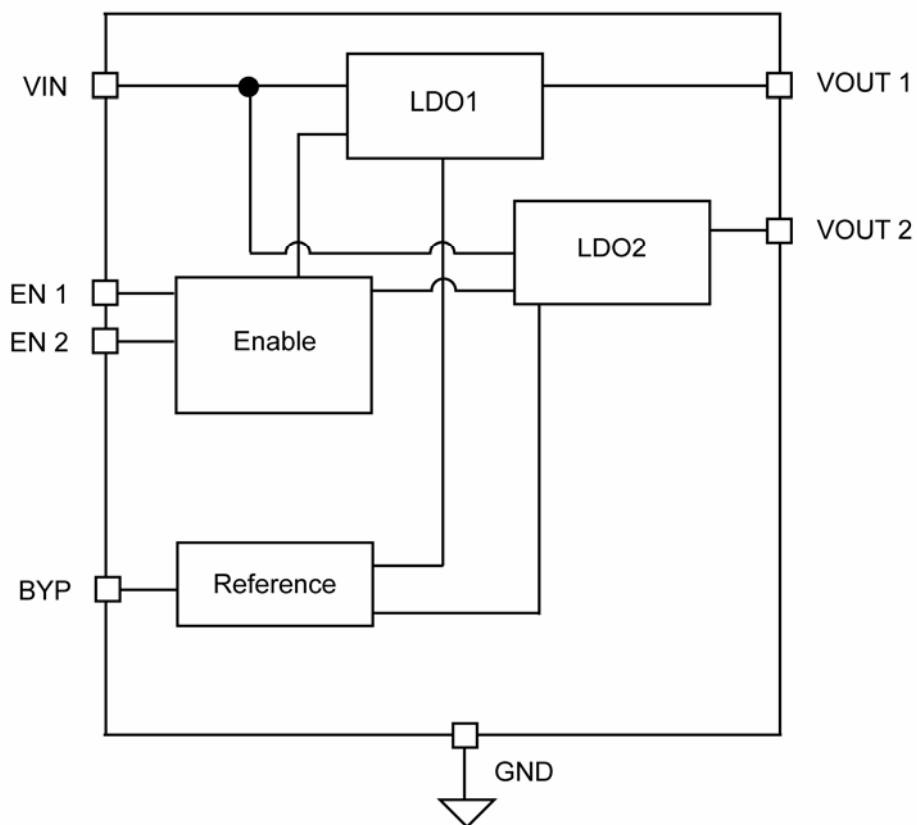


## Block Diagram



**MIC5330 Fixed Block Diagram**

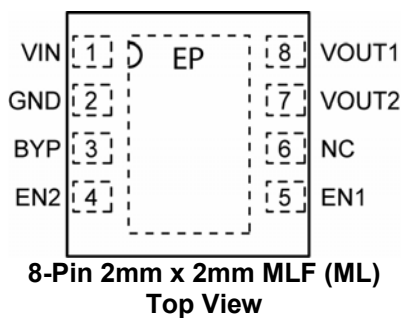
## Ordering Information

| Functional Part number | Ordering Part Number | Marking <sup>1</sup>    | V <sub>OUT1</sub> /V <sub>OUT2</sub> <sup>2</sup> | Junction Temperature Range | Package <sup>3</sup> |
|------------------------|----------------------|-------------------------|---|----------------------------|----------------------|
| MIC5330-1.8/1.5YML     | MIC5330-GFYML        | $\overline{\text{EGF}}$ | 1.8V/1.5V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-1.8/1.8YML     | MIC5330-GGYML        | $\overline{\text{EGG}}$ | 1.8V/1.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-1.8/1.6YML     | MIC5330-GWYML        | $\overline{\text{EGW}}$ | 1.8V/1.6V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.5/1.8YML     | MIC5330-JGYML        | $\overline{\text{EJG}}$ | 2.5V/1.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.5/2.5YML     | MIC5330-JJYML        | $\overline{\text{EJJ}}$ | 2.5V/2.5V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.6/1.85YML    | MIC5330-KDYML        | $\overline{\text{EKD}}$ | 2.6V/1.85   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.6/1.8YML     | MIC5330-KGYML        | $\overline{\text{EKG}}$ | 2.6V/1.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.7/2.7YML     | MIC5330-LLYML        | $\overline{\text{ELL}}$ | 2.7V/2.7V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.8/1.5YML     | MIC5330-MFYML        | $\overline{\text{EMF}}$ | 2.8V/1.5V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.8/1.8YML     | MIC5330-MGYML        | $\overline{\text{EMG}}$ | 2.8V/1.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.8/2.6YML     | MIC5330-MKYML        | $\overline{\text{EMK}}$ | 2.8V/2.6V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.8/2.8YML     | MIC5330-MMYML        | $\overline{\text{EMM}}$ | 2.8V/2.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.85/1.85YML   | MIC5330-NDYML        | $\overline{\text{END}}$ | 2.85V/1.85V                                       | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.85/2.6YML    | MIC5330-NKYML        | $\overline{\text{ENK}}$ | 2.85V/2.6V  | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.85/2.85YML   | MIC5330-NNYML        | $\overline{\text{ENN}}$ | 2.85V/2.85V                                       | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.9/1.5YML     | MIC5330-OFYML        | $\overline{\text{EOF}}$ | 2.9V/1.5V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.9/1.8YML     | MIC5330-OGYML        | $\overline{\text{EOG}}$ | 2.9V/1.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-2.9/2.9YML     | MIC5330-OOYML        | $\overline{\text{EOO}}$ | 2.9V/2.9V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.0/1.8YML     | MIC5330-PGYML        | $\overline{\text{EPG}}$ | 3.0V/1.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.0/2.5YML     | MIC5330-PJYML        | $\overline{\text{EPJ}}$ | 3.0V/2.5V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.0/2.6YML     | MIC5330-PKYML        | $\overline{\text{EPK}}$ | 3.0V/2.6V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.0/2.8YML     | MIC5330-PMYML        | $\overline{\text{EPM}}$ | 3.0V/2.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.0/2.85YML    | MIC5330-PNYML        | $\overline{\text{EPN}}$ | 3.0V/2.85V  | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.0/3.0YML     | MIC5330-PPYML        | $\overline{\text{EPP}}$ | 3.0V/3.0V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/1.5YML     | MIC5330-SFYML        | $\overline{\text{ESF}}$ | 3.3V/1.5V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/1.8YML     | MIC5330-SGYML        | $\overline{\text{ESG}}$ | 3.3V/1.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/2.5YML     | MIC5330-SJYML        | $\overline{\text{ESJ}}$ | 3.3V/2.5V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/2.6YML     | MIC5330-SKYML        | $\overline{\text{ESK}}$ | 3.3V/2.6V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/2.8YML     | MIC5330-SMYML        | $\overline{\text{ESM}}$ | 3.3V/2.8V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/2.85YML    | MIC5330-SNYML        | $\overline{\text{ESN}}$ | 3.3V/2.85V  | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/2.9YML     | MIC5330-SOYML        | $\overline{\text{ESO}}$ | 3.3V/2.9V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/3.0YML     | MIC5330-SPYML        | $\overline{\text{ESP}}$ | 3.3V/3.0V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/3.2YML     | MIC5330-SRYML        | $\overline{\text{ESR}}$ | 3.3V/3.2V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |
| MIC5330-3.3/3.3YML     | MIC5330-SSYML        | $\overline{\text{ESS}}$ | 3.3V/3.3V   | –40°C to +125°C            | 8-Pin 2x2 MLF®       |

### Notes:

1. Over bar (  $\overline{\phantom{x}}$  ) symbol may not be to scale.
2. Other voltage options available. Contact Micrel for more details.
3. MLF® is a GREEN RoHS compliant package. Lead finish is NiPdAu. Mold compound is Halogen Free.

## Pin Configuration



## Pin Description

| Pin Number<br>MLF-8 | Pin Name | Pin Function  |
|---------------------|----------|---|
| 1                   | VIN      | Supply Input.   |
| 2                   | GND      | Ground  |
| 3                   | BYP      | Reference Bypass: Connect external 0.1 $\mu$ F to GND to reduce output noise. May be left open when bypass capacitor is not required. |
| 4                   | EN2      | Enable Input (regulator 2). Active High Input. Logic High = On; Logic Low = Off; Do not leave floating.                               |
| 5                   | EN1      | Enable Input (regulator 1). Active High Input. Logic High = On; Logic Low = Off; Do not leave floating.                               |
| 6                   | NC       | Not internally connected  |
| 7                   | VOUT2    | Regulator Output – LDO2   |
| 8                   | VOUT1    | Regulator Output – LDO1   |
| –                   | EP       | Exposed Pad. Connect EP to GND.   |

**Absolute Maximum Ratings<sup>(1)</sup>**

Supply Voltage ( $V_{IN}$ ) ..... 0V to +6V  
 Enable Input Voltage ( $V_{EN}$ ) ..... 0V to +6V  
 Power Dissipation ..... Internally Limited<sup>(3)</sup>  
 Lead Temperature (soldering, 3sec) ..... 260°C  
 Storage Temperature ( $T_S$ ) ..... -65°C to +150°C  
 ESD Rating<sup>(4)</sup> ..... 2kV

**Operating Ratings<sup>(2)</sup>**

Supply voltage ( $V_{IN}$ ) ..... +2.3V to +5.5V  
 Enable Input Voltage ( $V_{EN}$ ) ..... 0V to  $V_{IN}$   
 Junction Temperature ..... -40°C to +125°C  
 Junction Thermal Resistance  
 MLF-8 ( $\theta_{JA}$ ) ..... 90°C/W

**Electrical Characteristics<sup>(5)</sup>**

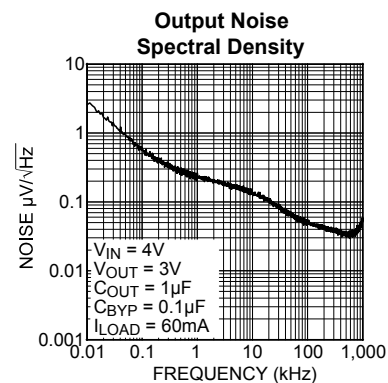
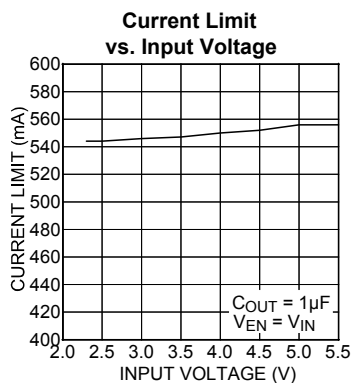
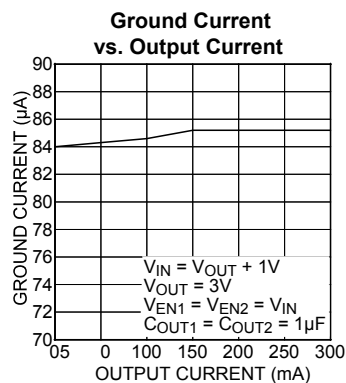
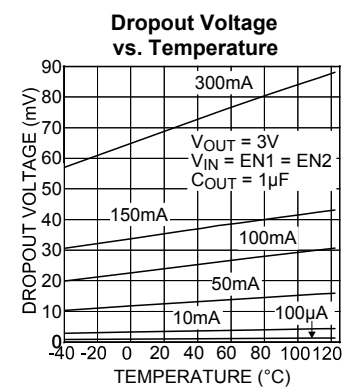
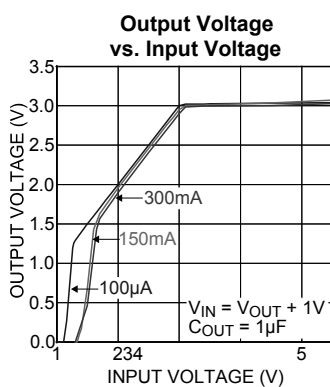
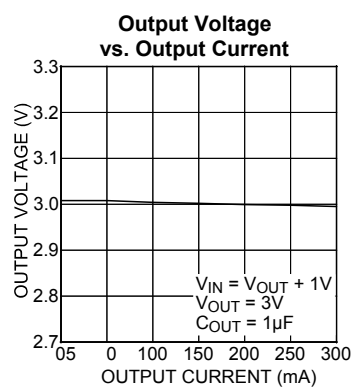
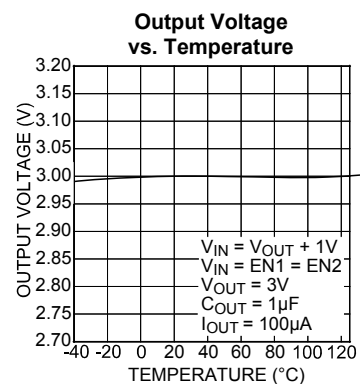
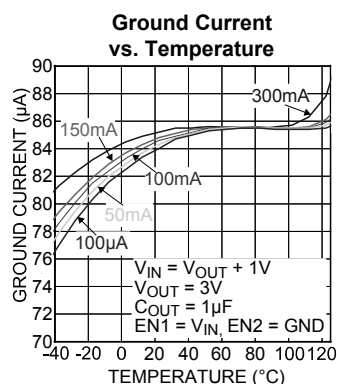
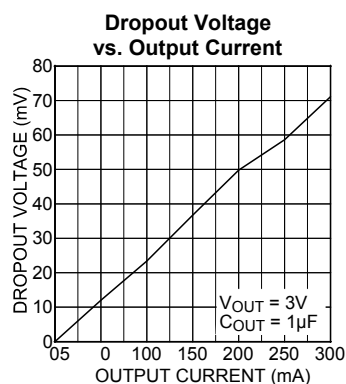
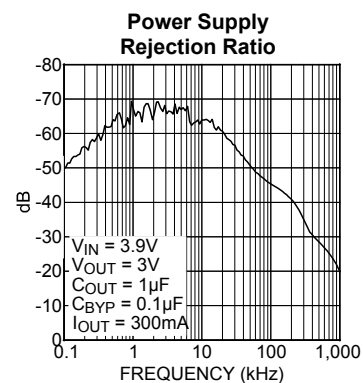
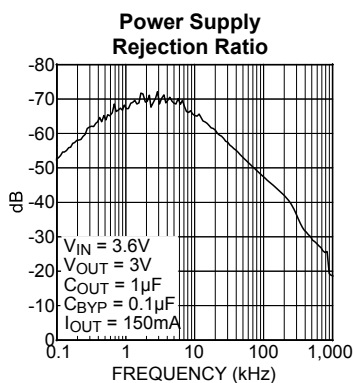
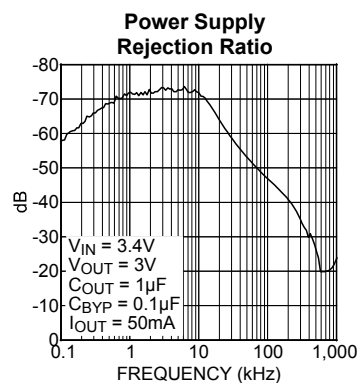
$V_{IN} = EN1 = EN2 = V_{OUT} + 1.0V$ ; higher of the two regulator outputs,  $I_{OUTLDO1} = I_{OUTLDO2} = 100\mu A$ ;  $C_{OUT1} = C_{OUT2} = 1\mu F$ ;  $C_{BYP} = 0.1\mu F$ ;  $T_J = 25^\circ C$ , **bold** values indicate  $-40^\circ C \leq T_J \leq +125^\circ C$ , unless noted.

| Parameter                                | Conditions   | Min         | Typ  | Max               | Units         |
|--|--|-------------|------|-------------------|---------------|
| Output Voltage Accuracy                  | Variation from nominal $V_{OUT}$                                   | -2.0        |      | +2.0              | %             |
|  | Variation from nominal $V_{OUT}$ ; $-40^\circ C$ to $+125^\circ C$ | <b>-3.0</b> |      | <b>+3.0</b>       | %             |
| Line Regulation                          | $V_{IN} = V_{OUT} + 1V$ to 5.5V; $I_{OUT} = 100\mu A$              |             | 0.02 | 0.3<br><b>0.6</b> | %/V<br>%/V    |
| Load Regulation                          | $I_{OUT} = 100\mu A$ to 300mA                                      |             | 0.5  |                   | %             |
| Dropout Voltage ( <b>Note 6</b> )        | $I_{OUT} = 100\mu A$   |             | 0.1  |                   | mV            |
|  | $I_{OUT} = 100mA$  |             | 25   | <b>75</b>         | mV            |
|  | $I_{OUT} = 150mA$  |             | 35   | <b>100</b>        | mV            |
|  | $I_{OUT} = 300mA$  |             | 75   | <b>200</b>        | mV            |
| Ground Current                           | EN1 = High; EN2 = Low; $I_{OUT} = 100\mu A$ to 300mA               |             | 85   | <b>120</b>        | $\mu A$       |
|  | EN1 = Low; EN2 = High; $I_{OUT} = 100\mu A$ to 300mA               |             | 85   | <b>120</b>        | $\mu A$       |
|  | EN1 = EN2 = High; $I_{OUT1} = 300mA$ , $I_{OUT2} = 300mA$          |             | 150  | <b>200</b>        | $\mu A$       |
| Ground Current in Shutdown               | EN1 = EN2 = 0V   |             | 0.01 | 2                 | $\mu A$       |
| Ripple Rejection                         | $f = 1kHz$ ; $C_{OUT} = 1.0\mu F$ ; $C_{BYP} = 0.1\mu F$           |             | 70   |                   | dB            |
|  | $f = 20kHz$ ; $C_{OUT} = 1.0\mu F$ ; $C_{BYP} = 0.1\mu F$          |             | 65   |                   | dB            |
| Current Limit                            | $V_{OUT} = 0V$   | <b>350</b>  | 550  | <b>950</b>        | mA            |
| Output Voltage Noise                     | $C_{OUT} = 1.0\mu F$ ; $C_{BYP} = 0.1\mu F$ ; 10Hz to 100kHz       |             | 30   |                   | $\mu V_{RMS}$ |
| <b>Enable Inputs (EN1 / EN2)</b>         |  |             |      |                   |               |
| Enable Input Voltage                     | Logic Low  |             |      | <b>0.2</b>        | V             |
|  | Logic High   | <b>1.1</b>  |      |                   | V             |
| Enable Input Current                     | $V_{IL} \leq 0.2V$   |             | 0.01 |                   | $\mu A$       |
|  | $V_{IH} \geq 1.0V$   |             | 0.01 |                   | $\mu A$       |
| <b>Turn on Time (See Timing Diagram)</b> |  |             |      |                   |               |
| Turn on Time (LDO1 and 2)                | $C_{OUT} = 1.0\mu F$ ; $C_{BYP} = 0.01\mu F$                       |             | 30   | <b>100</b>        | $\mu s$       |

**Notes:**

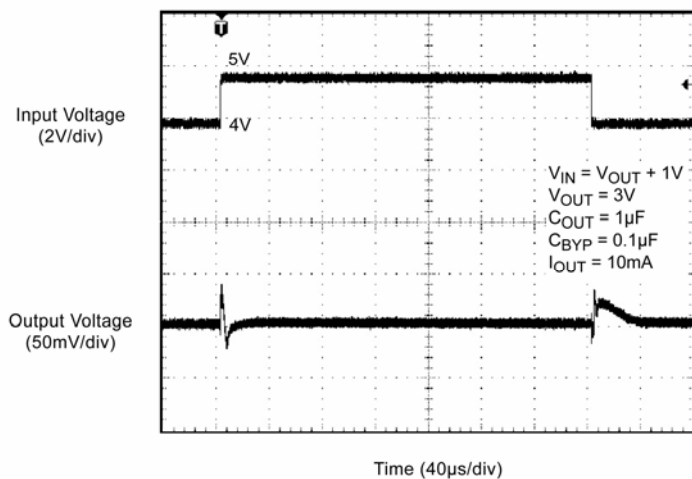
- Exceeding the absolute maximum rating may damage the device.
- The device is not guaranteed to function outside its operating rating.
- The maximum allowable power dissipation of any  $T_A$  (ambient temperature) is  $P_{D(max)} = (T_{J(max)} - T_A) / \theta_{JA}$ . Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
- Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5k in series with 100pF.
- Specification for packaged product only.
- Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal  $V_{OUT}$ . For outputs below 2.3V, the dropout voltage is the input to output differential with the minimum input voltage 2.3V.

## Typical Characteristics

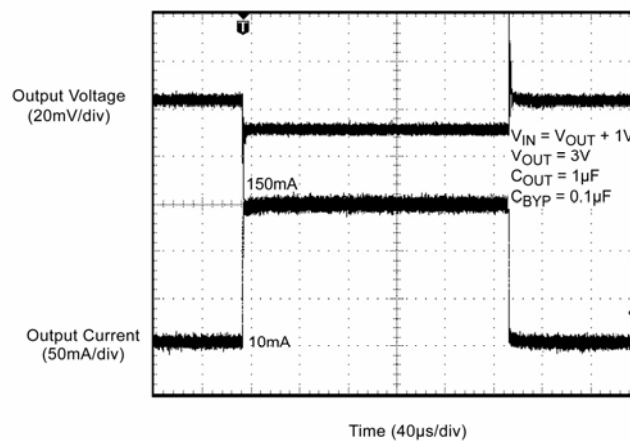


## Functional Characteristics

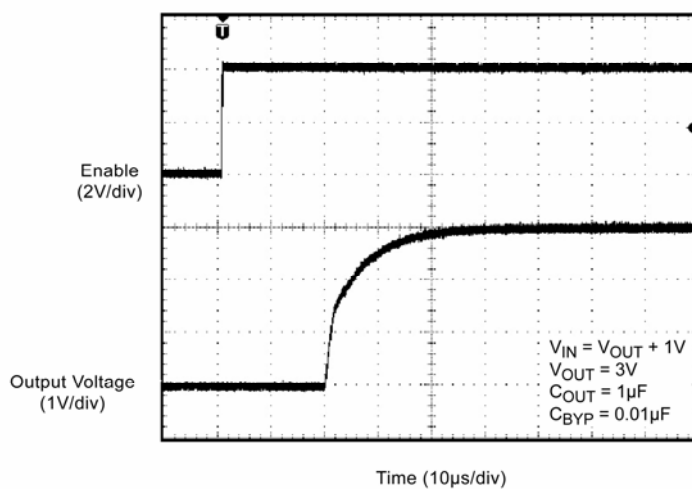
### Line Transient



### Load Transient



### Enable Turn On



## Applications Information

### Enable/Shutdown

The MIC5330 comes with dual active high enable pins that allow each regulator to be enabled independently. Forcing the enable pin low disables the regulator and sends it into a “zero” off mode current state. In this state, current consumed by the regulator goes nearly to zero. Forcing the enable pin high enables the output voltage. The active high enable pin uses CMOS technology and the enable pin cannot be left floating; a floating enable pin may cause an indeterminate state on the output.

### Input Capacitor

The MIC5330 is a high performance, high bandwidth device. Therefore, it requires a well bypassed input supply for optimal performance. A 1 $\mu$ F capacitor is required from the input to ground to provide stability. Low ESR ceramic capacitors provide optimal performance at a minimum of space. Additional high frequency capacitors, such as small valued NPO dielectric type capacitors, help filter out high frequency noise and are good practice in any RF based circuit.

### Output Capacitor

The MIC5330 requires an output capacitor of 1 $\mu$ F or greater to maintain stability. The design is optimized for use with low ESR ceramic chip capacitors. High ESR capacitors may cause high frequency oscillation. The output capacitor can be increased, but performance has been optimized for a 1 $\mu$ F ceramic output capacitor and does not improve significantly with larger capacitance.

X7R/X5R dielectric type ceramic capacitors are recommended because of their temperature performance. X7R type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60%, respectively, over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric, the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

### Bypass Capacitor

A capacitor can be placed from the noise bypass pin to ground to reduce output voltage noise. The capacitor bypasses the internal reference. A 0.1 $\mu$ F capacitor is recommended for applications that require low noise outputs. The bypass capacitor can be increased, further reducing noise and improving PSRR. Turn on time increases slightly with respect to

bypass capacitance. A unique, quick start circuit allows the MIC5330 to drive a large capacitor on the bypass pin without significantly slowing turn on time.

### No-Load Stability

Unlike many other voltage regulators, the MIC5330 will remain stable and in regulation with no load. This is especially important in CMOS RAM keep alive applications.

### Thermal Considerations

The MIC5330 is designed to provide 300mA of continuous current for both outputs in a very small package. Maximum ambient operating temperature can be calculated based on the output current and the voltage drop across the part. Given that the input voltage is 3.3V, the output voltage is 2.8V for  $V_{OUT1}$ , 2.5V for  $V_{OUT2}$  and the output current = 300mA. The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT1}) I_{OUT1} + (V_{IN} - V_{OUT2}) I_{OUT2} + V_{IN} I_{GND}$$

Because this device is CMOS and the ground current is typically <100 $\mu$ A over the load range, the power dissipation contributed by the ground current is < 1% and can be ignored for this calculation.

$$P_D = (3.3V - 2.8V) \times 300mA + (3.3V - 1.5) \times 300mA$$

$$P_D = 0.69W$$

To determine the maximum ambient operating temperature of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(MAX)} = \left( \frac{T_{J(MAX)} - T_A}{\theta_{JA}} \right)$$

$T_{J(max)} = 125^\circ\text{C}$ , the maximum junction temperature of the die  $\theta_{JA}$  thermal resistance =  $90^\circ\text{C/W}$ .

The table below shows junction-to-ambient thermal resistance for the MIC5330 in the MLF package.

| Package        | $\theta_{JA}$ Recommended Minimum Footprint |
|----------------|---|
| 8-Pin 2x2 MLF® | $90^\circ\text{C/W}$                        |

Thermal Resistance

Substituting  $P_D$  for  $P_{D(max)}$  and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit. The junction-to-ambient thermal resistance for the minimum footprint is  $90^{\circ}\text{C/W}$ .

The maximum power dissipation must not be exceeded for proper operation.

For example, when operating the MIC5330-MFYML at an input voltage of 3.3V and 300mA loads at each output with a minimum footprint layout, the maximum ambient operating temperature  $T_A$  can be determined as follows:

$$0.99\text{W} = (125^{\circ}\text{C} - T_A)/(90^{\circ}\text{C/W})$$

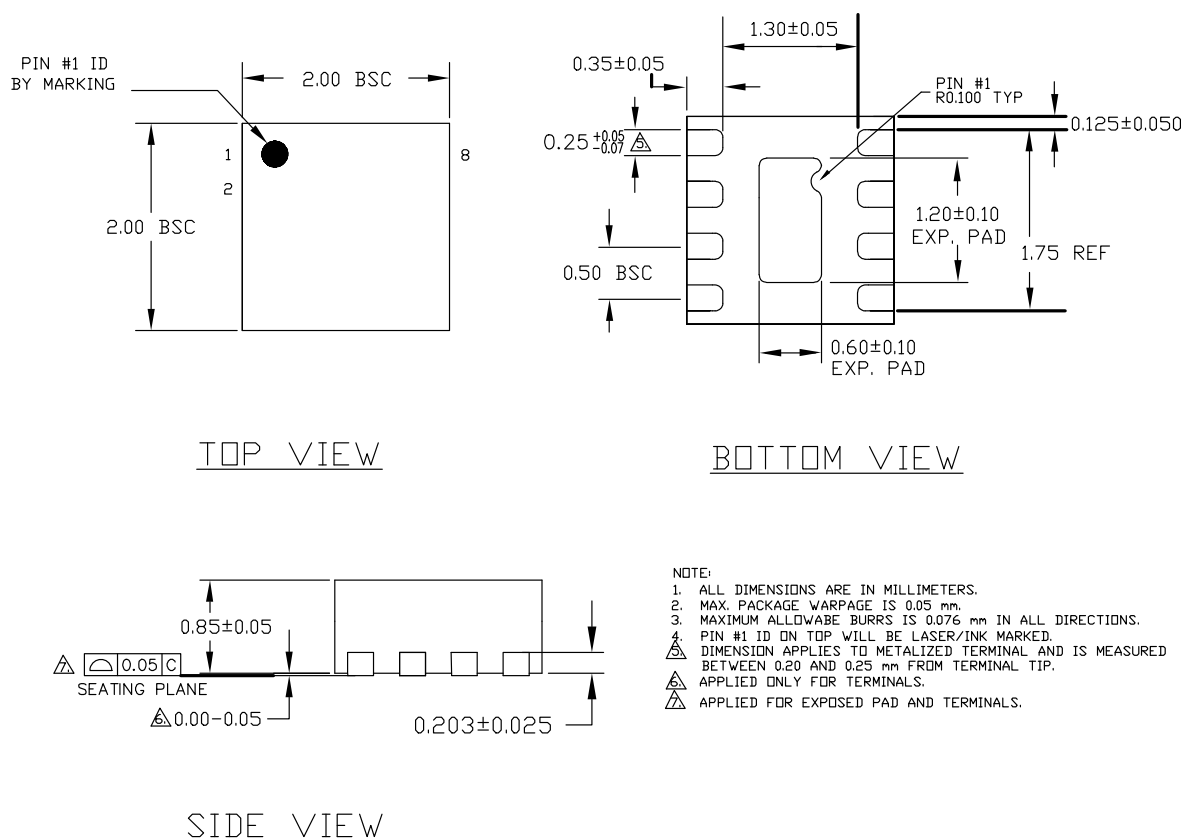
$$T_A = 62.9^{\circ}\text{C}$$

Therefore, a 2.8V/1.5V application with 300mA at each output current can accept an ambient operating temperature of  $62.9^{\circ}\text{C}$  in a 2mm x 2mm MLF<sup>®</sup> package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to the "Regulator Thermals" section of *Micrel's Designing with Low-Dropout Voltage Regulators* handbook. This information can be found on Micrel's website at:

[http://www.micrel.com/\\_PDF/other/LDOBk\\_ds.pdf](http://www.micrel.com/_PDF/other/LDOBk_ds.pdf)



## Package Information



**8-Pin 2mm x 2mm MLF (ML)**

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