

## Ordering Information

| Part Number | Package     | Packing   |
|-------------|-------------|-----------|
| HV857K7-G   | 8-Lead DFN  | 3000/Reel |
| HV857MG-G   | 8-Lead MSOP | 2500/Reel |

-G denotes a lead (Pb)-free / RoHS compliant package

## Absolute Maximum Ratings

| Parameter                | Value           |
|--------------------------|-----------------|
| Supply voltage, $V_{DD}$ | -0.5V to 6.5V   |
| Operating temperature    | -40°C to +85°C  |
| Storage temperature      | -65°C to +150°C |
| DFN-8 power dissipation  | 1.6W            |
| MSOP-8 power dissipation | 300mW           |
| Output voltage, $V_{CS}$ | -0.5 to +120V   |

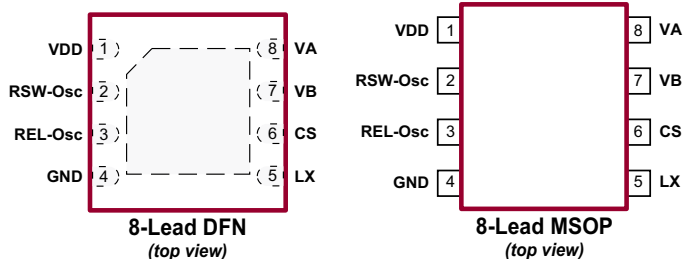
Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied. Continuous operation of the device at the absolute rating level may affect device reliability. All voltages are referenced to device ground.

## Typical Thermal Resistance

| Package     | $\theta_{ja}$ |
|-------------|---------------|
| 8-Lead DFN  | 37°C/W        |
| 8-Lead MSOP | 171°C/W       |

Mounted on FR4 board, 25mm x 25mm x 1.57mm

## Pin Configuration



Pads are at the bottom of the package.  
Exposed center pad is at ground potential.

## Product Marking



Y = Last Digit of Year Sealed  
W = Code for Week Sealed  
L = Lot Number  
— = "Green" Packaging

Package may or may not include the following marks: Si or

### 8-Lead DFN

Top Marking



Bottom Marking



L = Lot Number  
YY = Year Sealed  
WW = Week Sealed  
— = "Green" Packaging

Package may or may not include the following marks: Si or

### 8-Lead MSOP

## Recommended Operating Conditions

| Sym      | Parameter                 | Min | Typ | Max | Units | Conditions |
|----------|---------------------------|-----|-----|-----|-------|------------|
| $V_{DD}$ | Supply voltage            | 1.8 | -   | 5.0 | V     | ---        |
| $f_{EL}$ | Operating drive frequency | -   | -   | 1.0 | kHz   | ---        |
| $T_A$    | Operating temperature     | -40 | -   | +85 | °C    | ---        |

## Enable/Disable Function Table

| Sym  | Parameter                | Min            | Typ | Max      | Units | Conditions             |
|------|--------------------------|----------------|-----|----------|-------|------------------------|
| EN-L | Logic input low voltage  | 0              | -   | 0.2      | V     | $V_{DD} = 1.8$ to 5.0V |
| EN-H | Logic input high voltage | $V_{DD} - 0.2$ | -   | $V_{DD}$ | V     | $V_{DD} = 1.8$ to 5.0V |

## DC Electrical Characteristics (Over recommended operating conditions unless otherwise specified, $T_A = 25^\circ\text{C}$ )

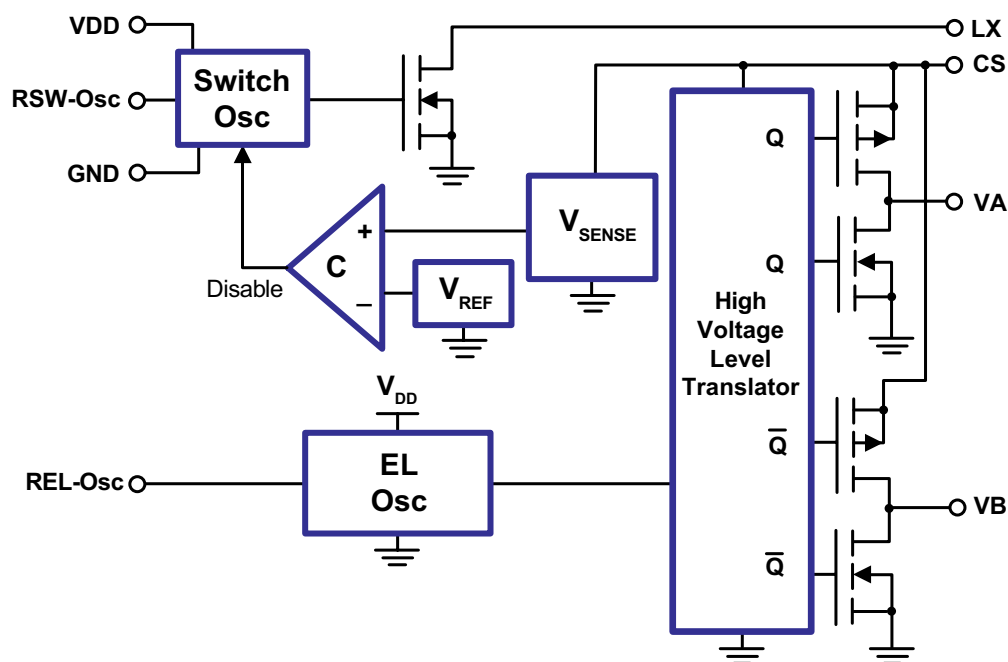
| Sym          | Parameter                             | Min | Typ | Max | Units    | Conditions             |
|--------------|---------------------------------------|-----|-----|-----|----------|------------------------|
| $R_{DS(ON)}$ | On-resistance of switching transistor | -   | -   | 6.0 | $\Omega$ | $I = 100\text{mA}$     |
| $V_{CS}$     | Max. output regulation voltage        | 85  | 95  | 105 | V        | $V_{DD} = 1.8$ to 5.0V |

## DC Electrical Characteristics *(cont.)*

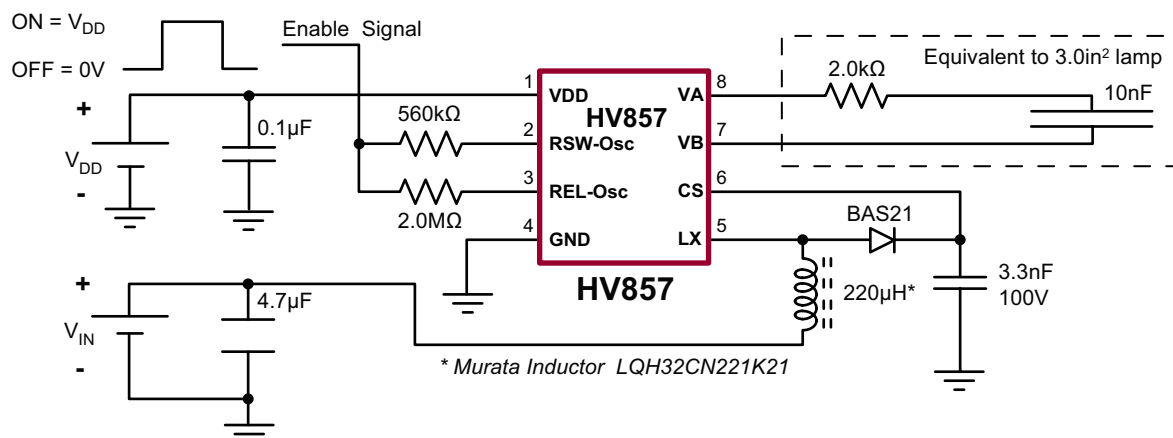
| Sym         | Parameter                                | Min | Typ | Max | Units   | Conditions                              |
|-------------|--|-----|-----|-----|---------|---|
| $V_A - V_B$ | Peak to Peak output voltage              | 170 | 190 | 210 | V       | $V_{DD}=1.8$ to $5.0V$                  |
| $I_{DDQ}$   | Quiescent $V_{DD}$ supply current        | -   | -   | 150 | nA      | $R_{SW-Osc}$ = Low                      |
| $I_{DD}$    | Input current going into the VDD pin     | -   | -   | 150 | $\mu A$ | $V_{DD} = 1.8$ to $5.0V$ . See Figure 1 |
| $I_{IN}$    | Input current including inductor current | -   | 20  | 25  | mA      | See Figure 1*                           |
| $V_{CS}$    | Output voltage on VCS                    | -   | 84  | -   | V       | See Figure 1                            |
| $f_{EL}$    | EL lamp frequency                        | 205 | 240 | 275 | Hz      | See Figure 1                            |
| $f_{SW}$    | Switching transistor frequency           | -   | 80  | -   | kHz     | See Figure 1                            |
| D           | Switching transistor duty cycle          | -   | 88  | -   | %       | See Figure 1                            |

\* The inductor used is a 220 $\mu$ H Murata inductor, max DC resistance of 8.4 $\Omega$ , part # LQH32CN221K21.

## Block Diagram



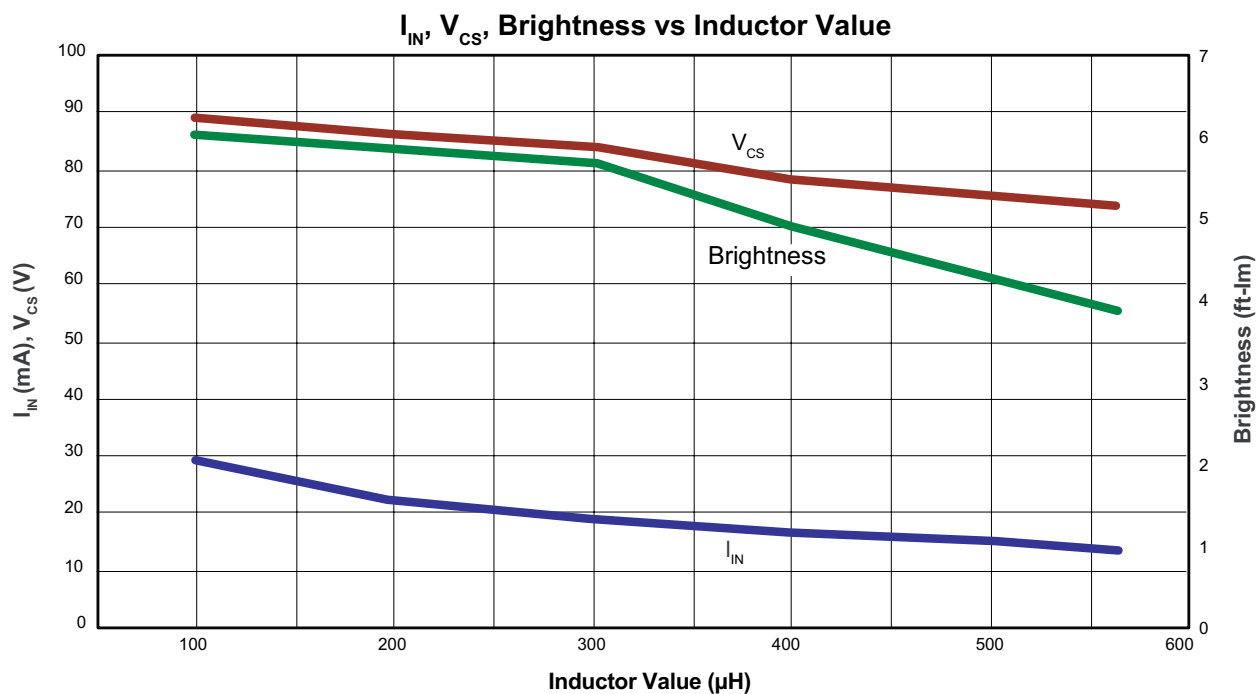
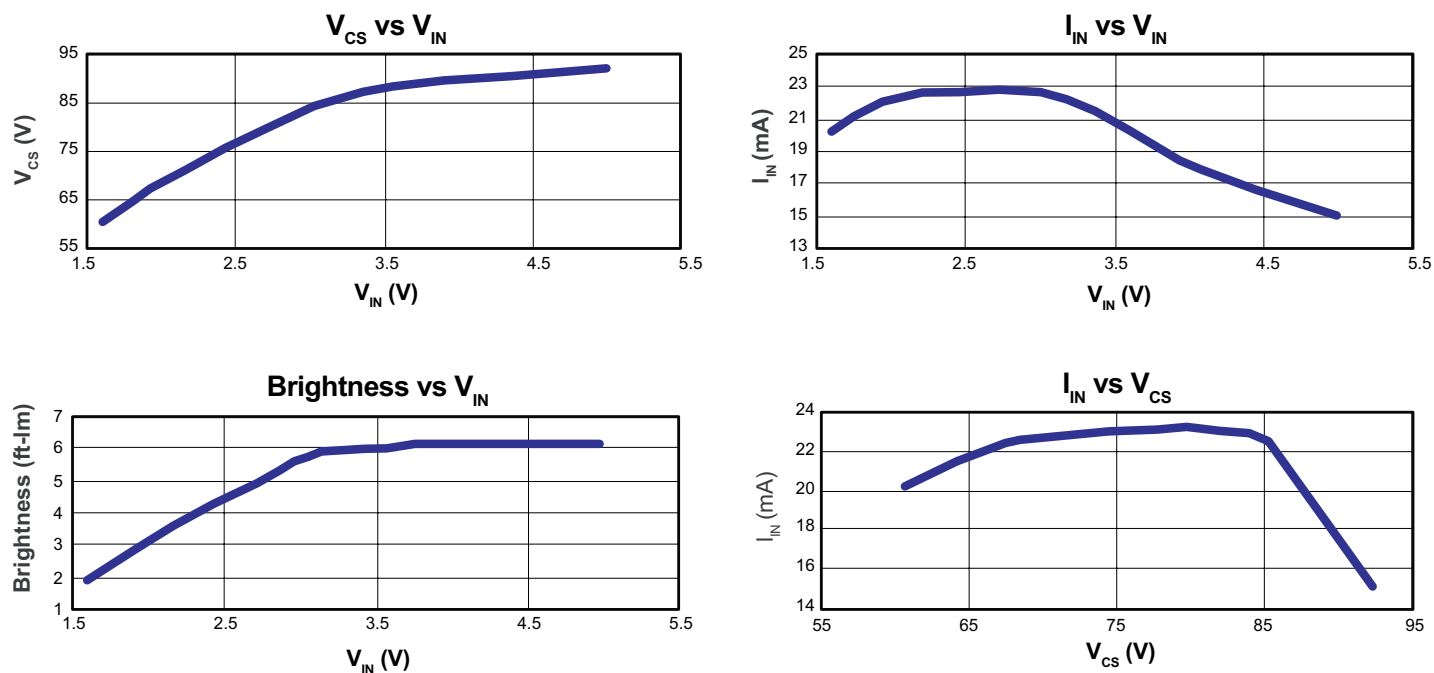
### Figure 1: Typical Application/ Test Circuit



## Typical Performance

| Device    | Lamp Size          | $V_{DD} = V_{IN}$ | $I_{IN}$ | $V_{CS}$ | $f_{EL}$ | Brightness |
|-----------|--------------------|-------------------|----------|----------|----------|------------|
| HV857MG-G | 3.0in <sup>2</sup> | 3.3V              | 20.0mA   | 84V      | 240Hz    | 6.0ft-lm   |

## Typical Performance Curves for Figure 1 ( $EL\ Lamp = 3.0in^2$ , $V_{DD} = 3.0V$ )



## External Component Description

| External Component       | Description  |
|--------------------------|--|
| Diode                    | Fast reverse recovery diode, BAS21 diode or equivalent.  |
| C <sub>s</sub> Capacitor | 0.003μF to 0.1μF, 100V capacitor to GND is used to store the energy transferred from the inductor.   |
| R <sub>EL</sub> Resistor | <p>The EL lamp frequency is controlled via an external R<sub>EL</sub> resistor connected between REL-Osc and VDD of the device. The lamp frequency increases as R<sub>EL</sub> decreases. As the EL lamp frequency increases, the amount of current drawn from the battery will increase and the output voltage V<sub>CS</sub> will decrease. The color of the EL lamp is dependent upon its frequency.</p> <p>A 2MΩ resistor would provide lamp frequency of 205 to 275Hz. Decreasing the R<sub>EL</sub> resistor by a factor of 2 will increase the lamp frequency by a factor of 2.</p> $f_{EL} = \frac{(2M\Omega)(240Hz)}{R_{EL}}$   |
| R <sub>SW</sub> Resistor | <p>The switching frequency of the converter is controlled via an external resistor, RSW between RSW-Osc and VDD of the device. The switching frequency increases as R<sub>SW</sub> decreases. With a given inductor, as the switching frequency increases, the amount of current drawn from the battery will decrease and the output voltage, V<sub>CS</sub>, will also decrease.</p> $f_{SW} = \frac{(560k\Omega)(80Hz)}{R_{SW}}$   |
| L <sub>x</sub> Inductor  | <p>The inductor L<sub>x</sub> is used to boost the low input voltage by inductive flyback. When the internal switch is on, the inductor is being charged. When the internal switch is off, the charge stored in the inductor will be transferred to the high voltage capacitor C<sub>s</sub>. The energy stored in the capacitor is connected to the internal H-bridge, and therefore to the EL lamp. In general, smaller value inductors, which can handle more current, are more suitable to drive larger size lamps. As the inductor value decreases, the switching frequency of the inductor (controlled by R<sub>SW</sub>) should be increased to avoid saturation.</p> <p>A 220μH Murata (LQH32CN221) inductor with 8.4Ω series DC resistance is typically recommended. For inductors with the same inductance value, but with lower series DC resistance, lower R<sub>SW</sub> resistor value is needed to prevent high current draw and inductor saturation.</p> |
| Lamp                     | As the EL lamp size increases, more current will be drawn from the battery to maintain high voltage across the EL lamp. The input power, (V <sub>IN</sub> x I <sub>IN</sub> ), will also increase. If the input power is greater than the power dissipation of the package, an external resistor in series with one side of the lamp is recommended to help reduce the package power dissipation.  |

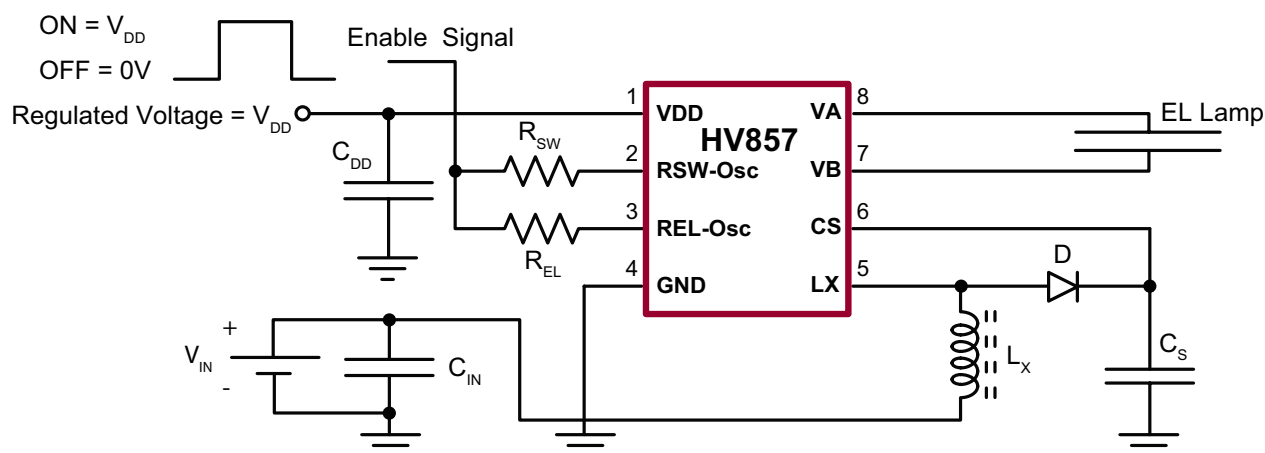
## Split Supply Configuration

The HV857 can also be used for handheld devices operating from a battery where a regulated voltage is available. This is shown in Figure 2. The regulated voltage can be used to run the internal logic of the HV857. The amount of current necessary to run the internal logic is 150 $\mu$ A Max at a  $V_{DD}$  of 3.0V. Therefore, the regulated voltage could easily provide the current without being loaded down.

## Enable/Disable Configuration

The HV857 can be easily enabled and disabled via a logic control signal on the  $R_{SW}$  and  $R_{EL}$  resistors as shown in Figure 2 below. The control signal can be from a microprocessor. The control signal has to track the  $V_{DD}$  supply.  $R_{SW}$  and  $R_{EL}$  are typically very high values. Therefore, only 10's of microamperes will be drawn from the logic signal when it is at a logic high (enable) state. When the microprocessor signal is high the device is enabled, and when the signal is low, it is disabled.

**Figure 2: Split Supply and Enable/Disable Configuration**

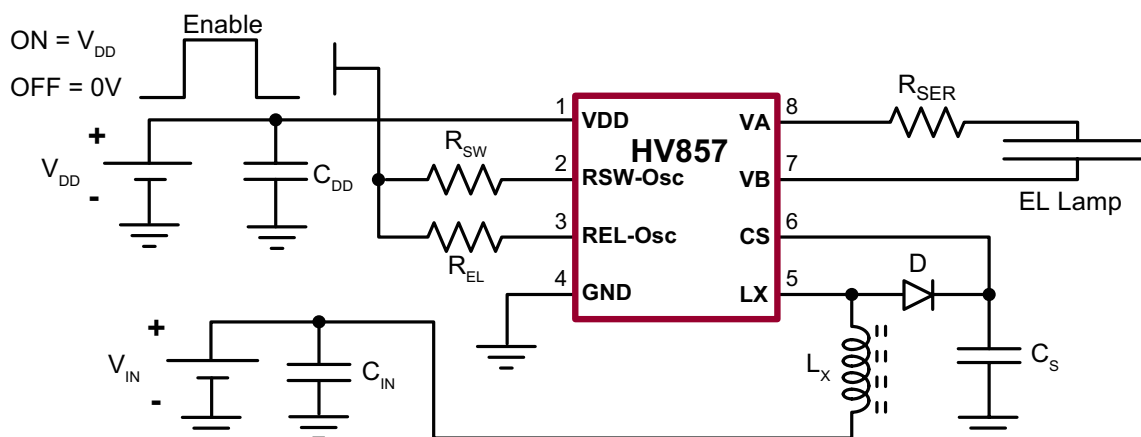


## Audible Noise Reduction

This section describes a method (patented) developed at Supertex to reduce the audible noise emitted by the EL lamps used in application sensitive to audible noise. Figure

3 shows a general circuit schematic that uses the resistor,  $R_{SER}$ , connected in series with the EL lamp.

**Figure 3: Typical Application Circuit for Audible Noise Reduction**



## Minimization of EL Lamp Audible Noise

The EL lamp, when lit, emits an audible noise. This is due to EL lamp construction and it creates a major problem for applications where the EL lamp can be close to the ear such as cellular phones. The noisiest waveform is a square wave and the quietest waveform has been assumed to be a sine wave.

After extensive research, Supertex has developed a waveform that is quieter than a sine wave. The waveform takes the shape of approximately  $2RC$  time constants for rising and  $2RC$  time constants for falling, where  $C$  is the capacitance of the EL lamp, and  $R$  is the external resistor,  $R_{SER}$ , connected in series with the EL lamp. This waveform has been proven to generate less noise than a sine wave.

The audible noise from the EL lamp can be set at a desired level based on the series resistor value used with the lamp. It is important to note that use of this resistor will reduce the voltage across the lamp. Reduction of voltage across the lamp will also have another effect on the overall performance of the Supertex EL drivers, age compensation (patented). This addresses a very important issue, EL lamp life that most mobile phone manufacturers are concerned about.

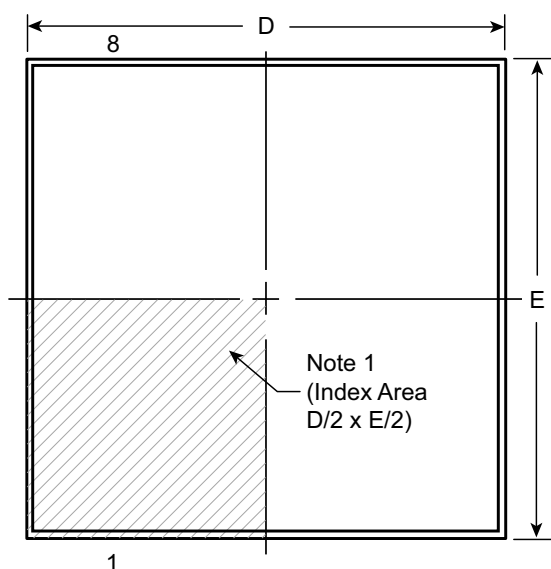
## Effect of Series Resistor on EL Lamp Audible Noise and Brightness

As EL lamp ages, its brightness is reduced and its capacitance is diminished. By using the RC model to reduce the audible noise emitted by the EL lamp, the voltage across the lamp will increase as its capacitance diminishes. Hence the increase in voltage will compensate for the reduction of the brightness. As a result, it will extend the EL lamp's half-life (half the original brightness).

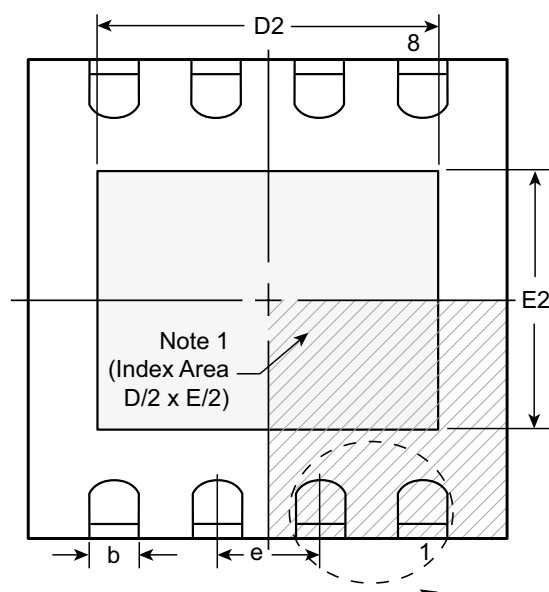
Increasing the value of the series resistor with the lamp will reduce the EL lamp audible noise as well as its brightness. This is due to the fact that the output voltage across the lamp will be reduced and the output waveform will have rounder edges.

# 8-Lead DFN Package Outline (K7)

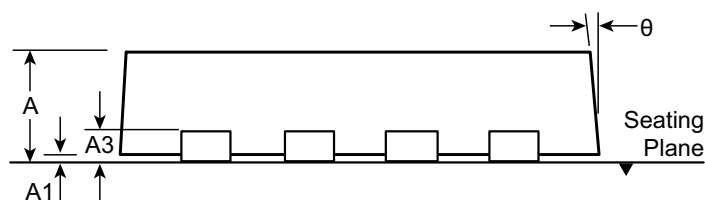
3.00x3.00mm body, 0.80mm height (max), 0.65mm pitch



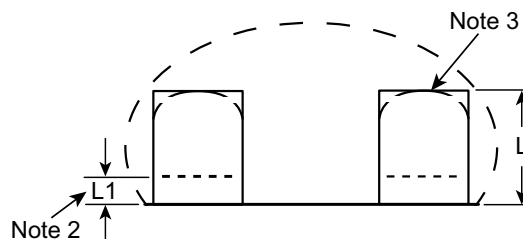
**Top View**



**Bottom View**



**Side View**



**View B**

## Notes:

1. A Pin 1 identifier must be located in the index area indicated. The Pin 1 identifier can be: a molded mark/identifier; an embedded metal marker; or a printed indicator.
2. Depending on the method of manufacturing, a maximum of 0.15mm pullback (L1) may be present.
3. The inner tip of the lead may be either rounded or square.

| Symbol            |     | A    | A1   | A3          | b    | D     | D2   | E     | E2   | e           | L    | L1    | θ   |
|-------------------|-----|------|------|-------------|------|-------|------|-------|------|-------------|------|-------|-----|
| Dimension<br>(mm) | MIN | 0.70 | 0.00 | 0.20<br>REF | 0.25 | 2.85* | 1.60 | 2.85* | 1.35 | 0.65<br>BSC | 0.30 | 0.00* | 0°  |
|                   | NOM | 0.75 | 0.02 |             | 0.30 | 3.00  | -    | 3.00  | -    |             | 0.40 | -     | -   |
|                   | MAX | 0.80 | 0.05 |             | 0.35 | 3.15* | 2.50 | 3.15* | 1.75 |             | 0.50 | 0.15  | 14° |

JEDEC Registration MO-229, Variation WEEC-2, Issue C, Aug. 2003.

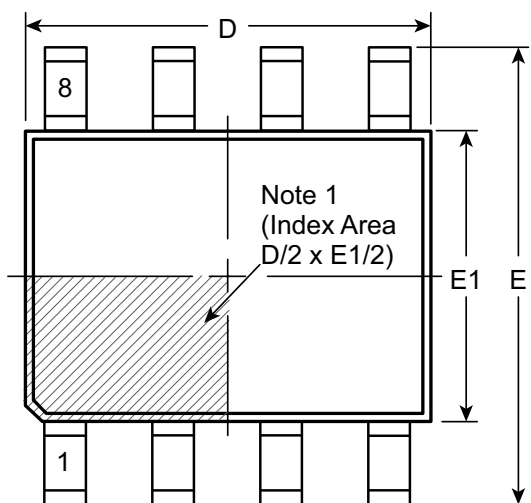
\* This dimension is not specified in the JEDEC drawing.

Drawings not to scale.

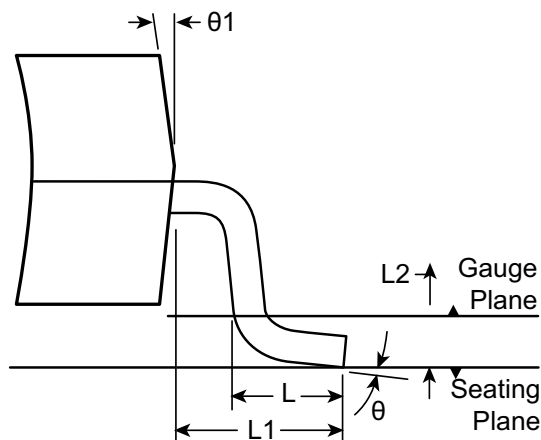
Supertex Doc. #: DSPD-8DFNK73X3P065, Version C081109.

# 8-Lead MSOP Package Outline (MG)

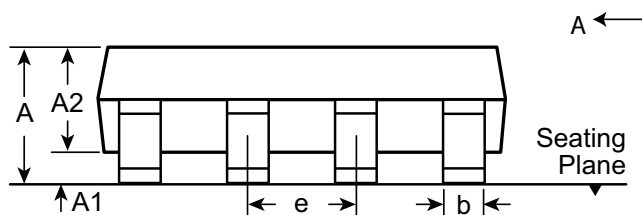
3.00x3.00mm body, 1.10mm height (max), 0.65mm pitch



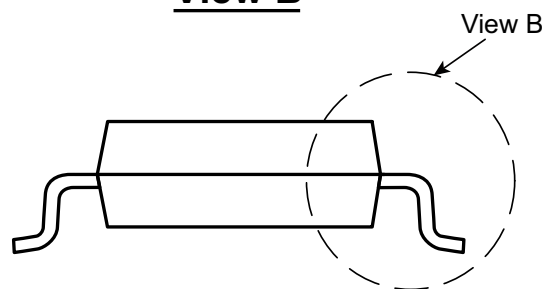
**Top View**



**View B**



**Side View**



**View A-A**

## Note:

1. A Pin 1 identifier must be located in the index area indicated. The Pin 1 identifier can be: a molded mark/identifier; an embedded metal marker; or a printed indicator.

| Symbol         |     | A     | A1   | A2   | b    | D     | E     | E1    | e        | L    | L1       | L2       | θ  | θ1  |
|----------------|-----|-------|------|------|------|-------|-------|-------|----------|------|----------|----------|----|-----|
| Dimension (mm) | MIN | 0.75* | 0.00 | 0.75 | 0.22 | 2.80* | 4.65* | 2.80* | 0.65 BSC | 0.40 | 0.95 REF | 0.25 BSC | 0° | 5°  |
|                | NOM | -     | -    | 0.85 | -    | 3.00  | 4.90  | 3.00  |          | 0.60 |          |          | -  | -   |
|                | MAX | 1.10  | 0.15 | 0.95 | 0.38 | 3.20* | 5.15* | 3.20* |          | 0.80 |          |          | 8° | 15° |

JEDEC Registration MO-187, Variation AA, Issue E, Dec. 2004.

\* This dimension is not specified in the JEDEC drawing.

**Drawings are not to scale.**

**Supertex Doc. #: DSPD-8MSOPMG, Version H041309.**

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to <http://www.supertex.com/packaging.html>.)

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