HMC929* PRODUCT PAGE QUICK LINKS

Last Content Update: 12/18/2017

COMPARABLE PARTS

View a parametric search of comparable parts.

EVALUATION KITS

- HMC929LP4E Evaluation Board

DOCUMENTATION

Data Sheet

- HMC929 Data Sheet

REFERENCE MATERIALS

Quality Documentation

- Package/Assembly Qualification Test Report: LP4, LP4B, LP4C, LP4K (QTR: 2013-00487 REV: 04)
- Package/Assembly Qualification Test Report: Plastic Encapsulated QFN (QTR: 05006 REV: 02)
- Semiconductor Qualification Test Report: PHEMT-H (QTR: 2013-00260)

DESIGN RESOURCES

- HMC929 Material Declaration
- PCN-PDN Information
- Quality And Reliability
- Symbols and Footprints

DISCUSSIONS

View all HMC929 EngineerZone Discussions.

SAMPLE AND BUY

Visit the product page to see pricing options.

TECHNICAL SUPPORT

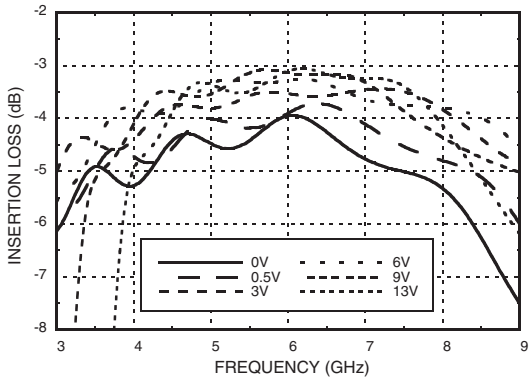
Submit a technical question or find your regional support number.

DOCUMENT FEEDBACK

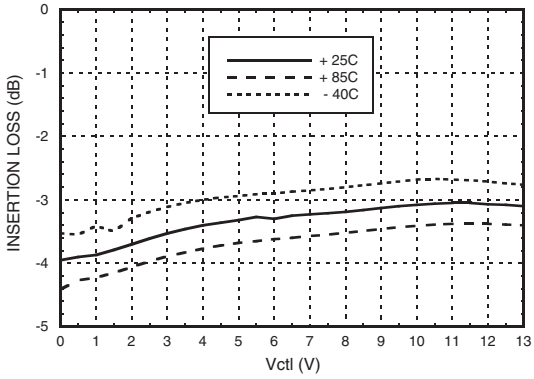
Submit feedback for this data sheet.

430° ANALOG PHASE SHIFTER, 4 - 8 GHz

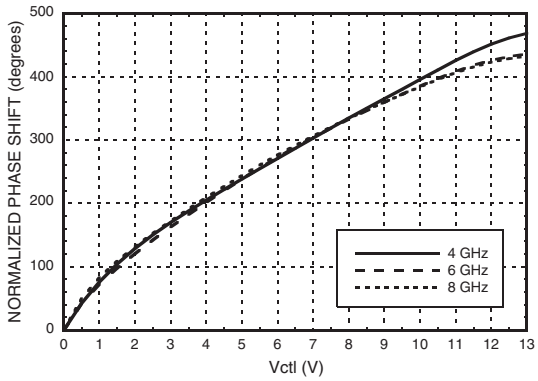
Insertion Loss vs. Frequency



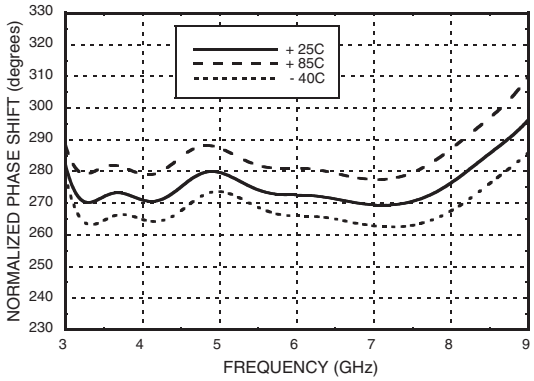
Insertion Loss vs. Vctl , F = 6 GHz



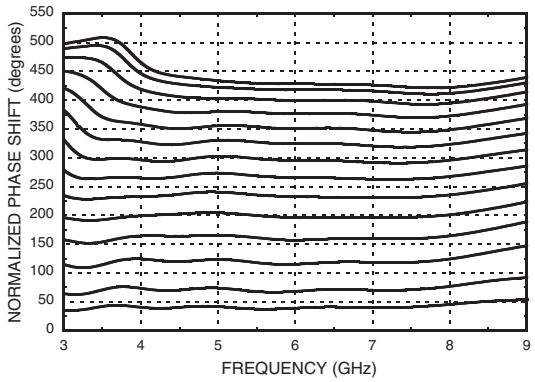
Phase Shift vs. Vctl



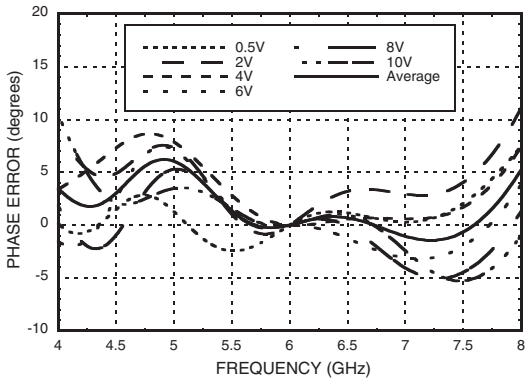
Phase Shift vs. Frequency @ Vctl = 6V (Relative to Vctl = 0V)



Phase Shift vs. Frequency (Relative to Vctl = 0V) Vctl = 0.5 to 13V



Phase Error vs. Frequency, Fmean = 6 GHz [1]



[1] 0 to 10V provides 0 - 380 degrees phase shift range

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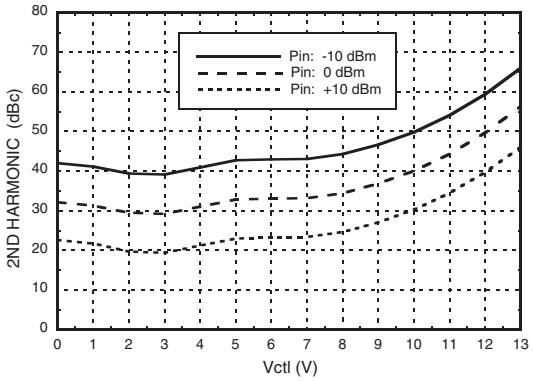
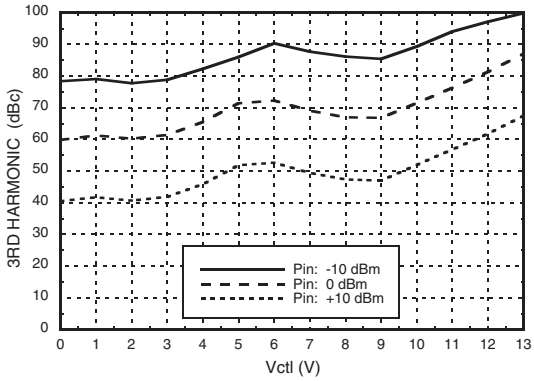
430° ANALOG PHASE SHIFTER, 4 - 8 GHz

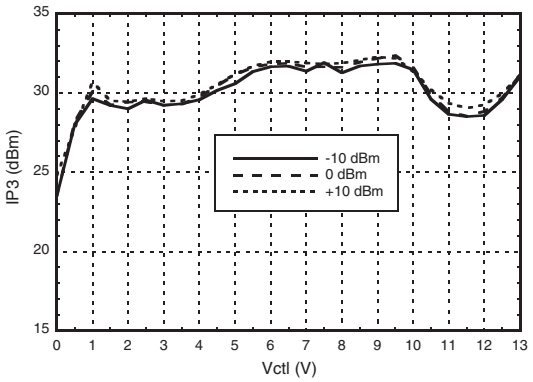
Second Harmonics vs. Vctl, F = 6 GHz



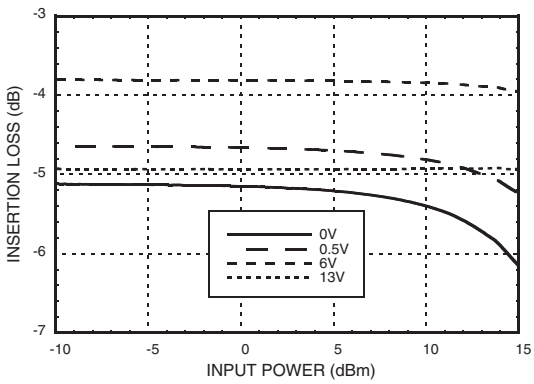
Third Harmonics vs. Vctl, F = 6 GHz



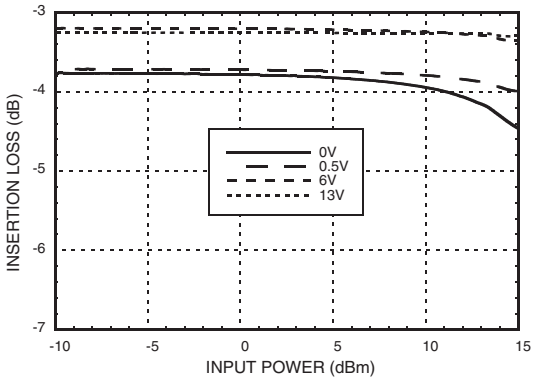
Input IP3 vs. Vctl, F = 6 GHz



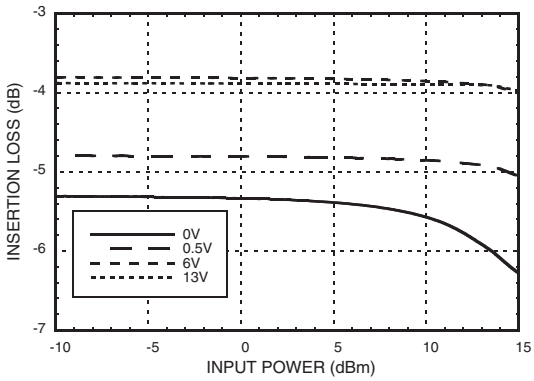
Insertion Loss vs. Pin @ 4 GHz



Insertion Loss vs. Pin @ 6 GHz



Insertion Loss vs. Pin @ 8 GHz



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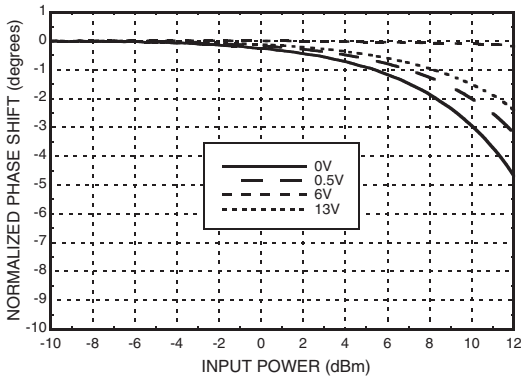
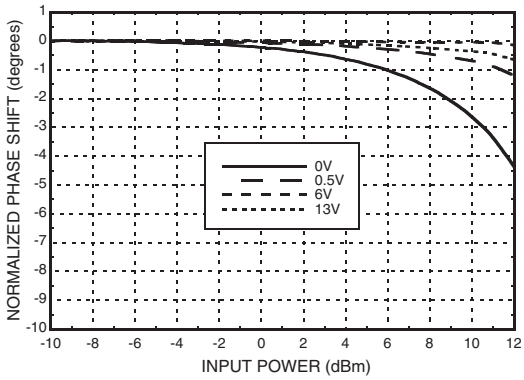
430° ANALOG PHASE SHIFTER, 4 - 8 GHz

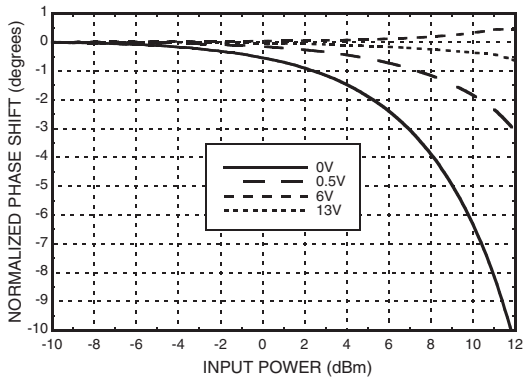
Phase Shift vs. Pin @ 4 GHz



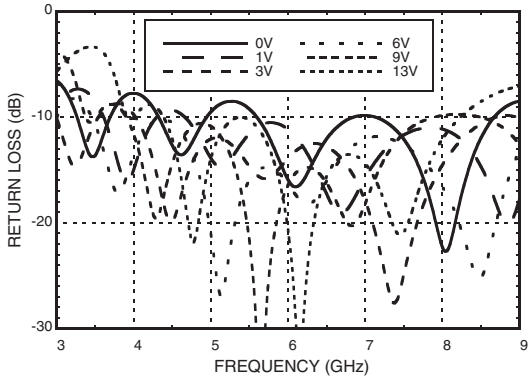
Phase Shift vs. Pin @ 6 GHz



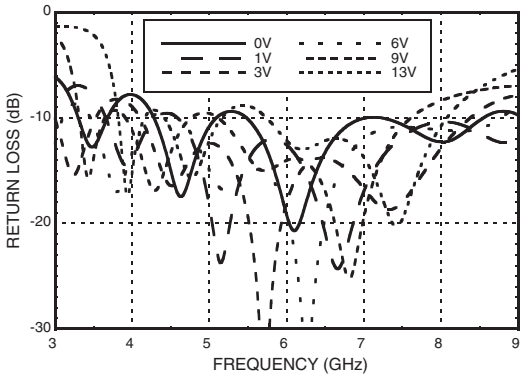
Phase Shift vs. Pin @ 8 GHz



Input Return Loss vs. Frequency, Vctl = 0 to +13V



Output Return Loss vs. Frequency, Vctl = 0 to +13V



Reliability Information

Junction Temperature (Tj)	150 °C
Nominal Junction Temperature (T = 85 °C, Pin = 10 dBm)	87 °C
Thermal Resistance (Junction to GND Paddle)	45 °C/W
Operating Temperature	-40 to +85 °C

Absolute Maximum Ratings

Input Power (RFIN)	+27 dBm
Control Voltage (Vctl)	-0.5V to +15V
Storage Temperature	-65 to +150 °C
ESD Sensitivity (HBM)	Class 1B

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

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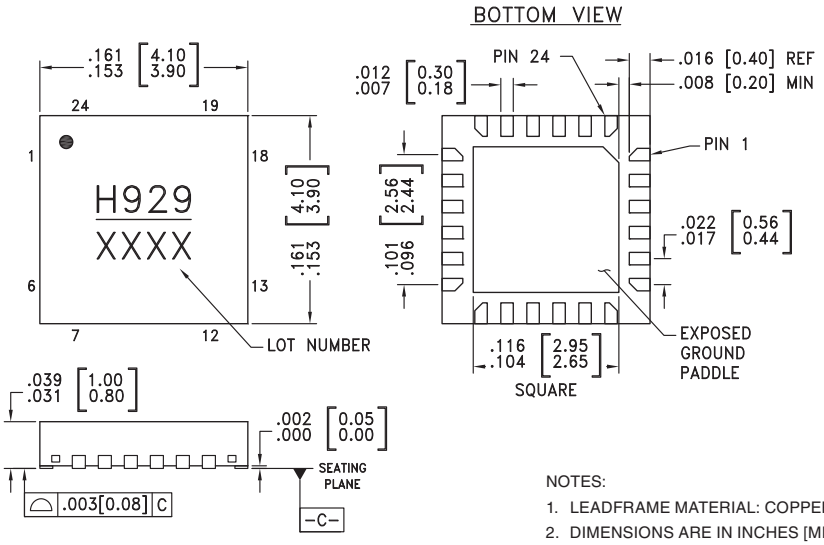
Outline Drawing



NOTES:

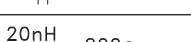
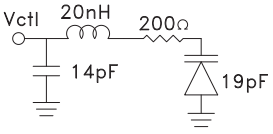
1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS].
3. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
4. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
5. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.
6. CLASSIFIED AS MOISTURE SENSITIVITY LEVEL (MSL) 1.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [1]
HMC929LP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 [2]	H929 XXXX

[1] 4-Digit lot number XXXX
[2] Max peak reflow temperature of 260 °C

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 5 - 14, 18 - 20, 22 - 24	N/C	No connection required. These pins may be connected to RF/DC ground without affecting performance.	
2, 4, 15, 17	GND	Ground: Backside of package has exposed metal ground slug that must be connected to ground thru a short path. Vias under the device are required.	
3	RFIN	Port is DC blocked.	
16	RFOUT	Port is DC blocked.	
21	Vctl	Phase shift control pin. Application of a voltage between 0 and 13 volts causes the transmission phase to change. The DC equivalent circuit is a series connected diode and resistor.	

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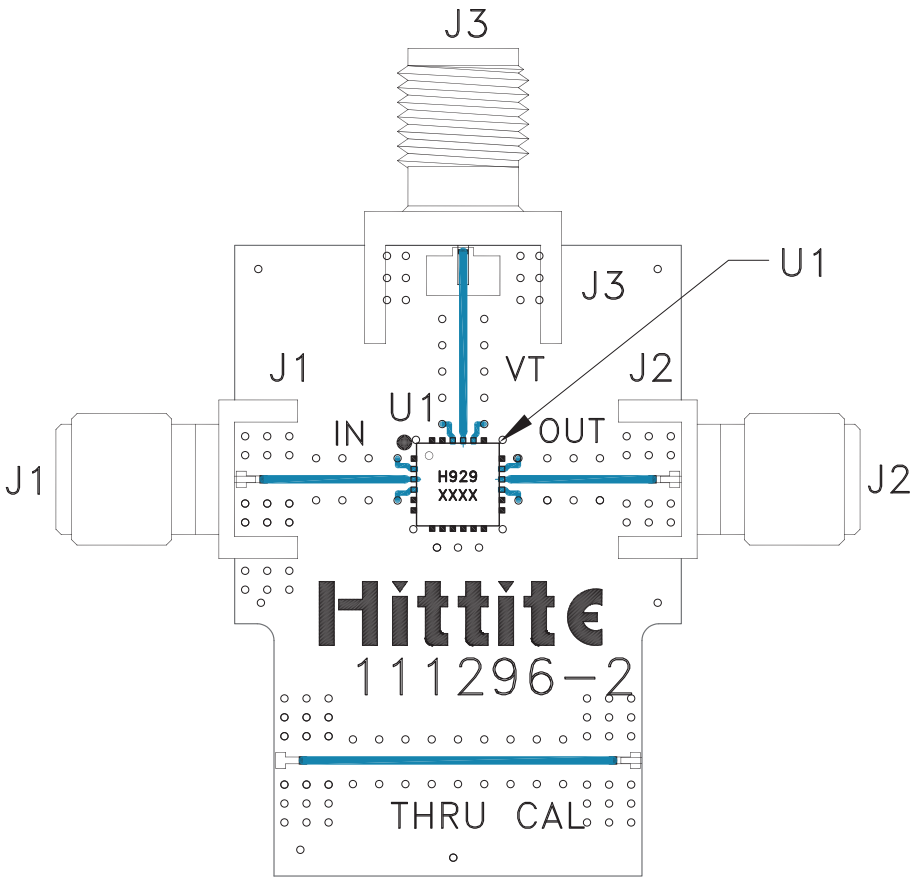
Evaluation PCB



List of Materials for Evaluation PCB 108812 [1]

Item	Description
J1, J2	PCB Mount SMA Connector, SRI
J3	PCB Mount SMA Connector
U1	HMC929LP4E Analog Phase Shifter
PCB [2]	111296 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.

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