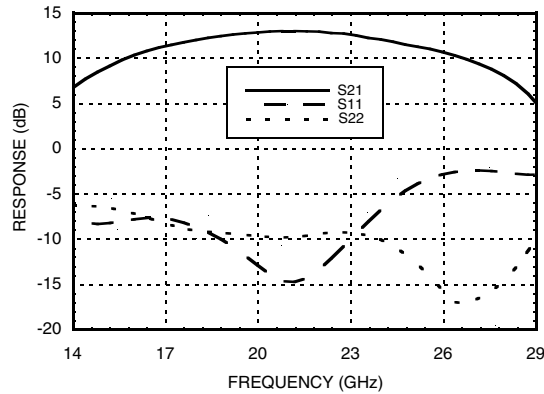


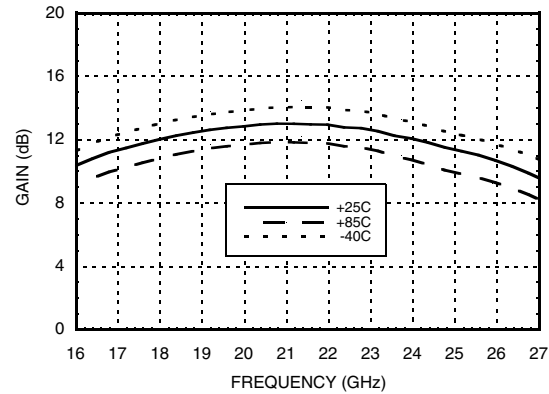


**GaAs PHEMT MMIC MEDIUM
POWER AMPLIFIER, 17.5 - 25.5 GHz**

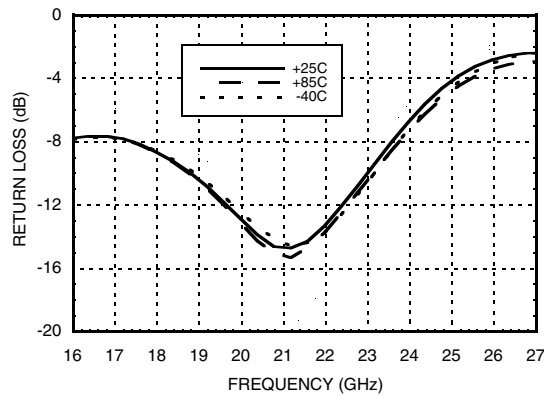
Broadband Gain & Return Loss



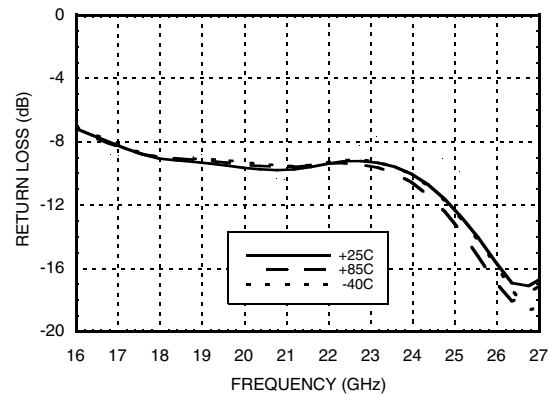
Gain vs. Temperature



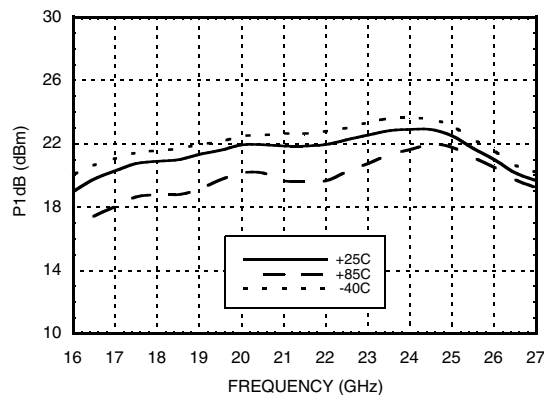
Input Return Loss vs. Temperature



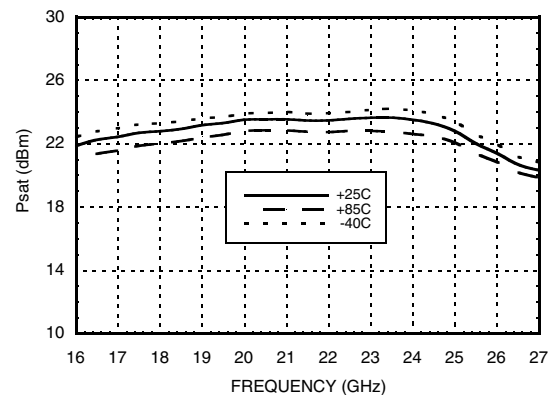
Output Return Loss vs. Temperature

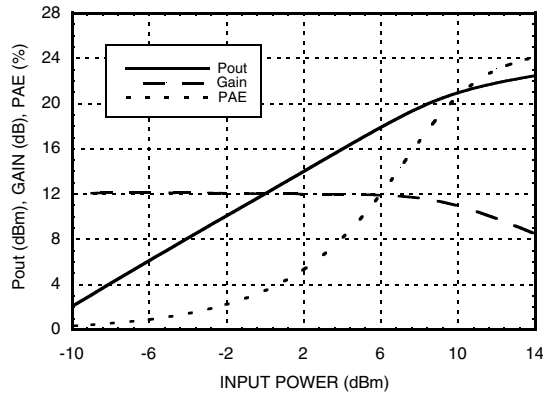
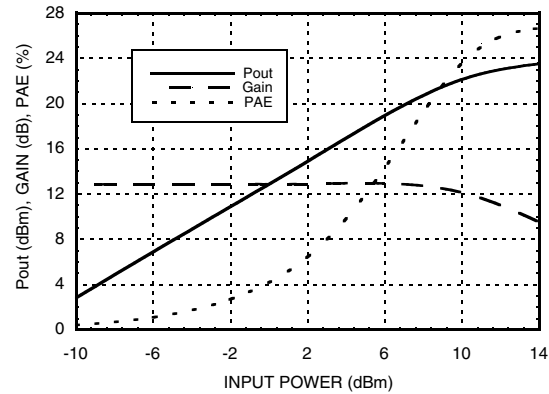
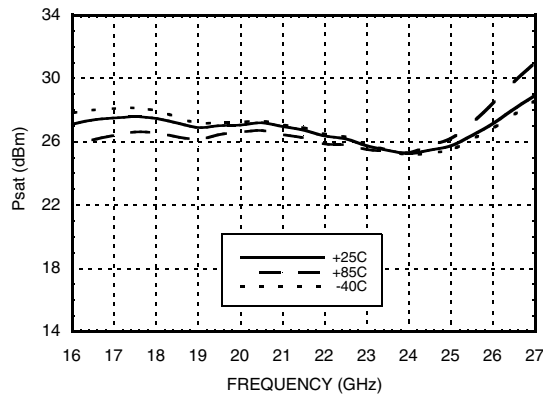
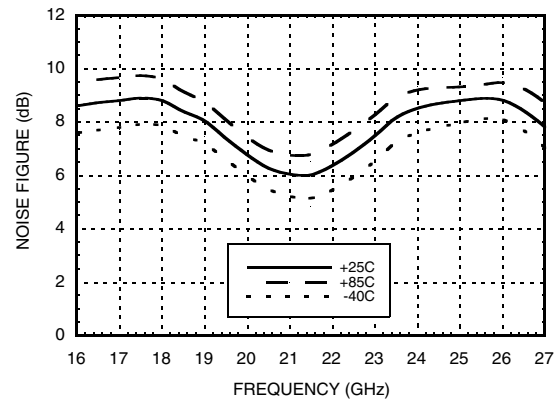
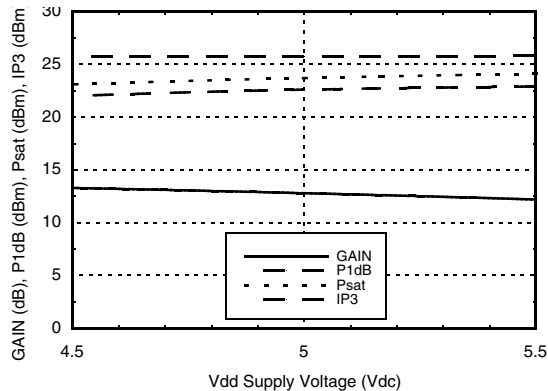
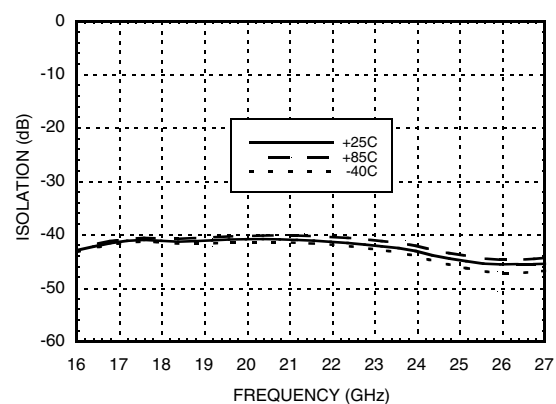


P1dB vs. Temperature



Psat vs. Temperature




**GaAs PHEMT MMIC MEDIUM
POWER AMPLIFIER, 17.5 - 25.5 GHz**
Power Compression @ 18 GHz

Power Compression @ 23 GHz

Output IP3 vs. Temperature

Noise Figure vs. Temperature

**Gain, Power and Output IP3 vs.
Supply Voltage @ 23 GHz**

Reverse Isolation vs. Temperature




GaAs PHEMT MMIC MEDIUM POWER AMPLIFIER, 17.5 - 25.5 GHz

Absolute Maximum Ratings

Drain Bias Voltage (Vdd)	+5.5 Vdc
Gate Bias Voltage (Vgg)	-8.0 to 0 Vdc
RF Input Power (RFIN)(Vdd = +5Vdc, Idd = 85 mA)	+16 dBm
Channel Temperature	175 °C
Continuous Pdiss (T = 85 °C) (derate 5.46 mW/°C above 85 °C)	0.491 W
Thermal Resistance (channel to ground paddle)	183 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

Typical Supply Current vs. Vdd

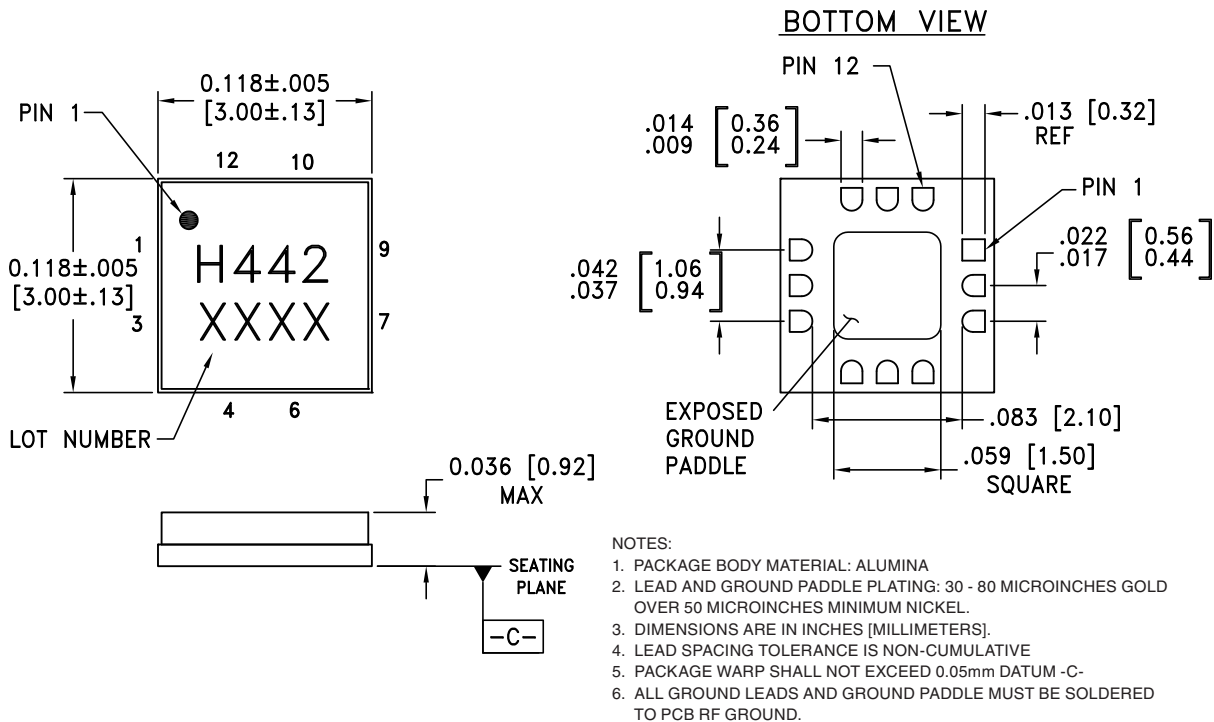
Vdd (V)	Idd (mA)
+4.5	82
+5.0	84
+5.5	86

Note: Amplifier will operate over full voltage range shown above



ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Outline Drawing



Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[2]
HMC442LC3B	Alumina, White	Gold over Nickel	MSL3 ^[1]	H442 XXXX

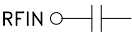
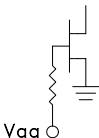
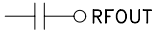
[1] Max peak reflow temperature of 260 °C

[2] 4-Digit lot number XXXX



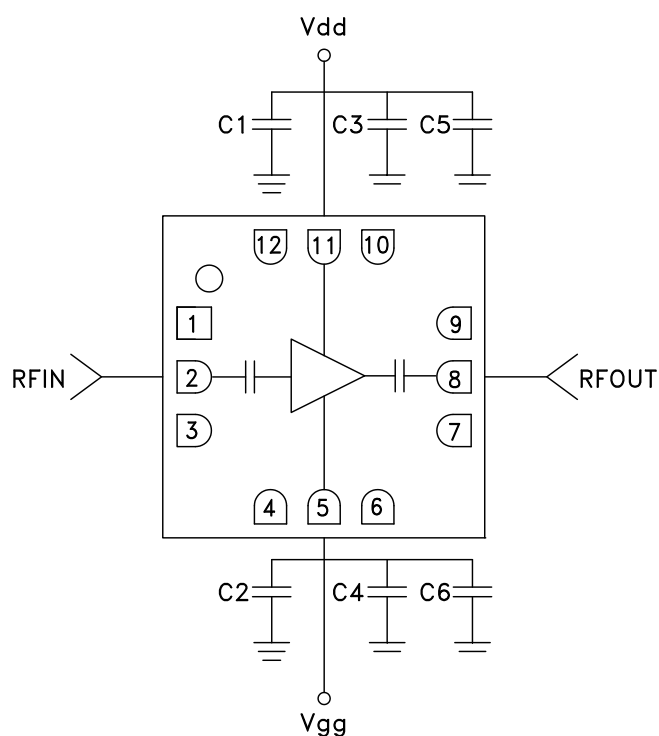
GaAs PHEMT MMIC MEDIUM POWER AMPLIFIER, 17.5 - 25.5 GHz

Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 3, 7, 9	GND	Package bottom must also be connected to RF/DC ground	
2	RFIN	This pin is AC coupled and matched to 50 Ohms.	
4, 6, 10, 12	N/C	This pin may be connected to RF/DC ground. Performance will not be affected.	
5	Vgg	Gate control for amplifier. Adjust to achieve Id of 84 mA. Please follow "MMIC Amplifier Biasing Procedure" Application Note.	
8	RFOUT	This pin is AC coupled and matched to 50 Ohms.	
11	Vdd	Power Supply Voltage for the amplifier. External bypass capacitors are required.	

Application Circuit

Component	Value
C1, C2	100 pF
C3, C4	1,000 pF
C5, C6	2.2 μ F



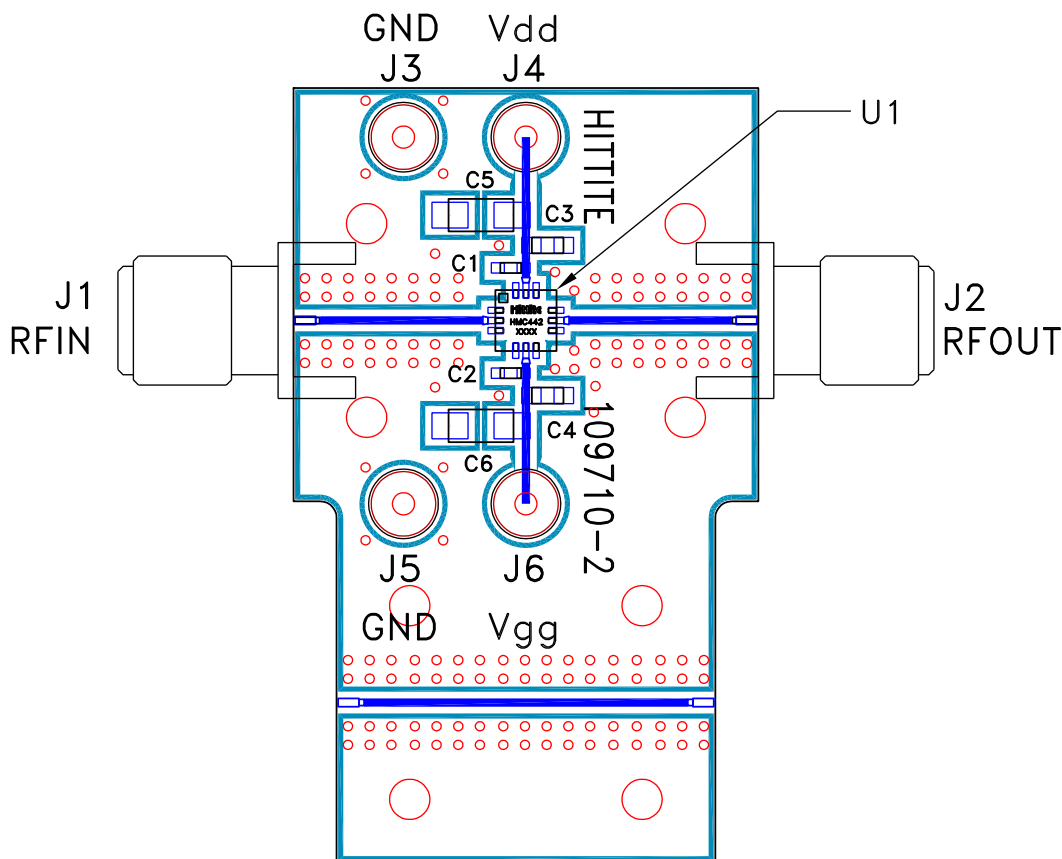
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**GaAs PHEMT MMIC MEDIUM
POWER AMPLIFIER, 17.5 - 25.5 GHz**

Evaluation PCB



List of Materials for Evaluation PCB 109712 [1]

Item	Description
J1 - J2	PCB Mount SMA Connector
J3 - J6	DC Pin
C1 - C2	100 pF Capacitor, 0402 Pkg.
C3 - C4	1000 pF Capacitor, 0603 Pkg.
C5 - C6	2.2 μF Capacitor, Tantalum
U1	HMC442LC3B Amplifier
PCB [2]	109710 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.