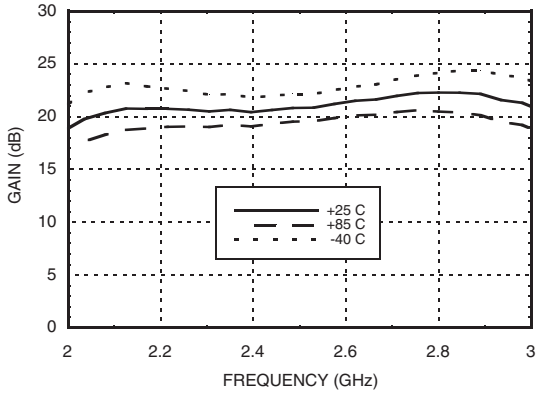
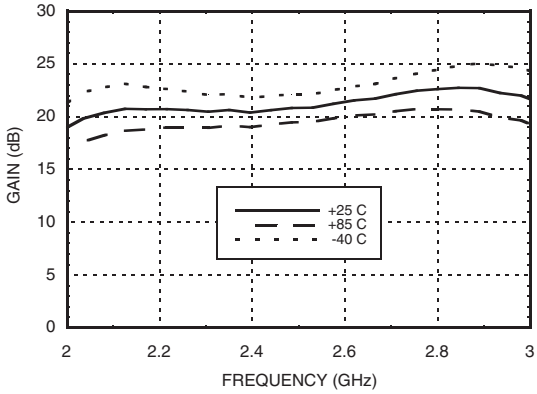
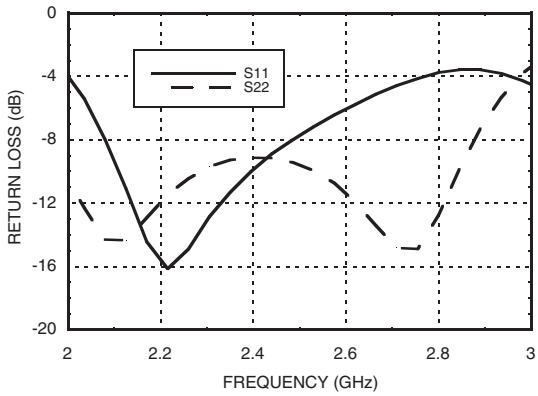
Gain vs. Temperature, Vs= 3.6V



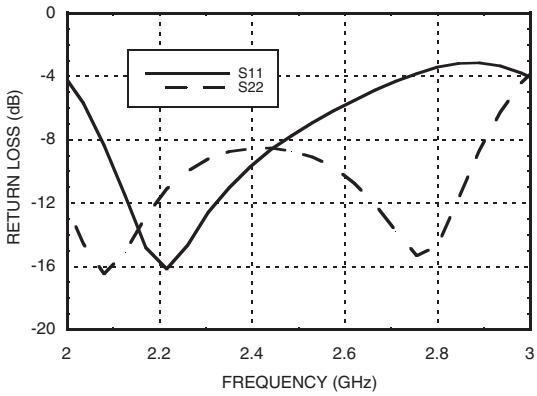
Gain vs. Temperature, Vs= 5V



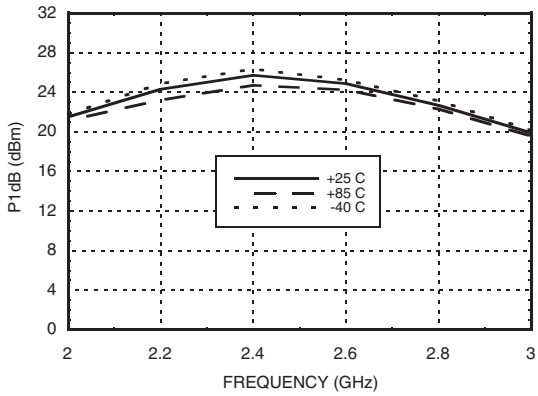
Return Loss, Vs= 3.6V



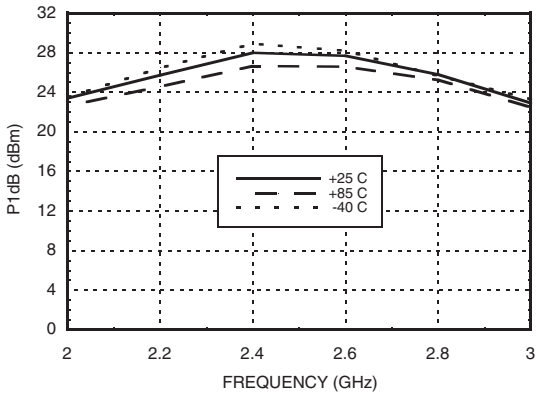
Return Loss, Vs= 5V



P1dB vs. Temperature, Vs= 3.6V



P1dB vs. Temperature, Vs= 5V



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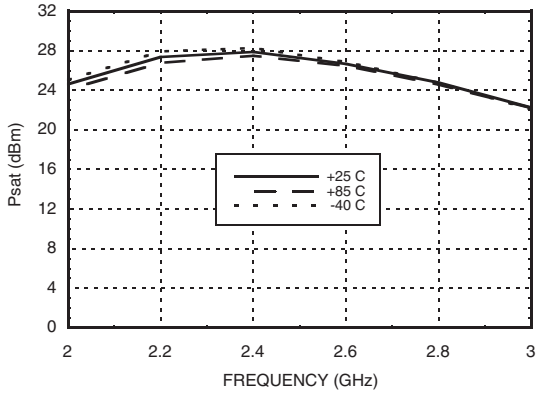
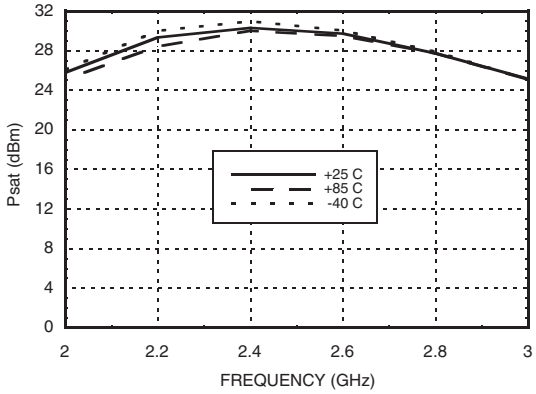
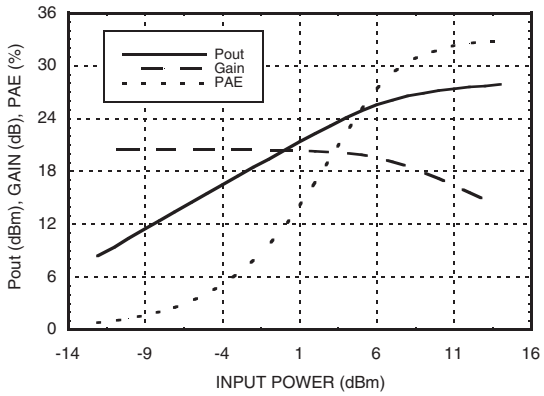
Psat vs. Temperature, Vs= 3.6V



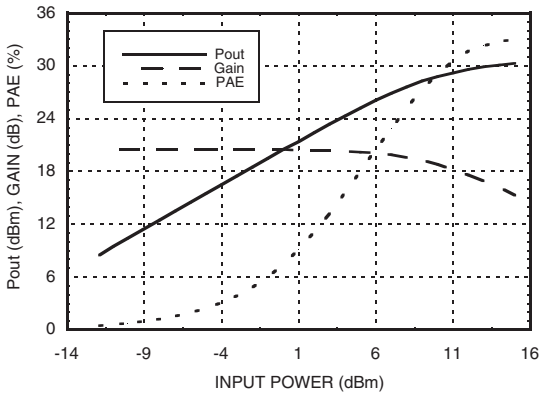
Psat vs. Temperature, Vs= 5V



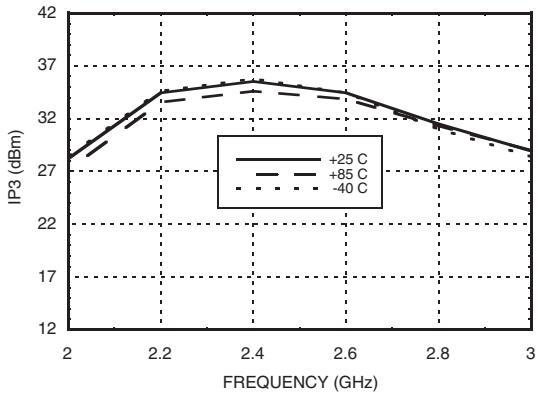
Power Compression@ 2.4 GHz, Vs= 3.6V



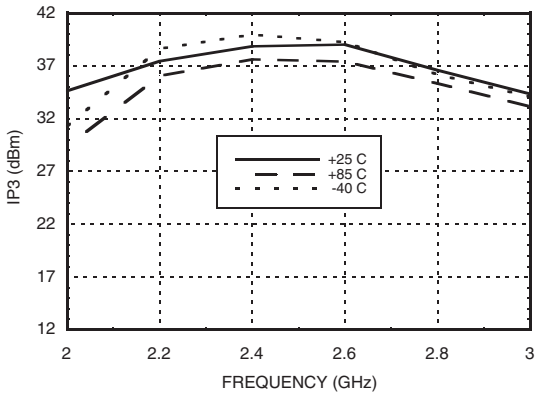
Power Compression@ 2.4 GHz, Vs= 5V



Output IP3 vs. Temperature, Vs= 3.6V



Output IP3 vs. Temperature, Vs= 5V



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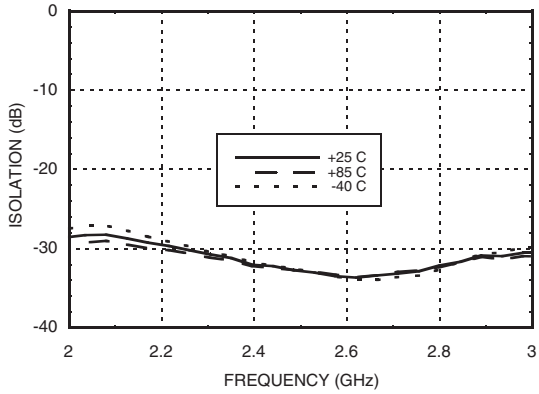
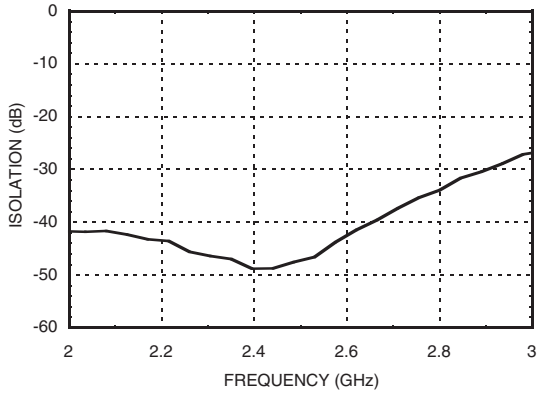
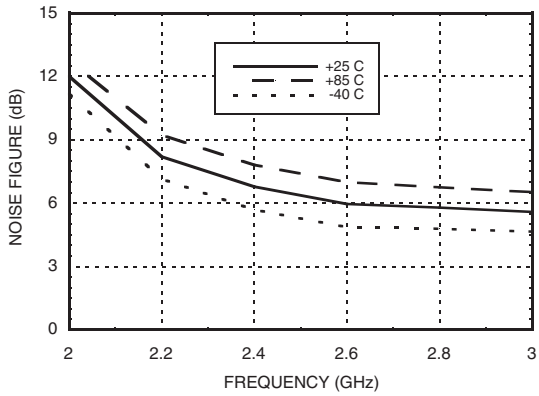
Reverse Isolation vs. Temperature, Vs= 3.6V



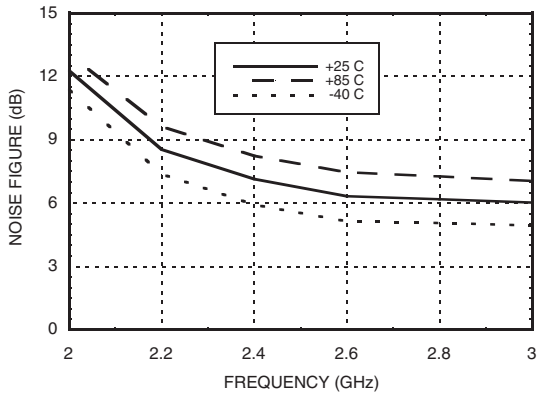
Power Down Isolation, Vs= 3.6V



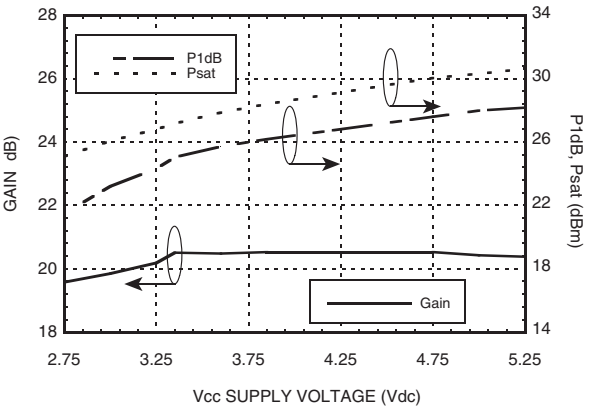
Noise Figure vs. Temperature, Vs= 3.6V



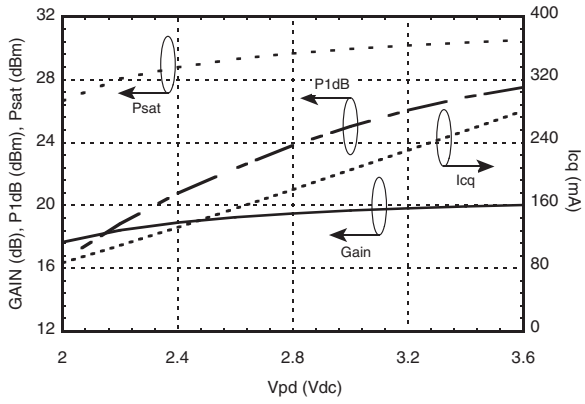
Noise Figure vs. Temperature, Vs= 5V



Gain & Power vs. Supply Voltage



Gain, Power & Quiescent Supply Current vs Vpd@ 2.4 GHz



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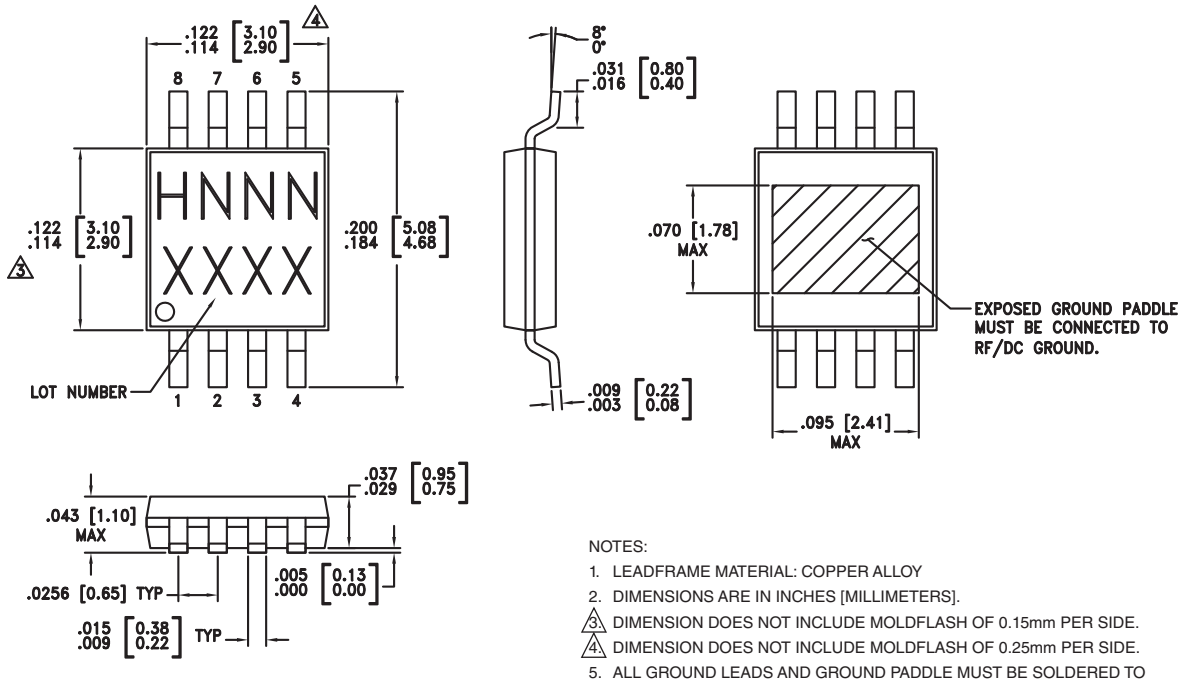
RoHS✓ EARTH FRIENDLY

Absolute Maximum Ratings

Collector Bias Voltage (Vcc)	+5.5 Vdc
Control Voltage (Vpd1, Vpd2)	+4.0 Vdc
RF Input Power (RFIN)(Vs = +5.0, Vpd = +3.6 Vdc)	+17 dBm
Junction Temperature	150 °C
Continuous Pdiss (T = 85 °C) (derate 27 mW/°C above 85 °C)	1.755 W
Thermal Resistance (junction to ground paddle)	37 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Outline Drawing



NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS].
3. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
4. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
5. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC414MS8G	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 [1]	H414 XXXX
HMC414MS8GE	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 [2]	H414 XXXX

[1] Max peak reflow temperature of 235 °C
[2] Max peak reflow temperature of 260 °C
[3] 4-Digit lot number XXXX

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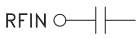
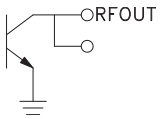
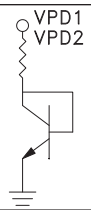
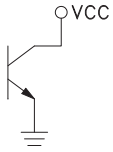
Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	RFIN	This pin is AC coupled and matched to 50 Ohms.	
2	NC	Not Connected.	
3, 4	RFOUT	RF output and DC bias for the output stage.	
5	GND	Ground: Backside of package has exposed metal ground slug that must be connected to ground thru a short path. Vias under the device are required.	
6, 8	Vpd1, Vpd2	Power control pin. For maximum power, this pin should be connected to 3.6V. For 5V operation, a dropping resistor is required. A higher voltage is not recommended. For lower idle current, this voltage can be reduced.	
7	Vcc	Power supply voltage for the first amplifier stage. An external bypass capacitor of 330 pF is required as shown in the application schematic.	

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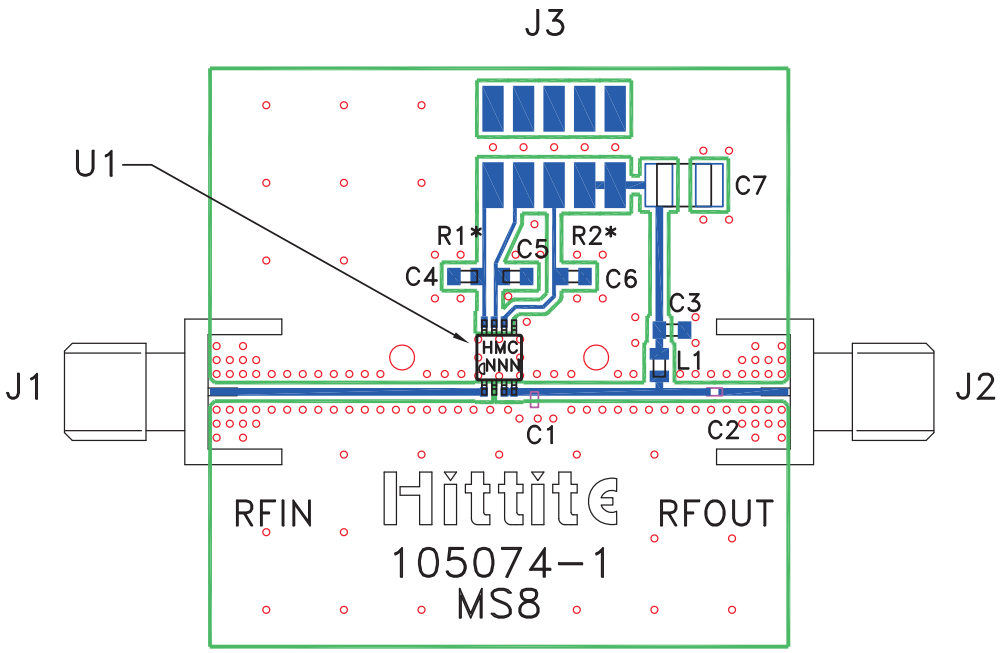
Evaluation PCB



* For 5V operation on Vctl line, select R1, R2 such that 3.6V is presented on Pins 6 and 8.

List of Materials for Evaluation PCB 105006 [1]

Item	Description
J1 - J2	PCB Mount SMA RF Connector
J3	2 mm DC Header
C1	2.7 pF Capacitor, 0603 Pkg.
C2	100 pF Capacitor, 0402 Pkg.
C3 - C6	330 pF Capacitor, 0603 Pkg.
C7	2.2 µF Capacitor, Tantalum
L1	18nH Inductor 0603 Pkg.
U1	HMC414MS8G / HMC414MS8GE Amplifier
PCB [2]	105074 Eval Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.

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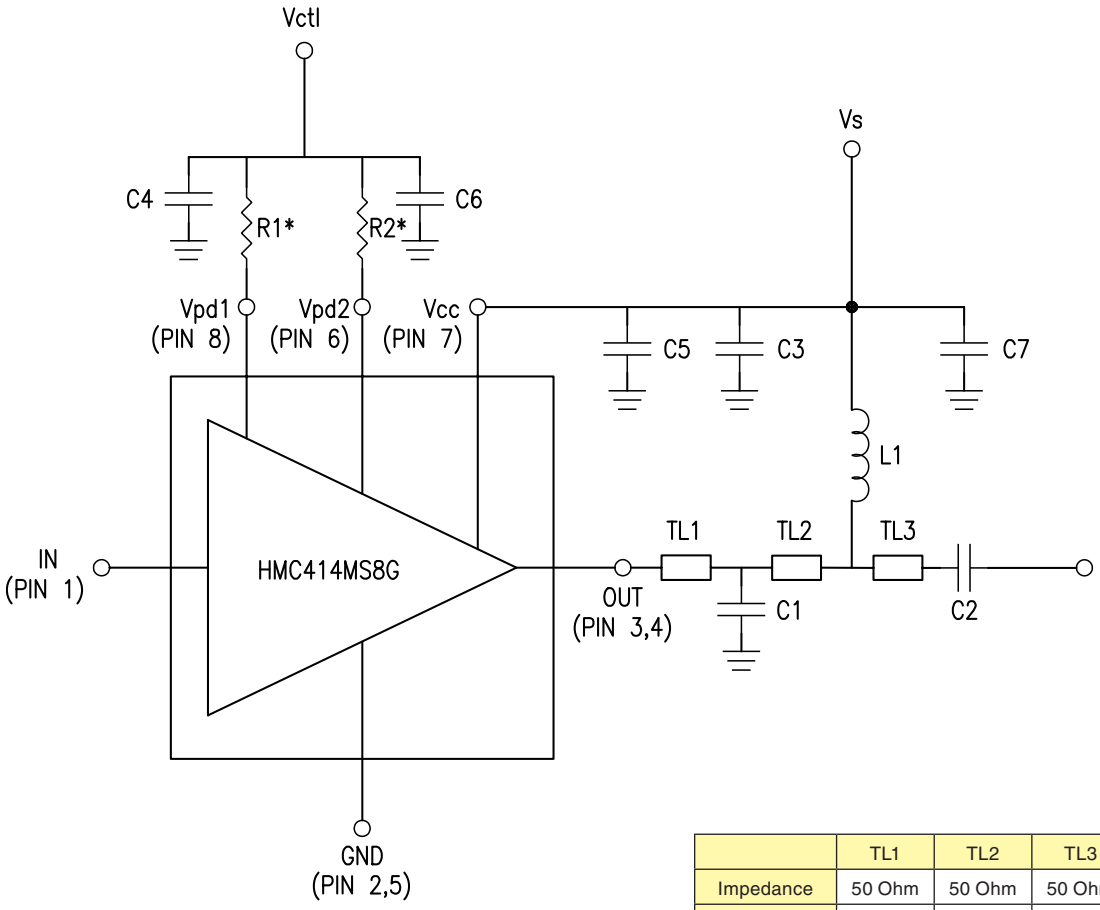
Application Circuit



	TL1	TL2	TL3
Impedance	50 Ohm	50 Ohm	50 Ohm
Length	0.036"	0.3"	0.11"

* For 5V operation on Vctl line, select R1, R2 such that 3.6V is presented on Pins 6 and 8.

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