

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	40	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 5)	I _D	T _A = +25°C 16.8 T _A = +70°C 13.9	A
Continuous Drain Current, V _{GS} = 10V (Note 6)	I _D	T _C = +25°C 70 T _C = +100°C 51	A
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	80	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	80	A
Avalanche Current, L = 0.1mH	I _{AS}	20	A
Avalanche Energy, L = 0.1mH	E _{AS}	20	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	2.6	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	47	°C/W
Total Power Dissipation (Note 6)	P _D	59	W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	2.5	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	40	-	-	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μA	V _{DS} = 32V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1	-	3	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	-	5	7.3	mΩ	V _{GS} = 10V, I _D = 20A
		-	7	9.8		V _{GS} = 4.5V, I _D = 20A
Diode Forward Voltage	V _{SD}	-	-	1.2	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	-	1895	-	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	-	485	-		
Reverse Transfer Capacitance	C _{rss}	-	20.9	-		
Gate Resistance	R _g	-	0.62	-	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	-	12.4	-	nC	V _{DS} = 30V, I _D = 20A
Total Gate Charge (V _{GS} = 10V)	Q _g	-	29.1	-		
Gate-Source Charge	Q _{gs}	-	5.9	-		
Gate-Drain Charge	Q _{gd}	-	3.5	-		
Turn-On Delay Time	t _{D(ON)}	-	5.4	-	ns	V _{DD} = 30V, V _{GS} = 10V, I _D = 20A, R _G = 3Ω
Turn-On Rise Time	t _R	-	4.5	-		
Turn-Off Delay Time	t _{D(OFF)}	-	16.2	-		
Turn-Off Fall Time	t _F	-	3.5	-		
Body Diode Reverse Recovery Time	t _{RR}	-	30.6	-	ns	I _F = 20A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{RR}	-	28.1	-	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

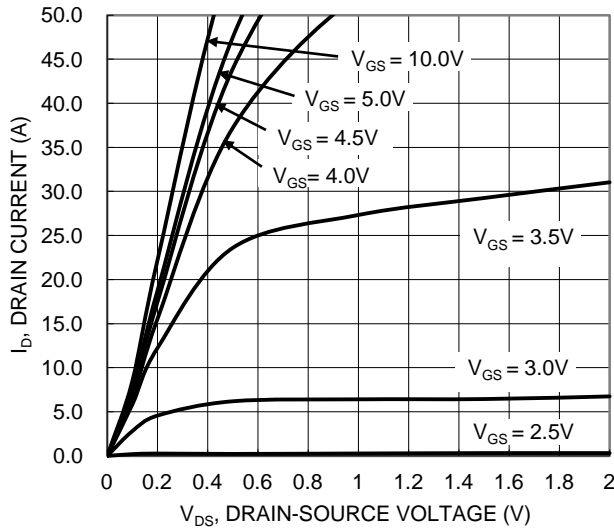


Figure 1. Typical Output Characteristic

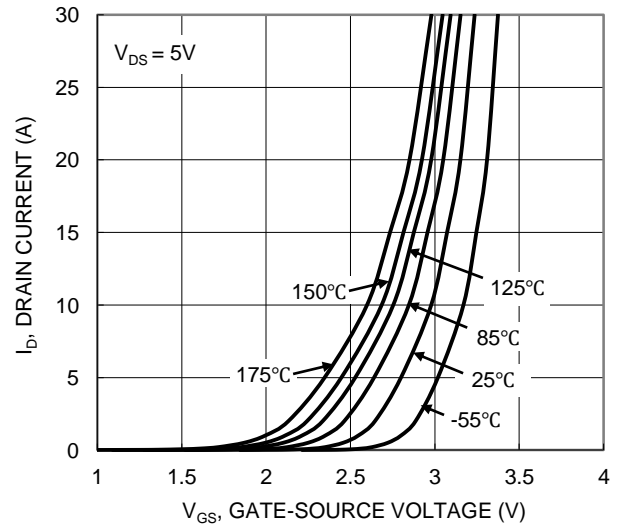


Figure 2. Typical Transfer Characteristic

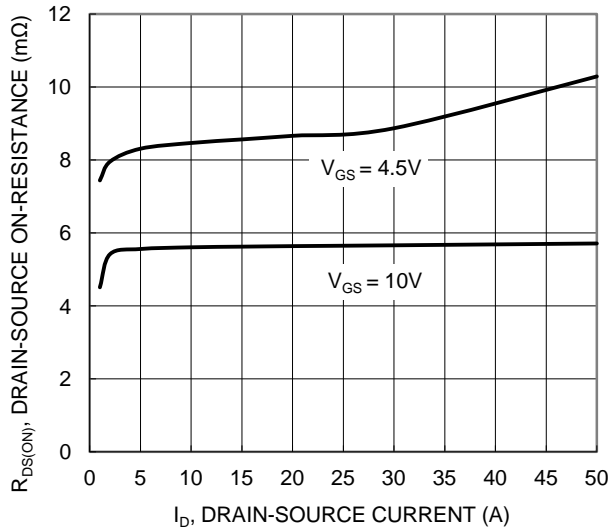


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

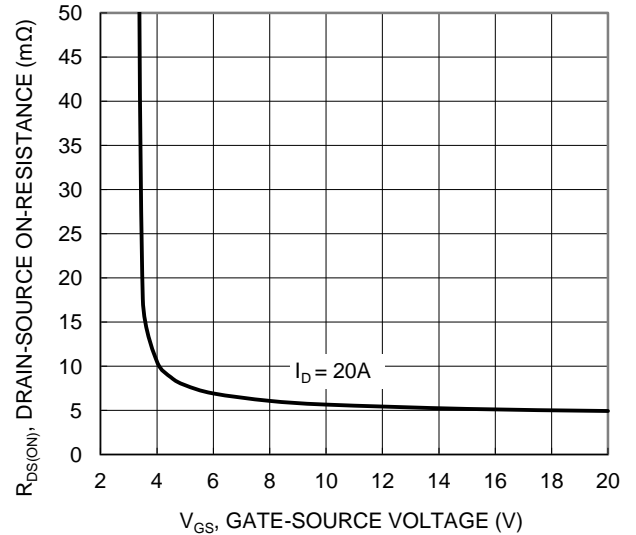


Figure 4. Typical Transfer Characteristic

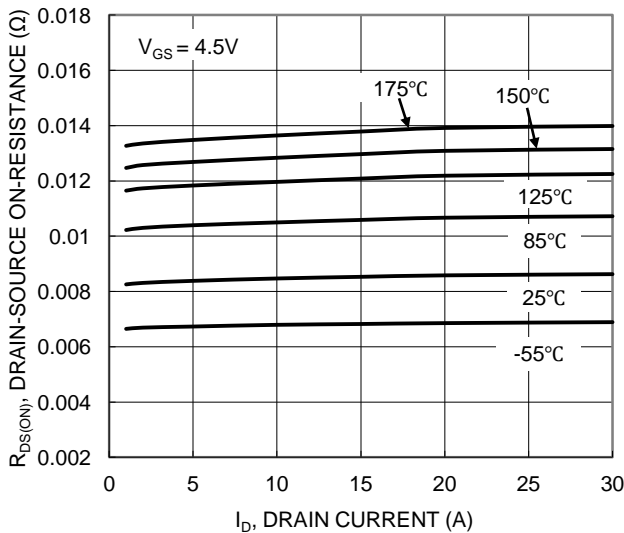


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

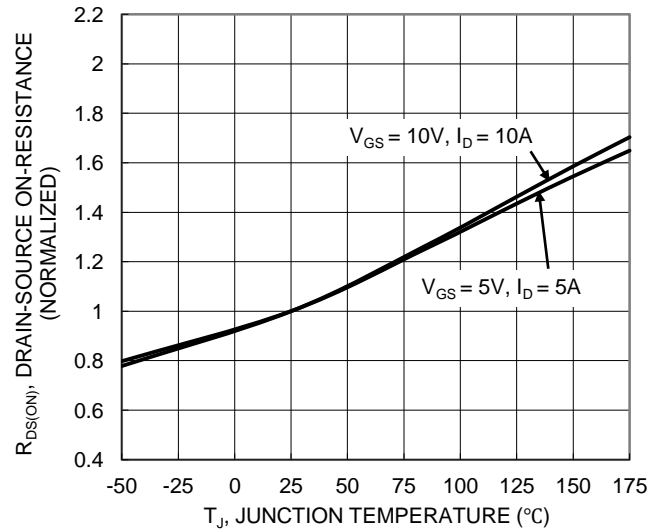


Figure 6. On-Resistance Variation with Temperature

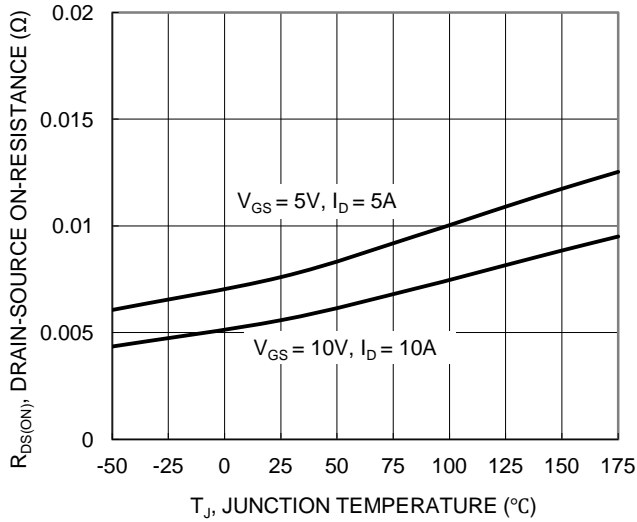


Figure 7. On-Resistance Variation with Temperature

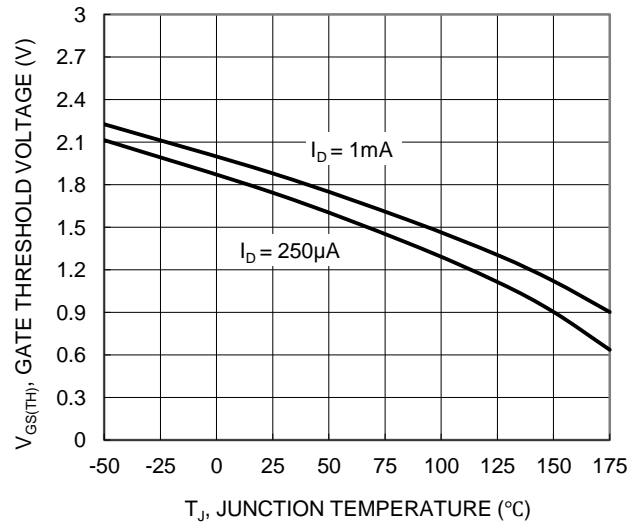


Figure 8. Gate Threshold Variation vs. Junction Temperature

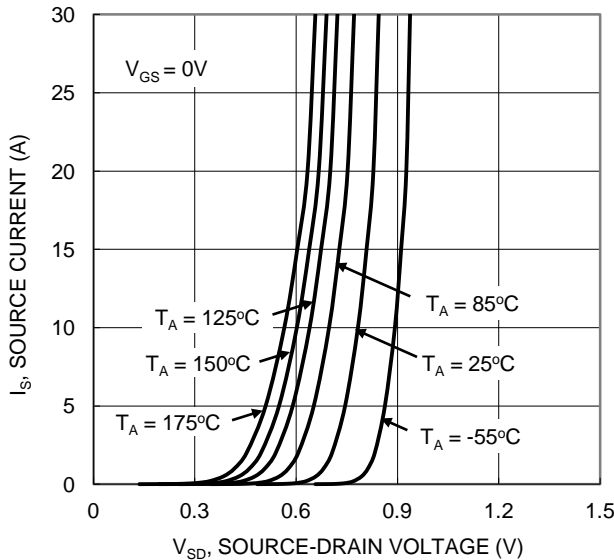


Figure 9. Diode Forward Voltage vs. Current

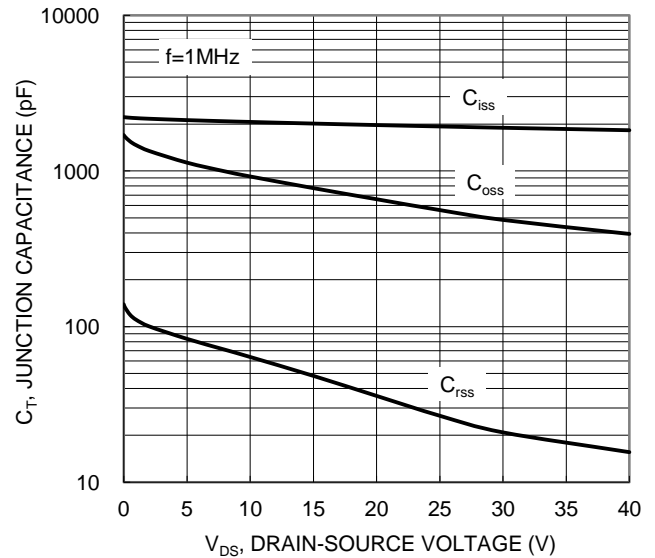


Figure 10. Typical Junction Capacitance

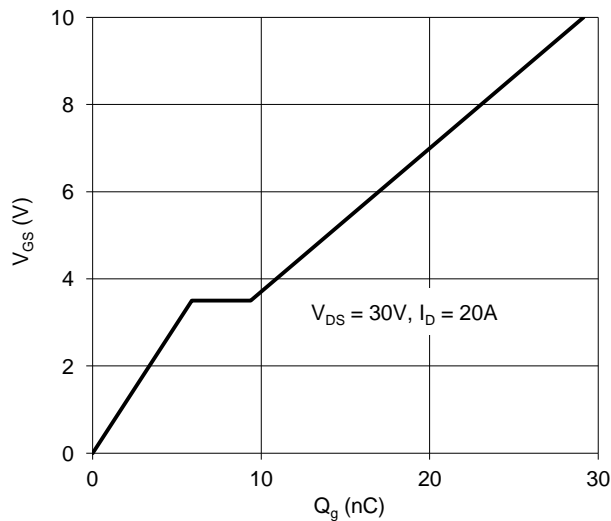


Figure 11. Gate Charge

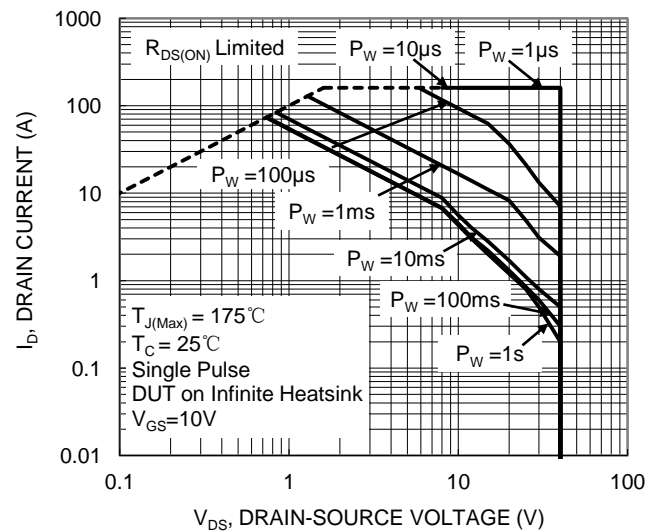


Figure 12. SOA, Safe Operation Area

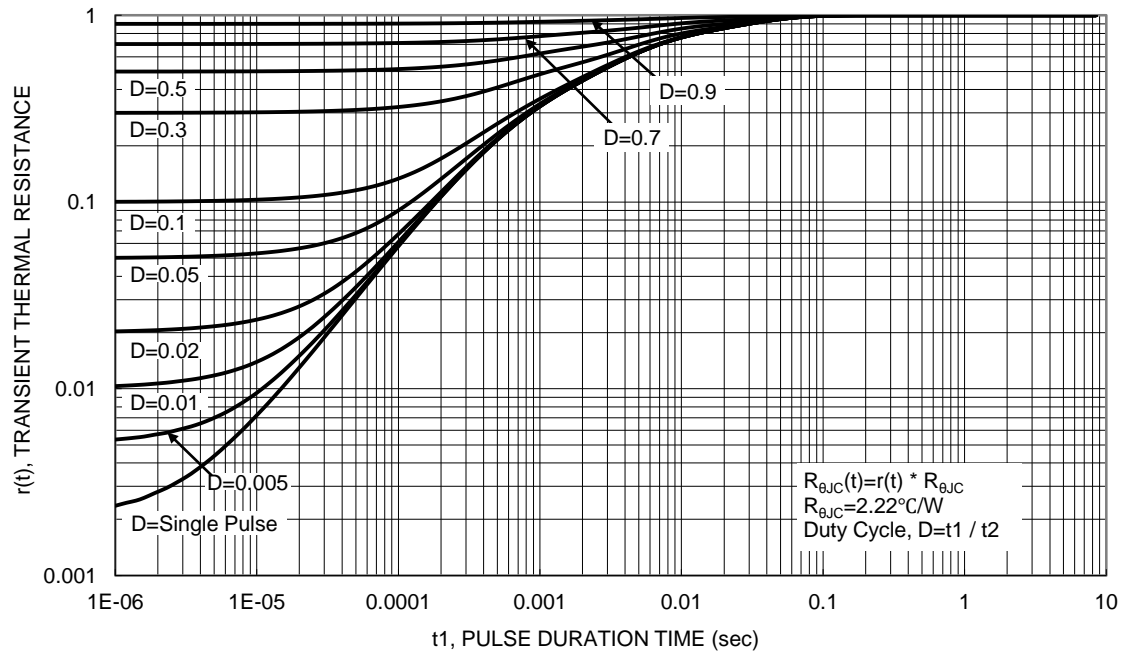
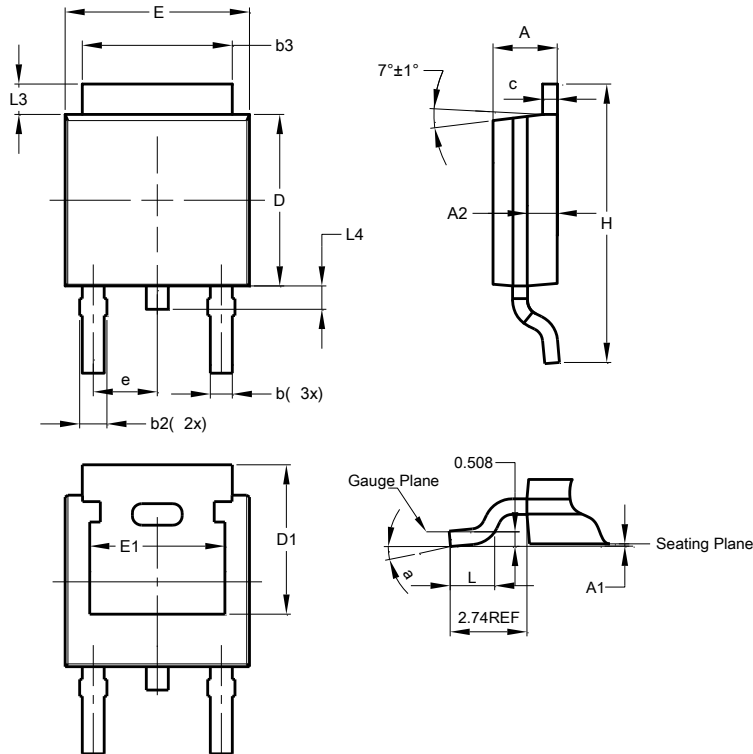


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

TO252 (DPAK)

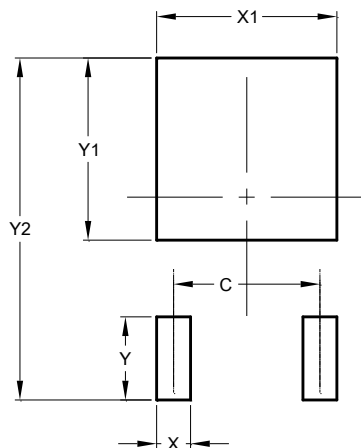


TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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