

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	-20	V
Gate-Source Voltage		V_{GSS}	±12	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	$T_A = +25$ °C $T_A = +70$ °C	I _D	-3.3 -2.6	А
Pulsed Drain Current		I _{DM}	-13	Α

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA} ◀	90	°C/W
Thermal Resistance, Junction to Case (Note 5)	Rejc	22	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

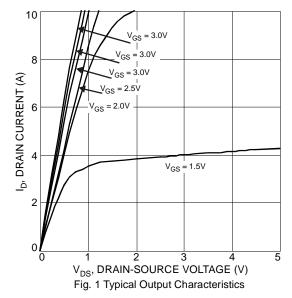
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

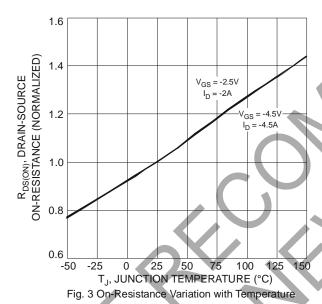
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20		7-1	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}			-1.0	μA	$V_{DS} = -16V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100 ±800	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$ $V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	$V_{GS(th)}$	-0.4	-0.6	-0.9	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance			60 73	75 96	mΩ	$V_{GS} = -4.5V, I_D = -1.5A$ $V_{GS} = -2.5V, I_D = -1.2A$	
Static Brain Source Of Resistance	R _{DS} (ON)		92	140		$V_{GS} = -2.3V$, $I_{D} = -1.2A$ $V_{GS} = -1.8V$, $I_{D} = -1.2A$	
Forward Transconductance	g FS		7	_	S	$V_{DS} = -10V, I_{D} = -1.5A$	
Diode Forward Voltage (Note 5)	V _{SD}	_		-1.0	V	$V_{GS} = 0V, I_{S} = -1.0A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}	_	627	_	pF	V 40V V 0V	
Output Capacitance	Coss	_	64	_	pF	V _{DS} = -10V, V _{GS} = 0V -f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	53	_	pF		
Gate Resistance	Rg	_	44.9	_	Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$	
Total Gate Charge	Q_g	_	6.5	_	nC		
Gate-Source Charge	Qgs	_	0.9	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_D = -3A$	
Gate-Drain Charge	Q_{gd}	_	1.5	_	nC		
Turn-On Delay Time	t _{D(on)}	_	12.5	_	ns		
Turn-On Rise Time	t _r	_	10.3	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t _{D(off)}	_	46.5		ns	$R_L = 10\Omega$, $R_G = 1.0\Omega$, $I_D = -1A$	
Turn-Off Fall Time	t _f	_	22.2	_	ns		

5. Device mounted on 1in^2 FR-4 PCB with 2 oz. Copper. $t \le 10$ sec. Notes:

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.







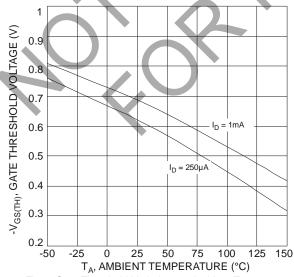


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

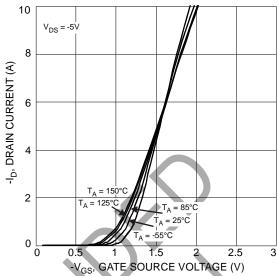
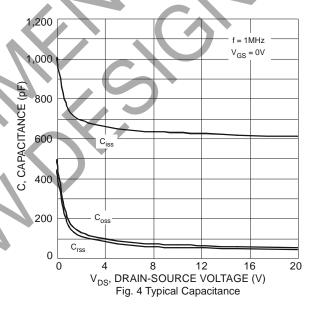


Fig. 2 Typical Transfer Characteristics



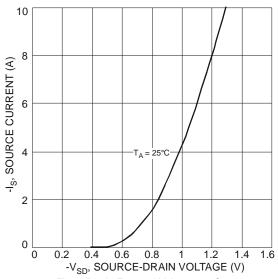
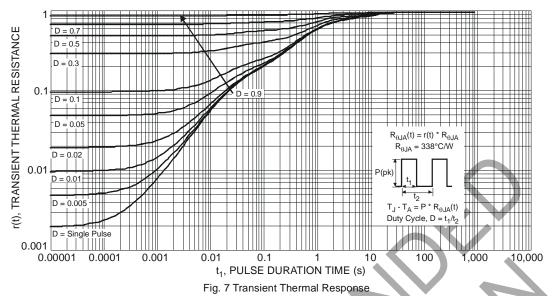


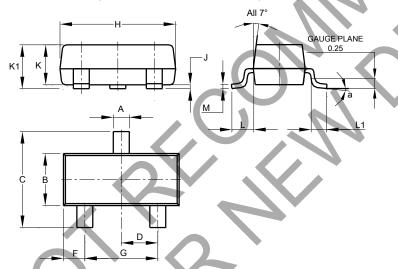
Fig. 6 Diode Forward Voltage vs. Current





Package Outline Dimensions

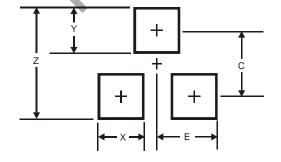
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
М	0.085	0.150	0.110		
α 8°					
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
Z	2.9		
Х	0.8		
Υ	0.9		
С	2.0		
E	1.35		



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