

# 

Characteristic		Symbol	Value	Units
Drain-Source Voltage		$V_{DSS}$	-60	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-6.6 -5.3	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)		I <sub>DM</sub>	-50	Α
Maximum Continuous Body Diode Forward Current (Note 6)		Is	-1.8	Α
Avalanche Current, L = 0.1mH		I <sub>AS</sub>	-35.5	Α
Avalanche Energy, L = 0.1mH		E <sub>AS</sub>	62.9	mJ

# Thermal Characteristics (@T<sub>A</sub> = +25°C unless otherwise specified.)

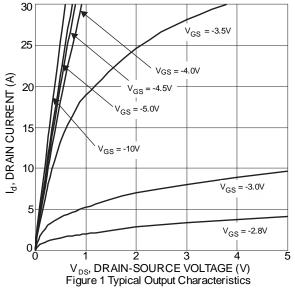
Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	P <sub>D</sub>	1.2	W	
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	100	°C/W	
Total Power Dissipation (Note 6)	P <sub>D</sub>	1.6	W	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{ heta JA}$	75	°C AA/	
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	12	°C/W	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

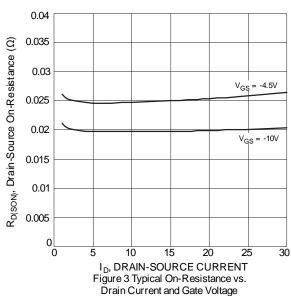
## **Electrical Characteristics** (T<sub>A</sub> = +25°C unless otherwise specified.)

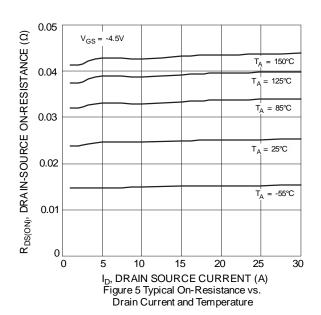
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-60	_		٧	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	-1	_	-3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		_	_	25	mΩ	$V_{GS} = -10V, I_D = -5A$	
Static Drain-Source On-Resistance	R <sub>DS</sub> (ON)	_		33	11122	$V_{GS} = -4.5V$ , $I_{D} = -4A$	
Diode Forward Voltage	$V_{SD}$	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	_	2569	_	pF	.,	
Output Capacitance	Coss	_	179	l	pF	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V, f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	143	l	рF		
Gate Resistance	$R_{g}$	_	8	l	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V,)	Qg	_	26.5	l	nC	V <sub>DS</sub> = -30V, I <sub>D</sub> = -5A	
Total Gate Charge (V <sub>GS</sub> = -10V),	$Q_g$	_	53.1		nC		
Gate-Source Charge	$Q_{gs}$	_	7.1		nC		
Gate-Drain Charge	$Q_{gd}$	_	12.6		nC		
Turn-On Delay Time	t <sub>D(on)</sub>	_	6	_	ns		
Turn-On Rise Time	t <sub>r</sub>	_	7.1	_	ns	$V_{GS} = -10V, V_{DS} = -30V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	110	_	ns	$R_G = 3\Omega$ , $I_D = -5A$	
Turn-Off Fall Time	t <sub>f</sub>		62	_	ns		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	_	20	_	nS	-I <sub>F</sub> = -5A, di/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	_	14	_	nC		

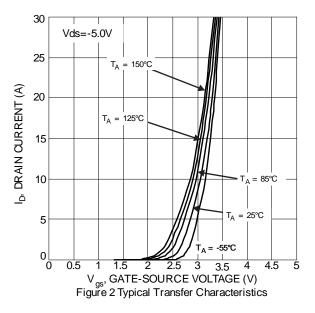
Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.

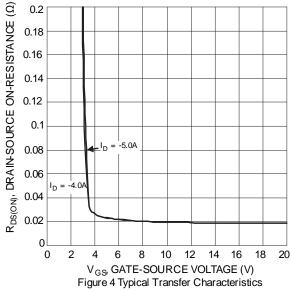


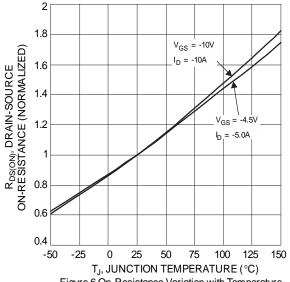




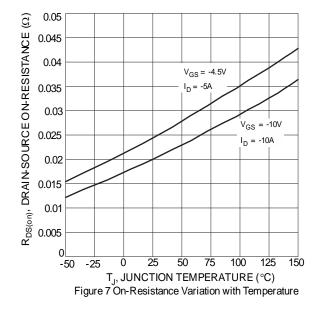


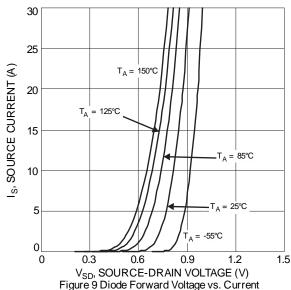


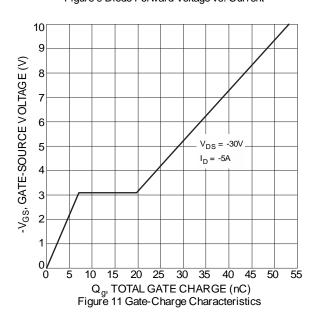












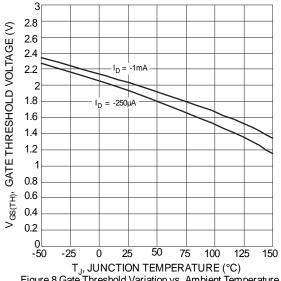
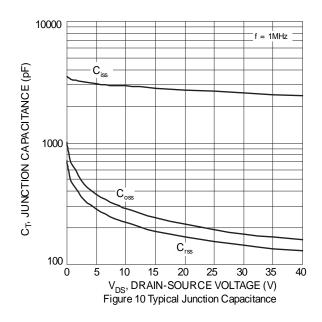
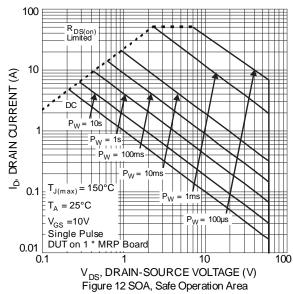
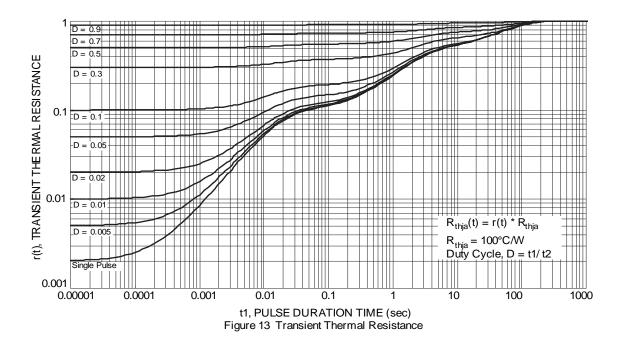


Figure 8 Gate Threshold Variation vs. Ambient Temperature



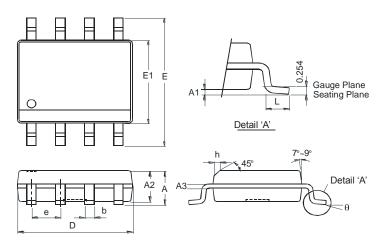






### **Package Outline Dimensions**

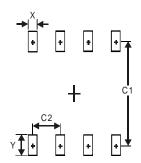
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SO-8				
Dim	Min	Max		
Α	=	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
A3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	<b>e</b> 1.27 Typ			
h	-	0.35		
L	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				

### **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27



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