

Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	-20	V
Gate-Source Voltage		V _{GSS}	±12	V
Drain Current (Note 5) Continuous	T _A = +25°C T _A = +70°C	ID	-3.0 -2.4	A
Pulsed Drain Current (Note 6)		I _{DM}	-15	A
Body-Diode Continuous Current (Note 5)		Is	-2.0	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	1.4	W
Thermal Resistance, Junction to Ambient (Note 5); Steady-State	$R_{ extsf{ heta}JA}$	90	°C/W
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
STATIC PARAMETERS						·	
Drain-Source Breakdown Voltage	BV _{DSS}	-20			V	$I_D = -250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I _{DSS}	_	_	-1	μA	V_{DS} = -20V, V_{GS} = 0V	
Gate-Body Leakage Current	I _{GSS}	_	_	±100	nA	$V_{DS} = 0V, V_{GS} = \pm 12V$	
Gate Threshold Voltage	V _{GS(TH)}	-0.6	_	-1.25	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	
On State Drain Current (Note 7)	I _{D(ON)}	-15			А	$V_{GS} = -4.5V, V_{DS} = -5V$	
		_	51	75	mΩ	$V_{GS} = -4.5V, I_D = -3.5A$	
Static Drain-Source On-Resistance (Note 7)	R _{DS(ON)}		87	110		$V_{GS} = -2.7V, I_D = -3.0A$	
			99	125		$V_{GS} = -2.5V, I_D = -2.6A$	
Forward Transconductance (Note 7)	g fs		7.3	—	S	$V_{DS} = -10V, I_D = -3.0A$	
Diode Forward Voltage (Note 7)	V _{SD}	_	-0.79	-1.26	V	$I_{S} = -1.7A, V_{GS} = 0V$	
Maximum Body-Diode Continuous Current (Note 5)	Is	_		1.7	А		
DYNAMIC PARAMETERS (Note 8)							
Total Gate Charge	Qg	_	7.3	_	nC	V_{GS} = -4.5V, V_{DS} = -10V, I_D = -3.0A	
Gate-Source Charge	Q _{gs}	_	2.0	_	nC	V_{GS} = -4.5V, V_{DS} = -10V, I_D = -3.0A	
Gate-Drain Charge	Q _{gd}	_	1.9	_	nC	V_{GS} = -4.5V, V_{DS} = -10V, I_D = -3.0A	
Turn-On Delay Time	t _{D(ON)}	_	12		ns	$V_{DS} = -10V, V_{GS} = -4.5V,$ $R_{L} = 10\Omega, R_{G} = 6\Omega$	
Turn-On Rise Time	t _R	_	20		ns		
Turn-Off Delay Time	t _{D(OFF)}	_	38	—	ns		
Turn-Off Fall Time	tF	_	41		ns		
Input Capacitance	Ciss	_	443	_	pF	V _{DS} = -16V, V _{GS} = 0V - f = 1.0MHz	
Output Capacitance	C _{oss}	_	128		pF		
Reverse Transfer Capacitance	C _{rss}	_	101	_	pF		

 5. Device mounted on 1"x1", FR-4 PC board with 2 oz. copper and test pulse width t ≤10s.
6. Repetitive Rating, pulse width limited by junction temperature.
7. Test pulse width t = 300μs.
8. Guaranteed by design. Not subject to production testing. Notes:















DMP2130L

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23					
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
К	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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