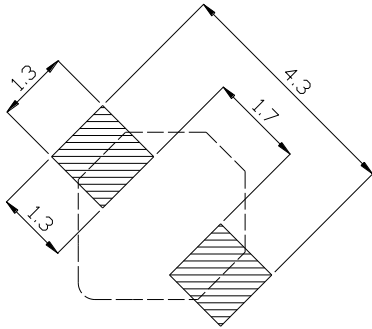


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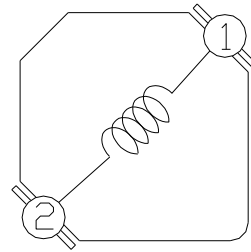
CDRH2D18B/HP



Reference Land pattern – [mm]



Connection



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Electrical Characteristics

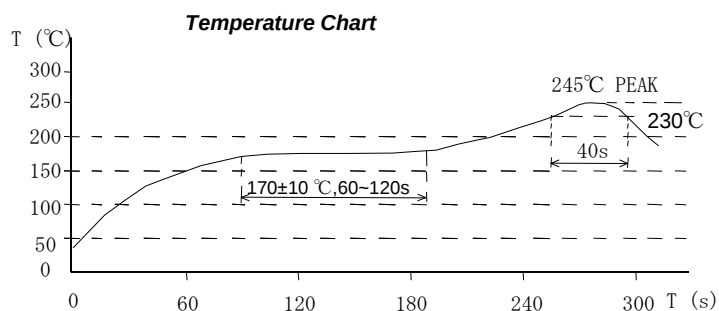
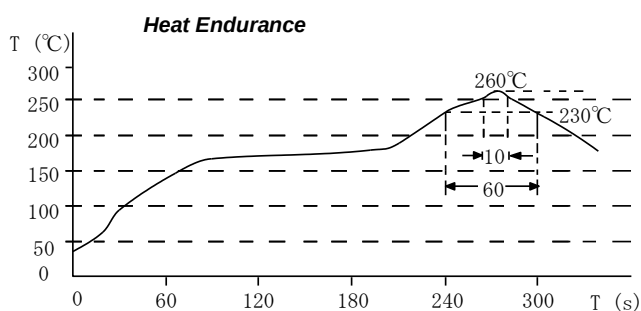
Part No.	Stamp	Inductance [within] (μ H) ※1	D.C.R. [Within] (m Ω) (at 20°C) Max. (Typ.)	Saturation Current (A) Max. (Typ.) ※2		Temperature Rise Current (A) Max. (Typ.) ※3
				at 20°C	at 100°C	
CDRH2D18B/HPNP-1R3NC	A	1.3 $\pm$ 25%	66 (53)	2.20	1.80	(1.80)
CDRH2D18B/HPNP-2R2NC	C	2.2 $\pm$ 25%	88 (70)	1.70	1.50	(1.48)
CDRH2D18B/HPNP-3R3NC	E	3.3 $\pm$ 25%	103 (82)	1.32	1.10	(1.30)
CDRH2D18B/HPNP-4R7NC	G	4.7 $\pm$ 25%	163 (130)	1.20	1.05	(1.00)
CDRH2D18B/HPNP-6R8NC	I	6.8 $\pm$ 25%	241 (193)	1.05	0.90	(0.80)
CDRH2D18B/HPNP-100MC	K	10 $\pm$ 20%	306 (245)	0.88	0.76	(0.70)
CDRH2D18B/HPNP-150MC	M	15 $\pm$ 20%	536 (429)	0.70	0.60	(0.48)
CDRH2D18B/HPNP-220MC	P	22 $\pm$ 20%	658 (526)	0.58	0.50	(0.45)

※1 Inductance measuring condition: at 100 kHz.

※2 Saturation current: The value of D.C. current when the inductance decreases to 65% of it's nominal value.

※3 Temperature rise current: The value of D.C. current when the temperature rise is $\Delta t=40^{\circ}\text{C}$ ($T_a=20^{\circ}\text{C}$).

Solder Reflow Condition



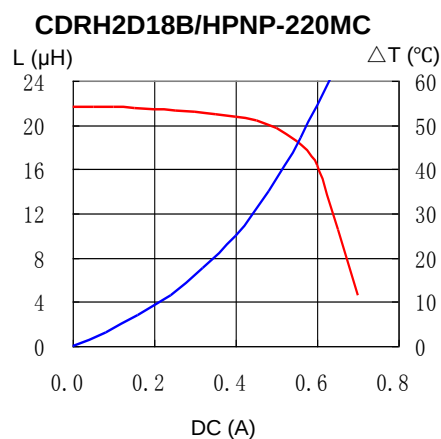
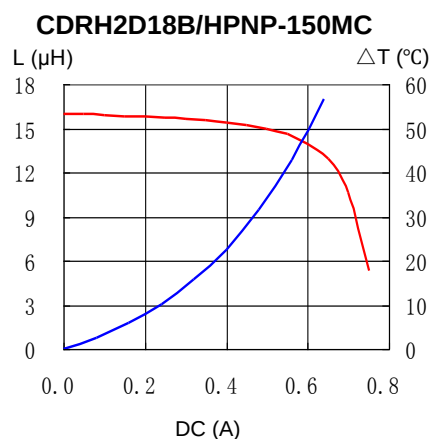
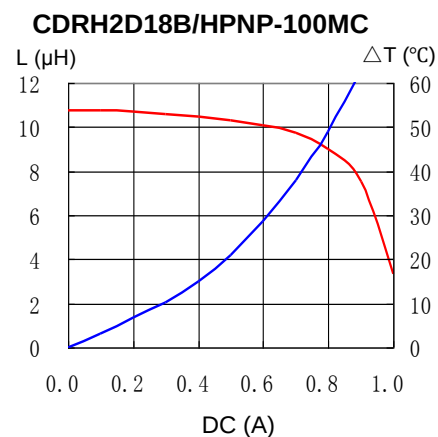
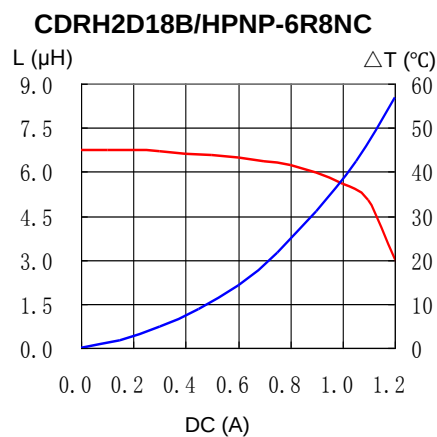
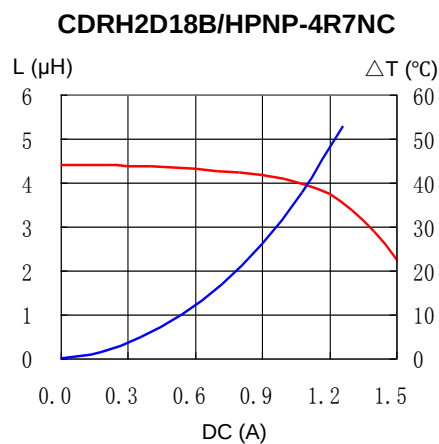
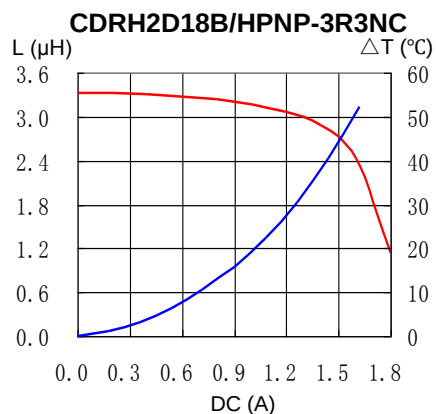
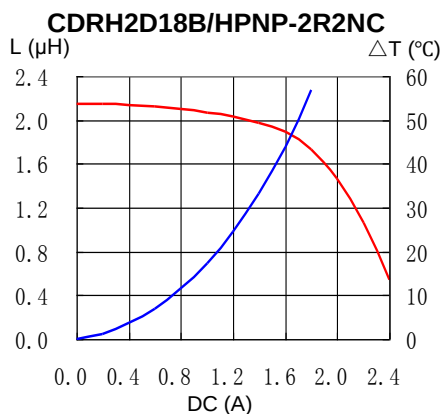
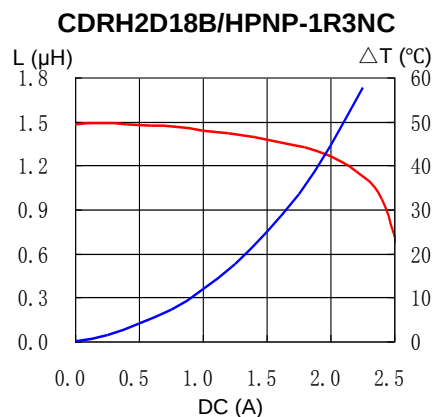
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Saturation Current & Temperature Rise Graph — L (20°C) — ΔT —



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