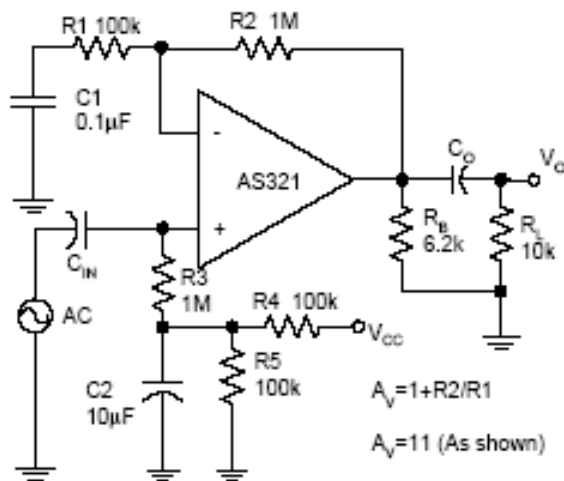
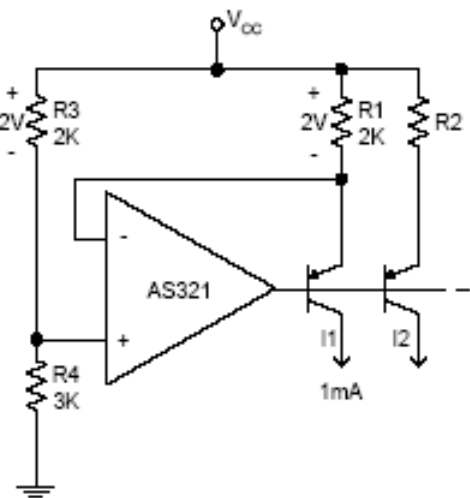


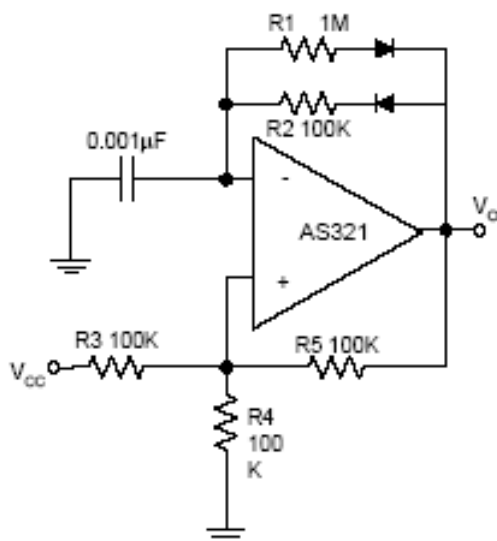
# Typical Applications Circuit (Cont.)



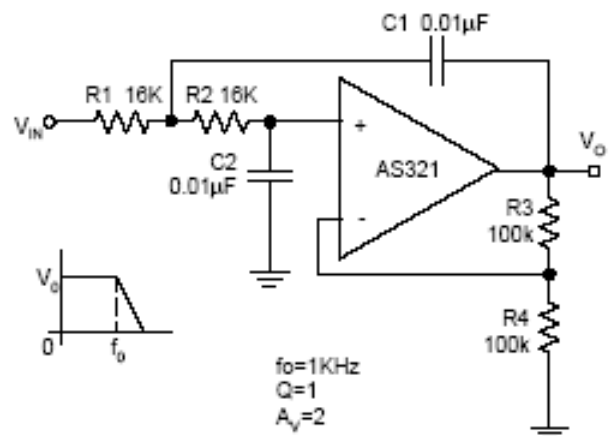
AC Coupled Non-Inverting Amplifier



Fixed Current Sources

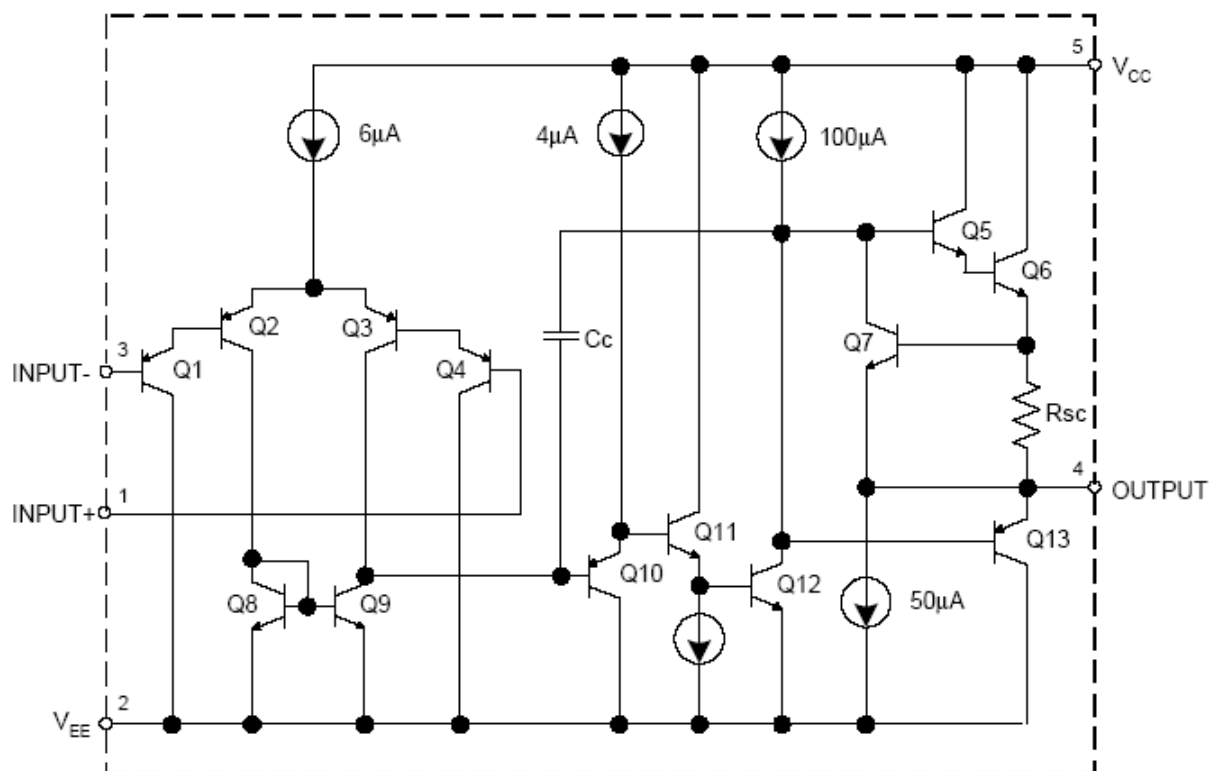


Pulse Generator



DC Coupled Low-Pass Active Filter

## Functional Block Diagram



## Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rating	Unit
$V_S (V_{CC}-V_{EE})$	Power Supply Voltage	40	V
$V_{ID}$	Differential Input Voltage	40	V
$V_{IN}$	Input Voltage	-0.3 to 40	V
$\theta_{JA}$	Thermal Resistance to Ambient	260	°C/W
$T_J$	Operating Junction Temperature	+150	°C
$T_{STG}$	Storage Temperature Range	-65 to +150	°C
$T_{LEAD}$	Lead Temperature (Soldering, 10 Seconds)	+260	°C

Note 4: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

## Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
$V_{CC}$	Supply Voltage	3	36	V
$T_A$	Ambient Operating Temperature Range	-40	+85	°C

**Electrical Characteristics** (Limits in standard typeface are for  $T_A = +25^\circ\text{C}$ , **bold** typeface applies over  $-40^\circ\text{C}$  to  $+85^\circ\text{C}$  (Note 5),  $V_{CC} = 5\text{V}$ ,  $V_{EE} = 0\text{V}$ ,  $V_O = 1.4\text{V}$ , unless otherwise specified.)

Symbol	Parameter		Conditions	Min	Typ	Max	Unit
V <sub>IO</sub>	Input Offset Voltage		V <sub>O</sub> = 1.4V, R <sub>S</sub> = 0Ω, V <sub>CC</sub> = 5V to 30V (Note 6)	—	2	5	mV
				—	—	7	
ΔV <sub>IO</sub> /ΔT	Average Temperature Coefficient of Input Offset Voltage		T <sub>A</sub> = -40°C to +85°C	—	7	—	μV/°C
I <sub>BIAS</sub>	Input Bias Current		I <sub>IN+</sub> or I <sub>IN-</sub> , V <sub>CM</sub> = 0V	—	20	100	nA
				—	—	200	
I <sub>IO</sub>	Input Offset Current		I <sub>IN+</sub> - I <sub>IN-</sub> , V <sub>CM</sub> = 0V	—	5	30	nA
				—	—	100	
V <sub>CM</sub>	Input Common Mode Voltage Range (Note 7)		V <sub>CC</sub> = 30V, CMRR ≥ 50dB	0	—	V <sub>CC</sub> -1.5	V
I <sub>CC</sub>	Supply Current		R <sub>L</sub> = ∞, V <sub>CC</sub> = 5V	—	0.35	0.80	mA
				—	0.45	1.0	
			R <sub>L</sub> = ∞, V <sub>CC</sub> = 30V	—	0.45	1.2	
				—	0.65	1.5	
G <sub>V</sub>	Large Signal Voltage Gain		V <sub>CC</sub> = 15V, V <sub>O</sub> = 1V to 11V, R <sub>L</sub> ≥ 2kΩ	85	100	—	dB
				80	—	—	
CMRR	Common Mode Rejection Ratio		V <sub>CM</sub> = 0V to (V <sub>CC</sub> -1.5)V, R <sub>S</sub> ≤ 10kΩ	60	70	—	dB
				60	—	—	
PSRR	Power Supply Rejection Ratio		V <sub>CC</sub> = 5V to 30V, R <sub>S</sub> ≤ 10kΩ	70	100	—	dB
				60	—	—	
I <sub>SOURCE</sub>	Output Current	Source	V <sub>IN+</sub> = 1V, V <sub>IN-</sub> = 0V, V <sub>CC</sub> = 15V, V <sub>O</sub> = 2V	20	40	—	mA
				20	—	—	
I <sub>SINK</sub>		Sink	V <sub>IN+</sub> = 0V, V <sub>IN-</sub> = 1V, V <sub>CC</sub> = 15V, V <sub>O</sub> = 2V	10	15	—	mA
				5	—	—	
			V <sub>IN+</sub> = 0V, V <sub>IN-</sub> = 1V, V <sub>CC</sub> = 15V, V <sub>O</sub> = 0.2V	12	50	—	μA
I <sub>SC</sub>		Output Short Circuit Current to Ground		V <sub>CC</sub> = 15V	—	40	60
V <sub>OH</sub>	Output Voltage Swing		V <sub>CC</sub> = 30V, R <sub>L</sub> = 2kΩ	26	—	—	V
				26	—	—	
			V <sub>CC</sub> = 30V, R <sub>L</sub> = 10kΩ	27	28	—	
				27	—	—	
V <sub>OL</sub>			V <sub>CC</sub> = 5V, R <sub>L</sub> = 10kΩ	—	5	20	mV
				—	—	30	
THD	Total Harmonic Distortion		f = 1kHz, AV = 20dB, R <sub>L</sub> = 2kΩ, V <sub>O</sub> = 2Vp-p, C <sub>L</sub> = 100pF, V <sub>CC</sub> = 30V	—	0.015	—	%
Φ <sub>M</sub>	Phase Margin		—	—	60	—	Deg
θ <sub>JC</sub>	Thermal Resistance (Junction to Case)		SOT25	—	101	—	°C/W

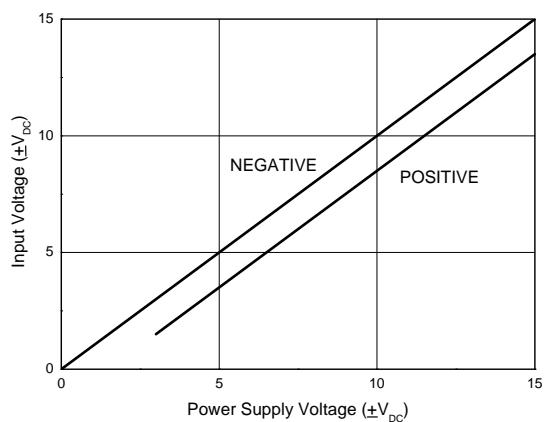
Notes: 5. Limits over the full temperature are guaranteed by design, but not tested in production.

6. Over the full input common-mode range  $0\text{V}$  to  $V_{CC}-1.5\text{V}$  (at  $+25^\circ\text{C}$ ).

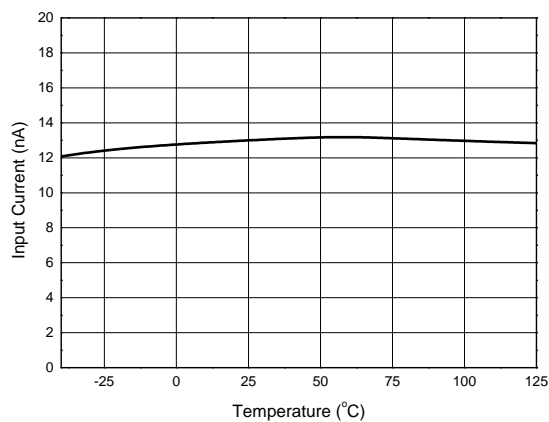
7. The input common-mode voltage of either input signal voltage should not be allowed to go negatively by more than  $0.3\text{V}$  (at  $+25^\circ\text{C}$ ). The upper end of the common-mode voltage range is  $V_{CC}-1.5\text{V}$  (at  $+25^\circ\text{C}$ ), but either or both inputs can go to  $+36\text{V}$  without damages, independent of the magnitude of the  $V_{CC}$ .

## Performance Characteristics

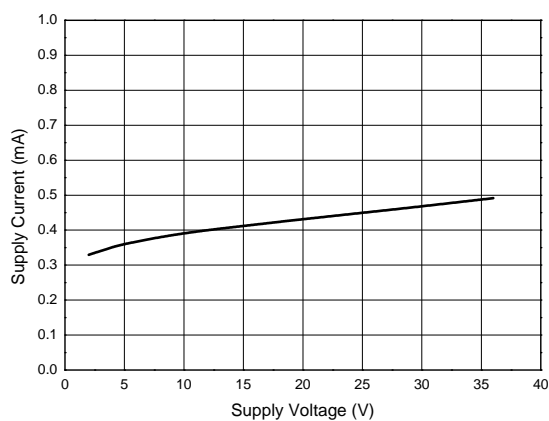
Input Voltage Range



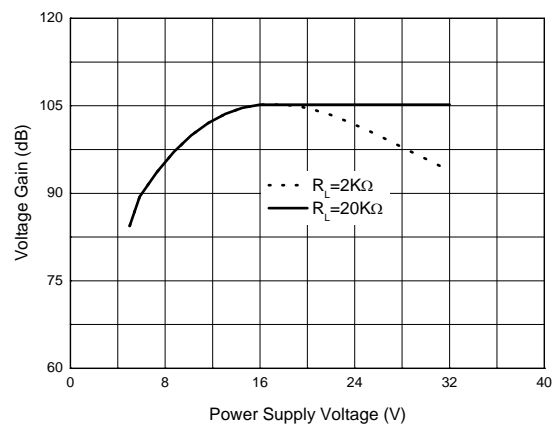
Input Current



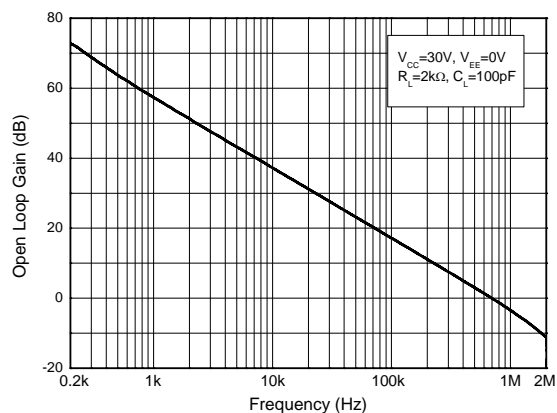
Supply Current



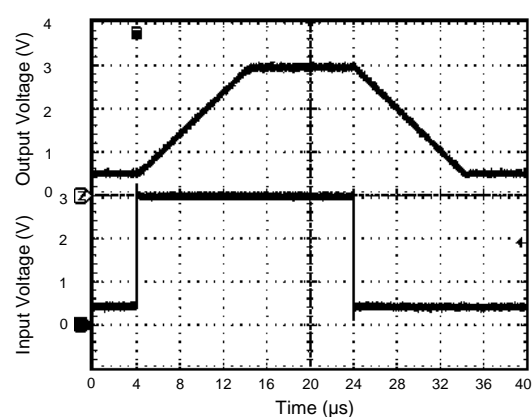
Voltage Gain



Open Loop Gain vs. Frequency

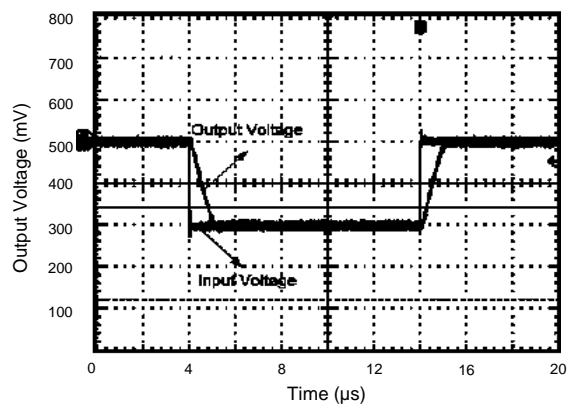


Voltage Follower Pulse Response

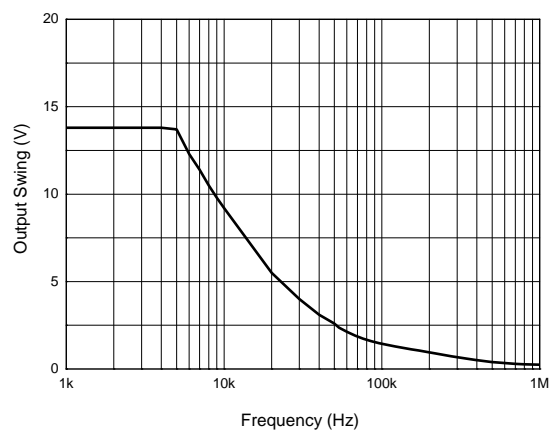


## Performance Characteristics (Cont.)

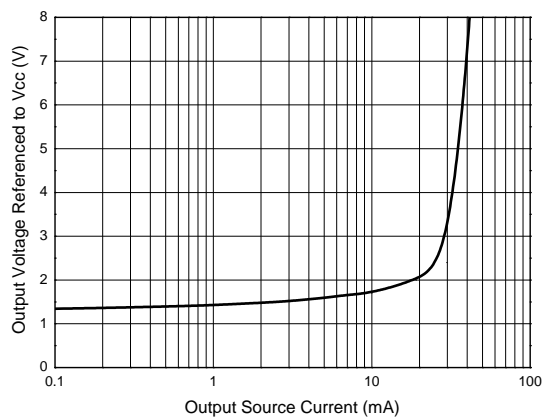
Voltage Follower Pulse Response (Small Signal)



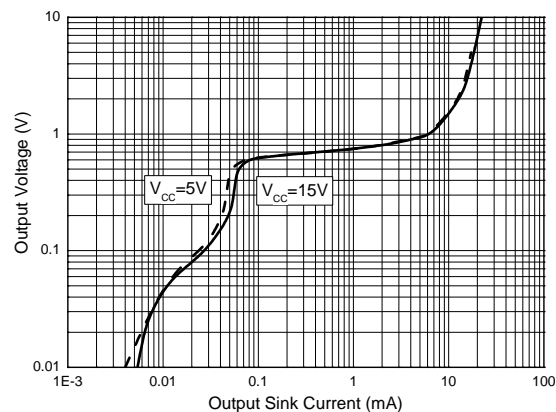
Large Signal Frequency Response



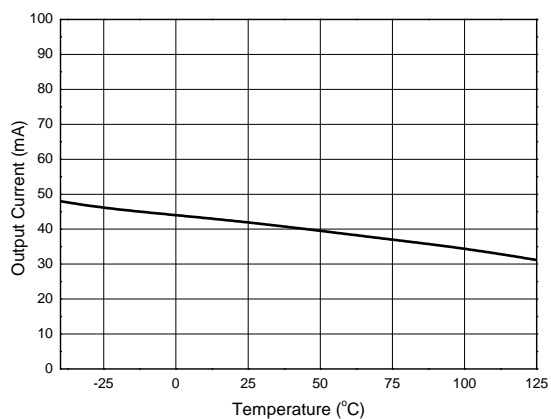
Output Characteristics: Current Sourcing



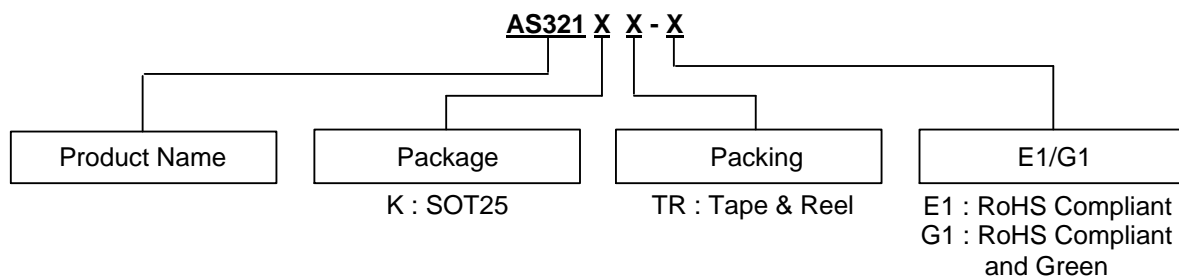
Output Characteristics: Current Sinking



Current Limiting



## Ordering Information



Part Number	Package (Note 9)	RoHS Compliant Lead Free/ Green	Temperature Range	Marking ID	Tape and Reel Quantity	Status (Note 8)	Alternative
AS321KTR-E1	SOT25	Lead Free	-40 to +85°C	E6T	3000	NRND	AS321KTR-G1
AS321KTR-G1	SOT25	Green	-40 to +85°C	G6T	3000	In Production	—

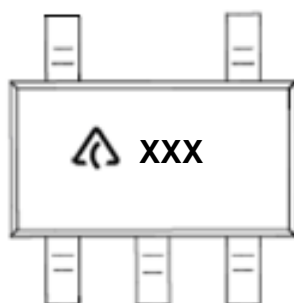



Notes: 8. AS321KTR-E1 (Lead Free package) is Not Recommended for New Design (NRND), recommended alternative is AS321KTR-G1 (Green package).  
9. For packaging details, go to our website at: <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information

(1) SOT25

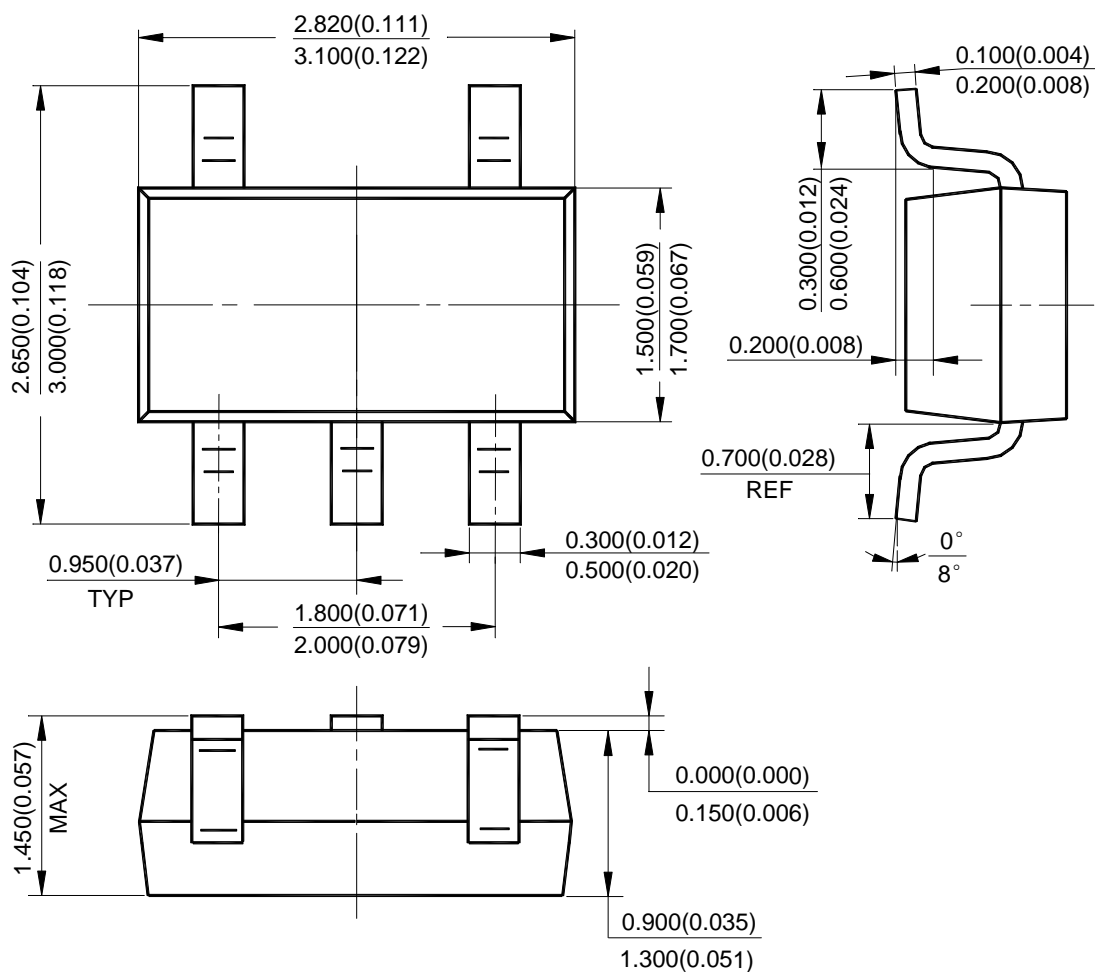
(Top View)



 : Logo  
XXX : Marking ID (See Ordering Information)

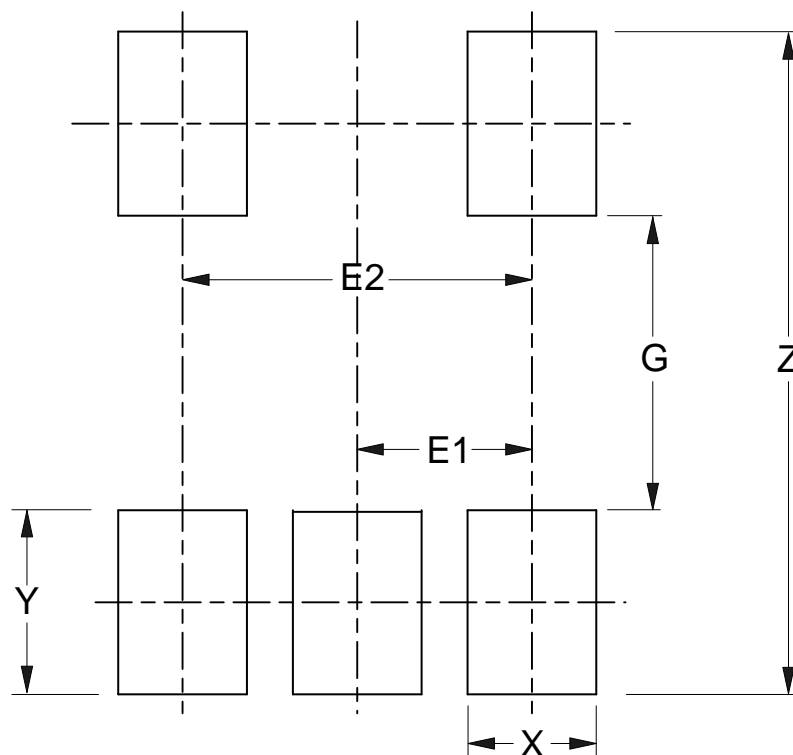
**Package Outline Dimensions** (All dimensions in mm(inch).)

(1) Package Type: SOT25



## Suggested Pad Layout

(1) Package Type: SOT25



Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E1 (mm)/(inch)	E2 (mm)/(inch)
Value	3.600/0.142	1.600/0.063	0.700/0.028	1.000/0.039	0.950/0.037	1.900/0.075



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