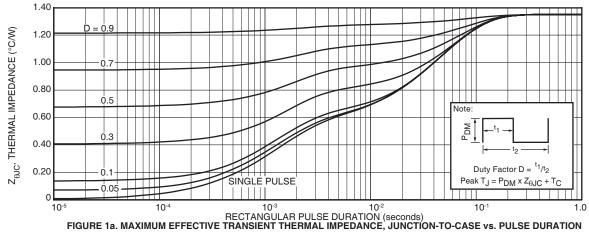
DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
t _{rr}	Reverse Recovery Time $I_F = 1A$, $di_F/dt = -100A/\mu s$, $V_R = 30V$, $T_J = 25$ °C		-	15		200
t _{rr}	Reverse Recovery Time	$I_F = 15A$, $di_F/dt = -200A/\mu s$ $V_R = 400V$, $T_C = 25^{\circ}C$	1	19		ns
Q _{rr}	Reverse Recovery Charge		-	21		nC
I _{RRM}	Maximum Reverse Recovery Current		-	2	-	Amps
t _{rr}	Reverse Recovery Time	$I_F = 15A$, $di_F/dt = -200A/\mu s$ $V_R = 400V$, $T_C = 125$ °C	-	105		ns
Q _{rr}	Reverse Recovery Charge		-	250		nC
I _{RRM}	Maximum Reverse Recovery Current		-	5	-	Amps
t _{rr}	Reverse Recovery Time	$I_F = 15A$, $di_F/dt = -1000A/\mu s$ $V_R = 400V$, $T_C = 125^{\circ}C$	-	55		ns
Q _{rr}	Reverse Recovery Charge		-	420		nC
I _{RRM}	Maximum Reverse Recovery Current		-	15		Amps

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction-to-Case Thermal Resistance			1.35	°C/W
W _T	Package Weight		0.22		oz
			5.9		g
Torque	Maximum Mounting Torque			10	lb•in
				1.1	N•m

Microsemi reserves the right to change, without notice, the specifications and information contained herein.



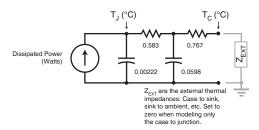
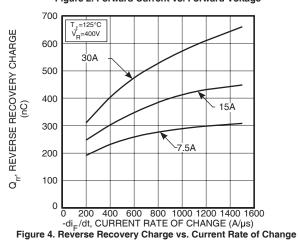
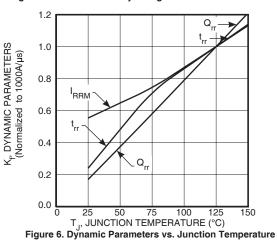
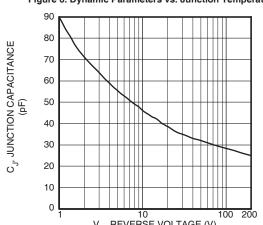


FIGURE 1b, TRANSIENT THERMAL IMPEDANCE MODEL







V_R, REVERSE VOLTAGE (V)
Figure 8. Junction Capacitance vs. Reverse Voltage

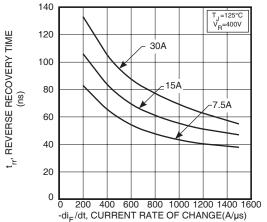
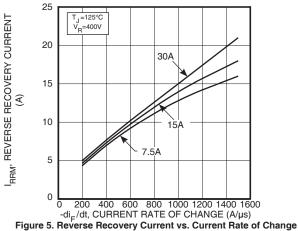


Figure 3. Reverse Recovery Time vs. Current Rate of Change



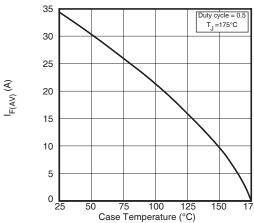


Figure 7. Maximum Average Forward Current vs. CaseTemperature

0.25 I_{RRM}

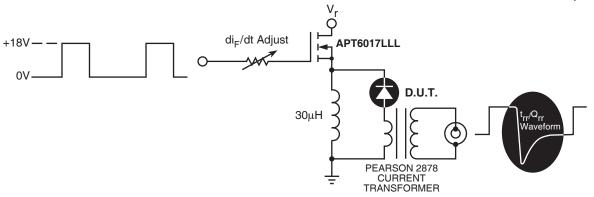


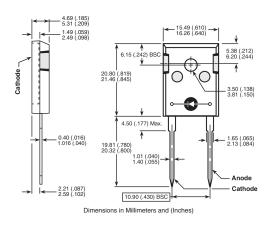
Figure 9. Diode Test Circuit

Zero

- 1 I_F Forward Conduction Current
- 2 di_F/dt Rate of Diode Current Change Through Zero Crossing.
- 3 I_{RRM} Maximum Reverse Recovery Current.
- 4 t_{rr} Reverse Recovery Time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through I_{RRM} and 0.25•I_{RRM} passes through zero.
- 5 Q_{rr} Area Under the Curve Defined by I_{RRM} and t_{rr}.

Figure 10, Diode Reverse Recovery Waveform and Definitions

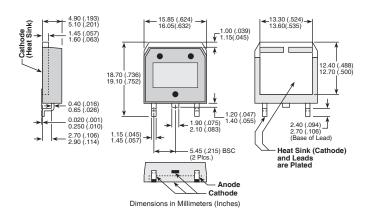
TO-247 Package Outline



D³PAK Package Outline

5

@3 100% Sn







Microsemi Headquarters

One Enterprise, Aliso Viejo,
CA 92656 USA
Within the USA: +1 (800) 713-4113
Outside the USA: +1 (949) 380-6100
Sales: +1 (949) 380-6136
Fax: +1 (949) 215-4996
Email: sales.support@microsemi.com

© 2019 Microsemi. All rights reserved. Microsemi and the Microsemi logo are trademarks of Microsemi Corporation. All other trademarks and service marks are the property of their respective owners.

Microsemi makes no warranty, representation, or guarantee regarding the information contained herein or the suitability of its products and services for any particular purpose, nor does Microsemi assume any liability whatsoever arising out of the application or use of any product or circuit. The products sold hereunder and any other products sold by Microsemi have been subject to limited testing and should not be used in conjunction with mission-critical equipment or applications. Any performance specifications are believed to be reliable but are not verified, and Buyer must conduct and complete all performance and other testing of the products, alone and together with, or installed in, any end-products. Buyer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the Buyer's responsibility to independently determine suitability of any products and to test and verify the same. The information provided by Microsemi hereunder is provided "as is, where is" and with all faults, and the entire risk associated with such information is entirely with the Buyer. Microsemi does not grant, explicitly or implicitly, to any party any patent rights, licenses, or any other IP rights, whether with regard to such information itself or anything described by such information. Information provided in this document is proprietary to Microsemi, and Microsemi reserves the right to make any changes to the information in this document or to any products and services at any time without notice.

Microsemi, a wholly owned subsidiary of Microchip Technology Inc. (Nasdaq: MCHP), offers a comprehensive portfolio of semiconductor and system solutions for aerospace & defense, communications, data center and industrial markets. Products include high-performance and radiation-hardened analog mixed-signal integrated circuits, FPGAs, SoCs and ASICs; power management products; timing and synchronization devices and precise time solutions, setting the world's standard for time; voice processing devices; RF solutions; discrete components; enterprise storage and communication solutions; security technologies and scalable anti-tamper products; Ethernet solutions; Power-over-Ethernet ICs and midspans; as well as custom design capabilities and services. Microsemi is headquartered in Aliso Viejo, California, and has approximately 4,800 employees globally. Learn more at www microsemi.com.