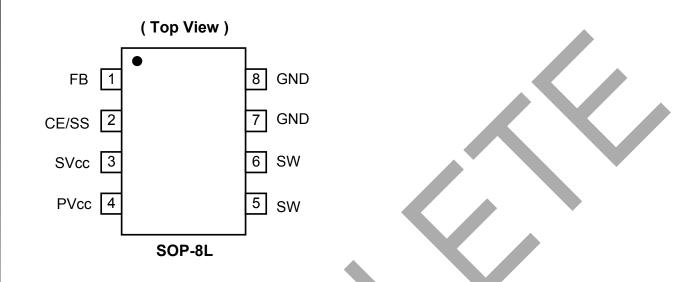


## **Pin Assignments**

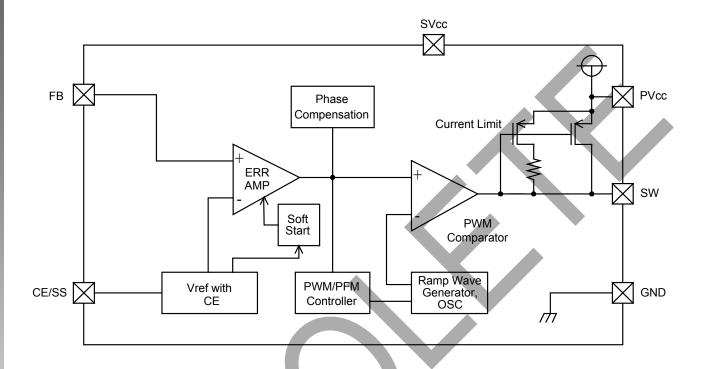


## **Pin Descriptions**

Pin Name	Pin No.	Description		
FB	1	Feedback pin		
CE/SS	2	Chip Enable/ Soft Start: H: Enable L: Disable		
SVcc	3	IC signal power supply pin, add a $20\Omega$ resistor to PVcc and a $0.1\mu F$ capacitor to GND.		
PVcc	4	IC power supply pin		
SW	5/6	Switch Pin. Connect external inductor/diode here. Minimize trace area at this pin to reduce EMI.		
GND	7/8	GND Pin		



## **Block Diagram**



#### Absolute Maximum Ratings (TA=25°C)

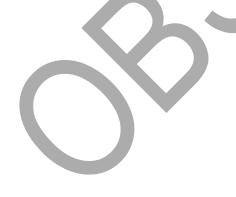
Symbol	Parameter	Ratings	Units
V <sub>CC</sub> /SV <sub>CC</sub>	V <sub>IN</sub> Pin Voltage	-0.3 ~ 5.0	V
$V_{SW}$	SW Pin Voltage	-0.3 ~ V <sub>IN</sub> +0.3	V
$V_{FB}$	FB Pin Voltage	-0.3 ~ V <sub>IN</sub> +0.3	V
V <sub>CE/SS</sub>	CE/SS Pin Voltage	-0.3 ~ V <sub>IN</sub> +0.3	V
PD	Continuous Total Power Dissipation	Internal limited	
T <sub>OPR</sub>	Operating Ambient Temperature	-25 ~ +80	°C
$T_{STG}$	Storage Temperature	-40 ~ +125	°C



#### **Electrical Characteristics**

 $V_{IN}$ =5V,  $V_{OUT}$ =2V, Load=300mA, TA=25°C

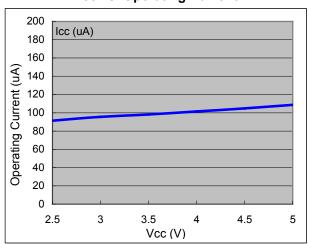
Symbol	Parameter	Conditions	Min	Тур.	Max	Units
$V_{FB}$	FB		0.975	1.0	1.025	V
$V_{IN}$	Input Voltage		2.2	-	5	V
	Line Regulation	V <sub>IN</sub> =2.2~5V, Load=10mA	-	_	0.12	%
	Load Regulation	I <sub>OUT</sub> =10~1200mA	-	-	1.2	%
V	UVLO Voltage (min.	// valtage required to registein [] et //	-	-	2	V
	operating voltage)	$V_{\text{CC}}$ , voltage required to maintain H at $V_{\text{OUT}}$				
I <sub>CC</sub>	Operating Current	CE/SS=V <sub>IN</sub> , No Load	1	100	150	μΑ
I <sub>ccq</sub>	Supply Current	No external components, CE/SS=V <sub>IN</sub> , V <sub>FB</sub> =1.2V	-	90	120	μA
I <sub>STB</sub>	Stand-by Current	No external components, CE/SS=0V, V <sub>FB</sub> =0V	-	6	-	μΑ
I <sub>CL</sub>	Current Limit	Peak current V <sub>IN</sub> =5V, V <sub>OUT</sub> =2V	1200	1400	1600	mA
Fosc	Oscillator Frequency	Load=300mA, V <sub>IN</sub> =5V, V <sub>OUT</sub> =2V	500	700	-	kHz
MAXDTY	Maximum Duty Ratio		85	90	-	%
PFMDTY	PFM Duty Ratio	No load	15	25	35	%
V <sub>CEH</sub>	CE/SS "High" Voltage	Apply 1.4V (min.) to CE/SS, determine V <sub>OUT</sub> "High"	1.4	-	-	V
V <sub>CEL</sub>	CE/SS "Low" Voltage	Same as V <sub>CEH</sub> , determine V <sub>OUT</sub> /"Low"	-	-	0.6	V
EFFI	Efficiency	V <sub>CC</sub> =5V, V <sub>OUT</sub> =3.3V, Load=300mA		93	-	%
Rdson	Rdson Condition	I <sub>OUT</sub> =300mA, V <sub>IN</sub> =5V, V <sub>OUT</sub> =2V	-	350	450	mΩ



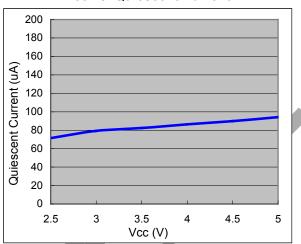


### **Typical Performance Characteristics**

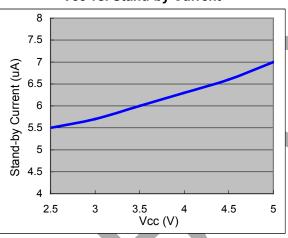
Vcc vs. Operating Current



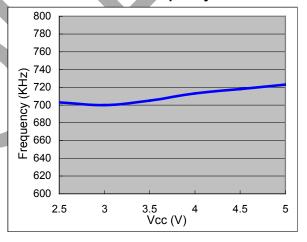
Vcc vs. Quiescent Current



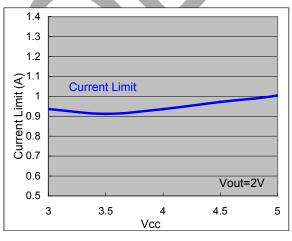
Vcc vs. Stand-by Current



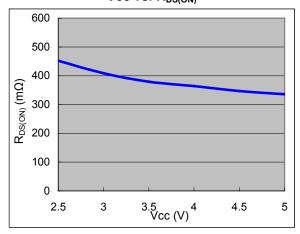
Vcc vs. Frequency



Vcc vs. Current Limit



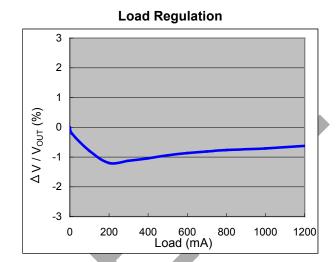
Vcc vs. R<sub>DS(ON)</sub>



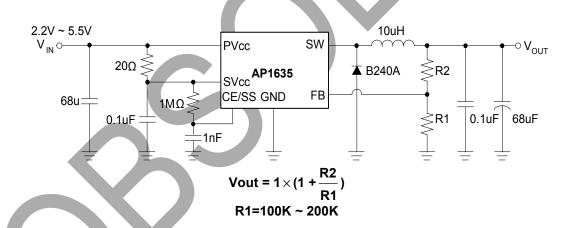


#### Typical Performance Characteristics (Continued)

#### 



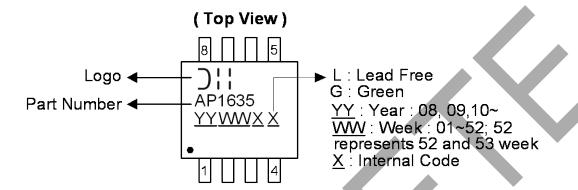
## **Typical Application Circuit**





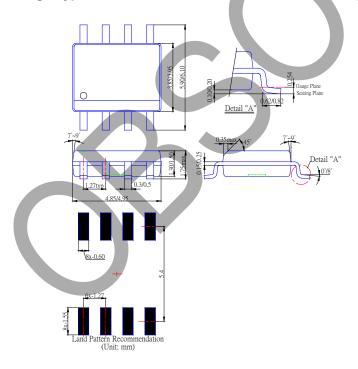
## Marking Information

(1) SOP-8L



#### Package Information (All Dimensions in mm)

(1) Package Type: SOP-8L





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