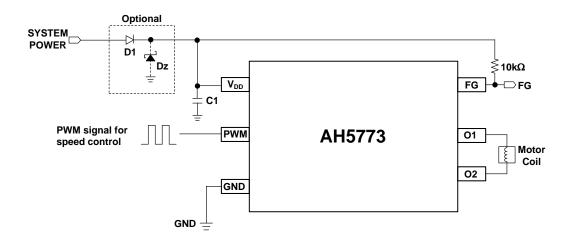


Typical Applications Circuit



Notes:

- 4. C1 is for power stabilization and to strengthen the noise immunity; the recommended value is 2.2μF. The value of capacitor can be optimized depending on the operating mode, motor voltage and the motor current. For PWM speed control mode, with datasheet current capability, the recommended capacitor value is 2.2μF, for lower motor current 1μF and higher should be used. If PWM speed control function is not used (PWM pin tied high or not connected) the capacitor value can be reduced towards 0.1μF. The value of the C1 should be checked in the motor design in its operating conditions if it is reduced from the recommended value of 2.2μF.
- 5. The AH5773 has an open-drain tachometer FG output that follows the magnetic change frequency. Typically a pull-up resistor of 10kΩ is recommended from FG pin to the supply voltage.

Pin Descriptions

Package Type: MSOP-8EP

| Pin Number | Pin Name | Description |
|------------|-----------------|---|
| 1 | FG | Frequency Generator (FG) - The FG output is the same as the magnetic change frequency |
| 2 | PWM | PWM signal input pin for speed control |
| 3 | O2 | Output drive sourcing & sinking pin |
| 4 | GND | Ground pin |
| 5 | 01 | Output drive sourcing & sinking pin |
| 6 | V _{DD} | Power supply input pin |
| 7 | NC | No connection (Note 6) |
| 8 | NC | No connection (Note 6) |
| Pad | Pad | Exposed pad for thermal dissipation. It can be connected to GND or left open circuit. |

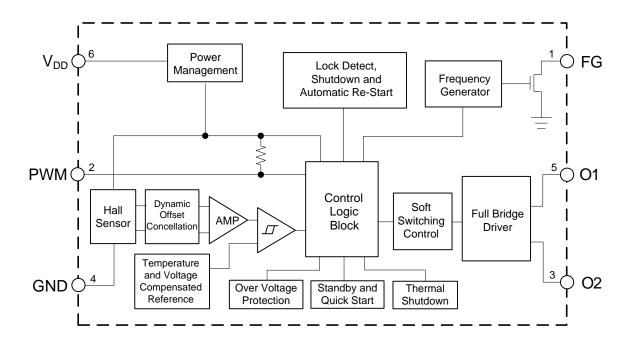
Note: 6. NC is "No Connection" pin and is not connected internally. This pin can be left open or tied to ground.

Package Type: U-DFN2020-6

| Pin Number | Pin Name | Description |
|------------|-----------------|---|
| 1 | PWM | PWM signal input pin for speed control |
| 2 | V _{DD} | Power supply input pin |
| 3 | O2 | Output drive sourcing & sinking pin |
| 4 | GND | Ground pin |
| 5 | 01 | Output drive sourcing & sinking pin |
| 6 | FG | Frequency Generator (FG) - The FG output is the same as the magnetic change frequency |
| Pad | Pad | Exposed pad for thermal dissipation. It can be connected to GND or left open circuit. |



Functional Block Diagram





Absolute Maximum Ratings (Note 7) (@TA = +25°C, unless otherwise specified.)

| Symbol | Charac | cteristics | Rating | Unit |
|----------------------|------------------------------------|--|-----------------|------|
| V _{DD_MAX} | Maximum Supply Voltage (Note 8) | | 24 | ٧ |
| V _{REVERSE} | Reverse Supply Voltage on All Pins | | -0.3 | V |
| V _{PWM_MAX} | Maximum Voltage on Logic PWM Pin | | 6 | V |
| I _{O(PEAK)} | Maximum Output Current (Peak) | | 1,000 | mA |
| В | Maximum Magnetic Flux Density | | Unlimited | _ |
| Р | Power Dissipation (Note 9) | MSOP-8EP | 2,180 (Note 10) | mW |
| P_{D} | Fower Dissipation (Note 9) | U-DFN2020-6 | 1,230 (Note 11) | mW |
| T _{STG} | Storage Temperature Range | | -65 to +150 | °C |
| TJ | Maximum Junction Temperature | | +150 | ů |
| ESD HBM | Human Body Model ESD Capability | V _{DD} , O1, O2, PWM and GND pins | 4 | kV |
| ESD UDIVI | Human Body Woder ESD Capability | FG pin | 2 | kV |

Notes:

- 7. Stresses greater than the 'Absolute Maximum Ratings' specified above may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time.
- 8. The absolute maximum V_{DD} of 24V is a transient stress rating and is not meant as a functional operating condition. It is not recommended to operate the device at the absolute maximum rated conditions for any period of time.
- 9. For thermal de-rating curves under different PCB size and layout conditions, see thermal performance section.
- 10. MSOP-8EP exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on 2in x 2in two-layer 2oz copper FR4 PCB (1.6mm thickness) with four thermal vias in exposed PADs to the copper flood on the bottom layer. See thermal performance section.
- 11. U-DFN2020-6 exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on 1in x 1in two-layer 2oz copper FR4 PCB (1.6mm thickness) with two thermal vias in exposed PADs to the copper flood on the bottom layer. See thermal performance section.

Recommended Operating Conditions

| Symbol | Characteristic | Conditions | Min | Max | Unit |
|----------------|-----------------------------|------------|-----|------|------|
| V_{DD} | Supply Voltage | Operating | 2.4 | 18 | ٧ |
| T _A | Operating Temperature Range | Operating | -40 | +105 | °C |

Electrical Characteristics (Note 12) (@T_A = +25°C, V_{DD} = 12V, unless otherwise specified.)

| Symbol | Characteristics | Conditions | Min | Тур | Max | Unit |
|------------------------|--|--|-----------------------|-----------------------|------|------|
| I _{DD} | Supply Current | No Load, PWM switching or high | _ | 3.17 | 4.5 | mA |
| I _{DD_STNDBY} | Standby Supply Current | PWM pin = Logic Low (GND) for >65ms | _ | 100 | 1 | μA |
| Vov_th | Over Voltage Protection Threshold for Shutdown to Standby Mode | Voltage increasing | 19 | 21 | 23 | V |
| Vov_rlth | Over Voltage Release Threshold | Voltage decreasing | 18 | 20 | 22 | V |
| | | $V_{DD} = 12V, I_{OUT} = 500mA$ | V _{DD} -0.6 | V _{DD} -0.36 | - | V |
| V | Output Valtaga High | $V_{DD} = 12V$, $I_{OUT} = 500$ mA $T_A = -40$ °C to +105°C | V _{DD} -0.7 | V _{DD} -0.36 | 1 | V |
| V _{OH} | Output Voltage High | $V_{DD} = 3V, I_{OUT} = 300mA$ | V _{DD} -0.32 | V _{DD} -0.23 | - | V |
| | | $V_{DD} = 3V$, $I_{OUT} = 300$ mA $T_A = -40$ °C to +105°C | V _{DD} -0.40 | V _{DD} -0.23 | - | V |
| | | $V_{DD} = 12V, I_{OUT} = 500mA$ | - | 0.27 | 0.35 | V |
| Va | Output Valtaga Law | $V_{DD} = 12V$, $I_{OUT} = 500$ mA $T_A = -40$ °C to $+105$ °C | - | 0.27 | 0.5 | V |
| VoL | Output Voltage Low | $V_{DD} = 3V, I_{OUT} = 300mA$ | _ | 0.18 | 0.24 | V |
| | | $V_{DD} = 3V$, $I_{OUT} = 300$ mA $T_A = -40$ °C to $+105$ °C | _ | 0.18 | 0.32 | V |

Note: 12. Typical data is measured at T_A = +25°C, V_{DD} = 12V. The maximum and minimum parameter values over operating temperature range are not tested in production, they are guaranteed by design, characterization and process control.



Electrical Characteristics (continued) (Notes 13 & 14) (@T_A = +25°C, V_{DD} = 12V, unless otherwise specified.)

| Symbol | Characteristics | Conditions | Min | Тур | Max | Unit |
|-------------------------|---|--|------|-------|------|------|
| | | $V_{DD} = 12V, I_{OUT} = 500mA$ | _ | 1.26 | 1.9 | Ω |
| D | Combined NMOS and PMOS R _{DSON} | $V_{DD} = 12V$, $I_{OUT} = 500$ mA $T_A = -40$ °C to $+105$ °C | - | 1.26 | 2.4 | Ω |
| R _{ON_TOTAL} | Including Bond Wire Resistance | $V_{DD} = 3V$, $I_{OUT} = 300$ mA | _ | 1.37 | 1.87 | Ω |
| | | $V_{DD} = 3V$, $I_{OUT} = 300$ mA $T_A = -40$ °C to +105°C | _ | 1.37 | 2.4 | Ω |
| tsw | Output Soft Switch Time | 17Ω load on out1/out2 | _ | 200 | - | μs |
| t _{ON} | On Time - Lock Detect Time | _ | _ | 420 | ı | ms |
| R_{DR} | Duty Ratio - Lock Detect to Shutdown Time | toff/ton | _ | 10 | _ | _ |
| I _{LEAK} | FG Output Leakage Current | _ | _ | 0.005 | 1 | μA |
| V_{FGOL} | FG Output Voltage Low | $I_{FG} = 5mA$ | _ | _ | 0.4 | V |
| V _{PWMH} | PWM Input H Level | _ | 2.1 | _ | 5.5 | V |
| V_{PWML} | PWM Input L Level | _ | _ | _ | 8.0 | V |
| I _{PWML} | PWM Pin Current | PWM pin: V _{PWM} = 0 | _ | 15 | _ | μA |
| f _{PWM} | PWM Input Frequency Range | _ | 0.05 | _ | 50 | kHz |
| D _{PWM_MIN} | Output Duty Ratio Range | Input PWM frequency of 25kHz, no load (Note 14) | 10 | - | 100 | % |
| T _{J_SDN_TH} | IC Junction Temperature Thermal Shutdown Threshold | _ | _ | +170 | - | °C |
| T _{J_SDN_HYST} | IC Junction Temperature Thermal Shutdown Hysteresis | _ | _ | +25 | _ | °C |

Notes:

- 13. Typical data is measured at T_A = +25°C, V_{DD} = 12V. The maximum and minimum parameter values over operating temperature range are not tested in production. They are guaranteed by design, characterization and process control.
- 14. D_{PWM_MIN} is the device output PWM duty-range capability. The minimum PWM duty ratios need to start the motor turning or maintain the rotation of the motor, depending on the supply voltage to the motor and the electrical and mechanical design of the motor.

Magnetic Characteristics (Notes 15, 16 & 17) (@T_A = -40°C to +105°C, V_{DD} = 2.4V to 18V, unless otherwise specified.)

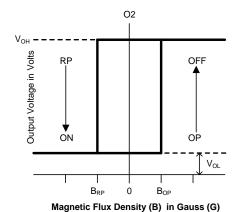
(1mT = 10 G)

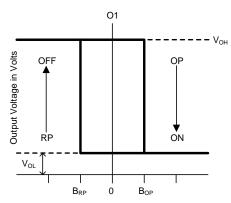
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|---|---------------|-----------------|-----|-----|-----|-------|
| B _{OP} (south pole to part marking side) | Operate Point | (Notes 15 & 17) | 5 | 20 | 35 | |
| B _{RP} (north pole to part marking side) | Release Point | (Notes 15 & 17) | -35 | -20 | -5 | Gauss |
| B _{HY} (B _{OP} -B _{RP}) | Hysteresis | ı | - | 40 | - | |

Notes:

- 15. Typical data is measured at T_A = +25°C, V_{DD} = 12V. The maximum and minimum parameter values over operating temperature range are not tested in production. They are guaranteed by design, process control and characterization.
- 16. Magnetic characteristics may vary with supply voltage, operating temperature and after soldering.
- 17. The peak amplitude of the rotating-motor magnetic-flux density at the sensor location should be greater than +/-70G.

Operating Characteristics

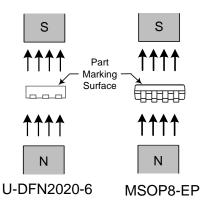


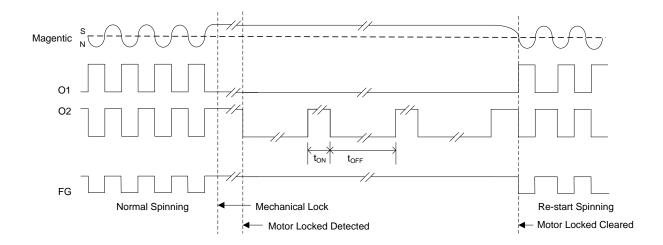


Magnetic Flux Density (B) in Gauss (G)



Operating Characteristics (continued) (Notes 18, 19, 20 & 21)





Truth Table

| O1 | O2 | PWM | FG |
|----|----|-------------|-------------|
| Н | L | Н | L |
| L | Н | Н | Н |
| L | L | L for >65ms | H (Note 21) |

Notes:

- 18. In "Normal Spinning", the FG changes its state at each edge of O1.
- 19. When the motor locks with South pole at the Hall element, O2 is kept on "L" and O1 is a clock with t_{ON}/t_{OFF} ratio. When motor locks with North pole at the Hall element, O1 is kept on "L", O2 is a clock with toN/toFF ratio.
- 20. When "Re-start spinning" occurs, the motor speed ramps up to the "Normal Spinning" speed from zero. Speed ramp-up profile depends on motor characteristics.

 21. In standby mode FG switch is turned off to save system power.



Application Note

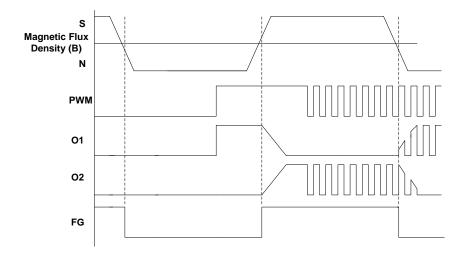
DC Supply Voltage Speed Control

Motor speed can be controlled by varying the V_{DD} supply voltage while PWM pin is tied to V_{DD} pin.

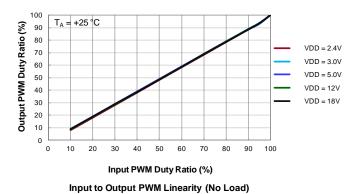
For example, with 12V nominal motor, changing supply voltage between 12V to 2.4V, speed can be reduced from 100% to 20% typically.

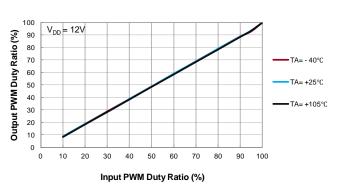
PWM Speed Control

Motor speed can also be adjusted by applying a PWM speed control signal into the PWM pin while keeping the V_{DD} pin at nominal motor voltage. The motor speed is proportional to the PWM signal duty. For example, with 12V nominal motor, V_{DD} pin is maintained at 12V typical while varying the PWM control signal duty to adjust the motor speed linearly. The figure below shows the output O1 and O2 in relation to PWM speed control signal at PWM pin.



Frequency of PWM speed-control signal can be between 50Hz to 50kHz. Recommended typical PWM signal frequency is 25kHz to keep switching frequency away from audible band. AH5773 has a very good input to output PWM linearity over the operating range for no load conditions (i.e. no motor coils connected to the output). When the motor coil is connected, the nonlinearity of coils and permanent magnet profile will introduce non-linearity to the motor speed against input PWM duty ratio.





Input to Output PWM Linearity (No Load)

If PWM signal level at PWM pin stays low for longer than 65ms typical, the outputs are disabled.

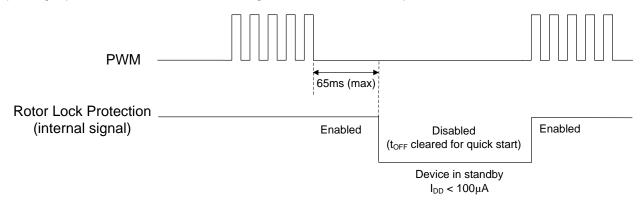
Depending on the motor design and its inertia, at nominal voltage (i.e. 12V for 12V motor), minimum start-up PWM duty required can be typically between 20% - 45% while minimum running PWM duty can be down to 10% - 25% typical. If voltage at V_{DD} is lower than the nominal motor voltage, both start-up PWM duty and minimum running PWM duty required will be higher.



Application Note (continued)

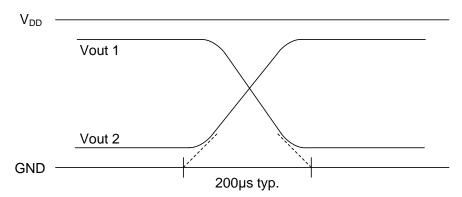
Standby Mode and Quick Start

When PWM signal input at PWM pin is low for longer than 65ms, internal rotor lock protection t_{OFF} is cleared and the device enters standby mode. In standby mode typical supply current is less than 100 μ A. This allows the device to enter motor start t_{ON} time on the next PWM high signal providing a quick start. When the device is enabled again, the Hall sensors take 100 μ s to stabilize.



Soft Switching

AH5773 uses soft switching of the motor coil current during commutation to minimize audible switching noise and electromagnetic interference (EMI) to provide a low noise solution.



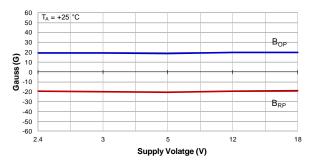
Overvoltage Shutdown of Output Drive

When the supply voltage reaches the over voltage shutdown threshold, V_{OV_TH} , the AH5773 shuts down all the output drive switches and enters standby mode to help prevent over-voltage stress on the coil.

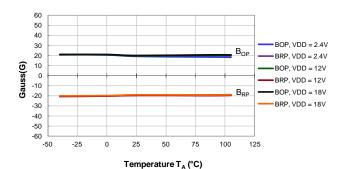


Typical Operating Characteristics

Typical Magnetic Operating Switch Points

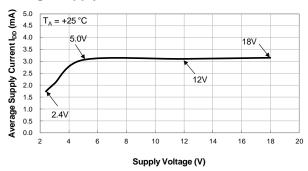


Magnetic Switch Points Bop & Brp vs. Supply Voltage

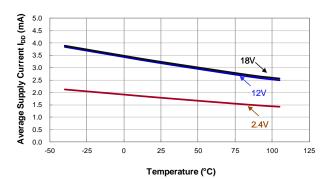


Magnetic Switch Points Bop & Brp vs. Temperature

Average Supply Current

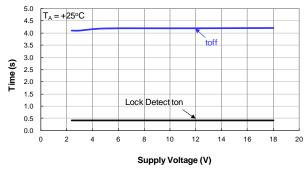


Average Supply Current vs. Supply Voltage

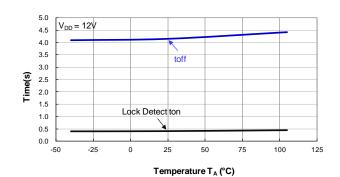


Average Supply Current vs. Temperature

Lock Detect ton and Shutdown toff Periods



Lock Detect Ton and Toff vs. Supply Voltage

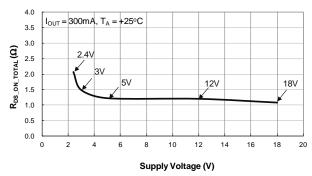


Lock Detect Ton and Toff vs. Temperature

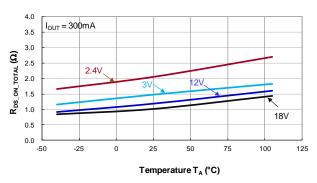


Typical Operating Characteristics (continued)

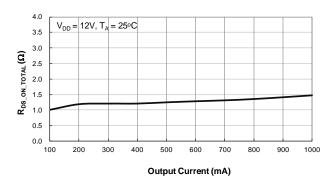
Total H-Bridge Path Resistance – Total R_{DSON} of High Side and Low Side Switches



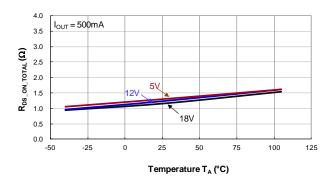
 $\label{eq:high+LowSide} \textbf{High+Low Side Resistance} \ R_{\text{DS_ON_TOTAL}} \ \text{vs. Supply Voltage}$



 $\label{eq:high+LowSide} \textbf{High+Low Side Resistance} \; \textbf{R}_{\textbf{DS_ON_TOTAL}} \; \textbf{vs.} \\ \textbf{Temperature}$



High+Low Side Resistance R_{DS ON TOTAL} vs. Current



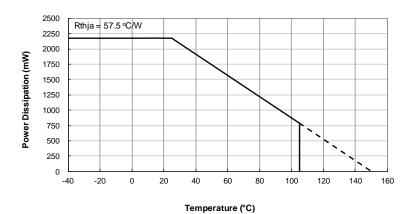
High+Low Side Resistance R_{DS_ON_TOTAL} vs. Temperature



Thermal Performance

MSOP-8EP Power Dissipation De-rating Curve 1 (Note 22)

| T _A (°C) | -40 | 0 | 25 | 50 | 60 | 70 | 80 | 85 | 90 | 95 | 100 | 105 | 110 | 120 | 125 | 130 | 140 | 150 |
|---------------------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| P _D (mW) | 2,180 | 2,180 | 2,180 | 1,744 | 1,570 | 1,395 | 1,221 | 1,134 | 1,046 | 959 | 872 | 785 | 698 | 523 | 436 | 349 | 174 | 0 |

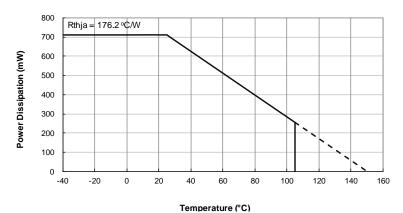


MSOP-8EP Thermal Derating Curve

Note 22: MSOP-8EP exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on a 2in x 2in two-layer 2oz copper FR4 PCB (1.6mm thickness) with four thermal vias in the exposed PAD to the copper flood on the bottom layer.

MSOP-8EP Power Dissipation De-rating Curve 2 (Note 23)

| T _A (°C) | -40 | 0 | 25 | 50 | 60 | 70 | 80 | 85 | 90 | 95 | 100 | 105 | 110 | 120 | 125 | 130 | 140 | 150 |
|---------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| P _D (mW) | 710 | 710 | 710 | 568 | 511 | 454 | 398 | 369 | 341 | 312 | 284 | 256 | 227 | 170 | 142 | 114 | 57 | 0 |



MSOP-8EP Thermal Derating Curve

Note 23: MSOP-8EP exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on a 1in x1in 2oz copper FR4 PCB (1.6mm thickness) with no thermal vias in the exposed PAD or any copper flood connecting to the landing pattern or on the bottom layer.

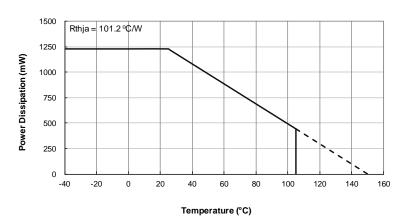


Thermal Performance (continued)

(1) Package Type: U-DFN2020-6

U-DFN2020-6 Power Dissipation De-rating Curve 1 (Note 24)

| T _A (°C) | -40 | 0 | 25 | 50 | 60 | 70 | 80 | 85 | 90 | 95 | 100 | 105 | 110 | 120 | 125 | 130 | 140 | 150 |
|---------------------|------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| P _D (mW) | 1230 | 1230 | 1230 | 984 | 886 | 787 | 689 | 640 | 590 | 541 | 492 | 443 | 394 | 295 | 246 | 197 | 98 | 0 |

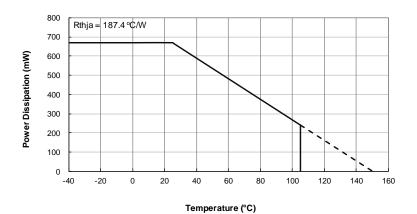


U-DFN2020-6 Thermal Derating Curve

Note 24: U-DFN2020-6 exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on a 1in x1in two-layer 2oz copper FR4 PCB (1.6mm thickness) with two thermal vias in the exposed PAD to the copper flood on the bottom layer.

U-DFN2020-6 Power Dissipation De-rating Curve 2 (Note 25)

| Ī | T _A (°C) | - 40 | 0 | 25 | 50 | 60 | 70 | 80 | 85 | 90 | 95 | 100 | 105 | 110 | 120 | 125 | 130 | 140 | 150 |
|---|---------------------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| | P _D (mW) | 670 | 670 | 670 | 536 | 482 | 429 | 375 | 348 | 322 | 295 | 268 | 241 | 214 | 161 | 134 | 107 | 54 | 0 |

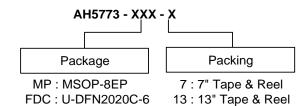


U-DFN2020-6 Thermal Derating Curve

Note 25: U-DFN2020-6 exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on a 1in x1in two-layer 2oz copper FR4 PCB (1.6mm thickness) with no thermal vias or any copper flood connecting to the landing pattern or the bottom layer.



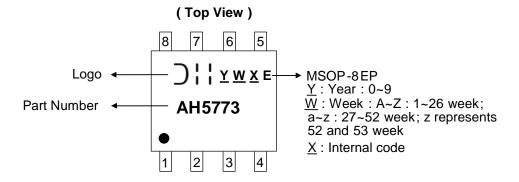
Ordering Information



| Part Number Package Code | Backage Code | Packaging | 13" Tape and Reel | |
|--------------------------|--------------|-------------|-------------------|--------------------|
| | Fackage Code | | Quantity | Part Number Suffix |
| AH5773-MP-13 | MP | MSOP-8EP | 2,500/Tape & Reel | -13 |
| AH5773-FDC-7 | FDC | U-DFN2020-6 | 3,000/Tape & Reel | -7 |

Marking Information

(1) Package Type: MSOP-8EP



(2) Package Type: U-DFN2020-6

(Top View)

<u>XX</u> <u>Y W X</u> ● \underline{XX} : Identification Code \underline{Y} : Year: 0~9

W: Week: A~Z: 1~26 week;

a~z : 27~52 week; z represents

52 and 53 week \underline{X} : Internal Code

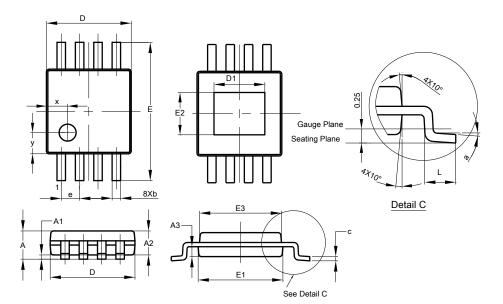
| Part Number | Package | Identification Code |
|--------------|-------------|---------------------|
| AH5773-FDC-7 | U-DFN2020-6 | KH |



Package Outline Dimensions (All dimensions in mm.)

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

(1) Package Type: MSOP-8EP



| MSOP-8EP | | | |
|----------------------|------|------|-------|
| Dim | Min | Max | Тур |
| Α | - | 1.10 | - |
| A1 | 0.05 | 0.15 | 0.10 |
| A2 | 0.75 | 0.95 | 0.86 |
| А3 | 0.29 | 0.49 | 0.39 |
| b | 0.22 | 0.38 | 0.30 |
| C | 0.08 | 0.23 | 0.15 |
| D | 2.90 | 3.10 | 3.00 |
| D1 | 1.60 | 2.00 | 1.80 |
| Е | 4.70 | 5.10 | 4.90 |
| E1 | 2.90 | 3.10 | 3.00 |
| E2 | 1.30 | 1.70 | 1.50 |
| E3 | 2.85 | 3.05 | 2.95 |
| е | - | - | 0.65 |
| L | 0.40 | 0.80 | 0.60 |
| а | 0° | 8° | 4° |
| X | - | - | 0.750 |
| у | - | - | 0.750 |
| All Dimensions in mm | | | |

Top View 0.9/1.1 PART MARKING SURFACE Pin1

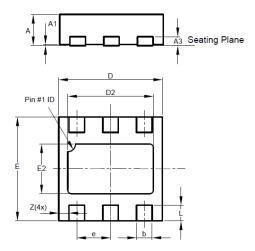
Sensor Location



Package Outline Dimensions (All dimensions in mm.)

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

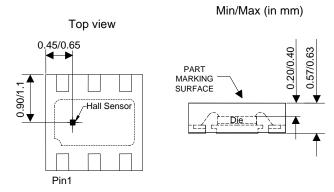
(2) Package Type: U-DFN2020-6 (Type C)



| U-DFN2020-6 | | | | |
|----------------------|------|-------|------|--|
| (Type C) | | | | |
| Dim | Min | Max | Тур | |
| Α | 0.57 | 0.63 | 0.60 | |
| A1 | 0.00 | 0.05 | 0.02 | |
| А3 | - | - | 0.15 | |
| b | 0.25 | 0.35 | 0.30 | |
| D | 1.95 | 2.075 | 2.00 | |
| D2 | 1.55 | 1.75 | 1.65 | |
| Е | 1.95 | 2.075 | 2.00 | |
| E2 | 0.86 | 1.06 | 0.96 | |
| е | - | - | 0.65 | |
| L | 0.25 | 0.35 | 0.30 | |
| Z | - | - | 0.20 | |
| All Dimensions in mm | | | | |

[KST2]

Bottom View



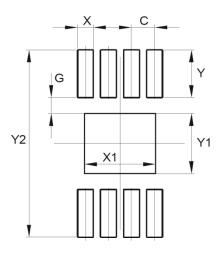
Sensor Location



Suggested Pad Layout

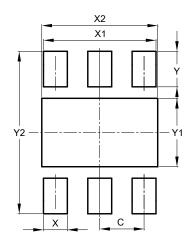
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

(1) Package Type: MSOP-8EP



| Dimensions | Value (in mm) |
|------------|---------------|
| С | 0.650 |
| G | 0.450 |
| X | 0.450 |
| X1 | 2.000 |
| Υ | 1.350 |
| Y1 | 1.700 |
| Y2 | 5.300 |

(2) Package Type: U-DFN2020-6



| Dimensions | Value (in mm) |
|------------|---------------|
| С | 0.650 |
| Х | 0.350 |
| X1 | 1.650 |
| X2 | 1.700 |
| Y | 0.525 |
| Y1 | 1.010 |
| Y2 | 2 400 |



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