Vishay High Power Products

Ultrafast Rectifier, 2 x 15 A FRED Pt[™]



DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t _{rr}	I_F = 1.0 A, dI _F /dt = 50 A/µs, V _R = 30 V		-	-	40		
		T _J = 25 °C	I _F = 15 A dI _F /dt = - 200 A/μs V _R = 200 V	-	32	-	ns	
		T _J = 125 °C		-	45	-		
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2.4	-	A	
		T _J = 125 °C		-	6.1	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	38	-	nC	
		T _J = 125 °C		-	137	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	0.9	2.0		
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	40	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-	-	
Weight			-	6.0	-	g	
			-	0.21	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)	
Marking device		Case style TO-247AC	30CPH03				



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100 l_F - Instantaneous Forward Current (A) = 175 °C = 125 °C T, 10 T_ = 25 °C 1 0.4 0.6 0.8 1.0 1.2 1.4 1.6 V_F - Forward Voltage Drop (V)

Fig. 1 - Typical Forward Voltage Drop Characteristics

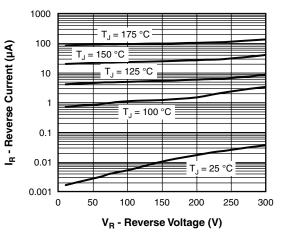


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

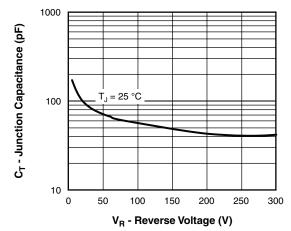


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

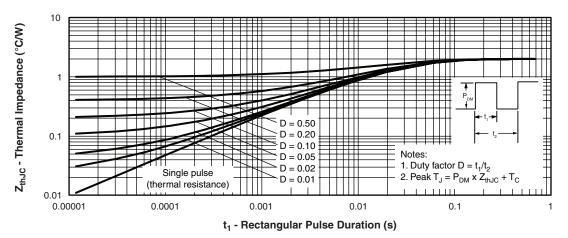


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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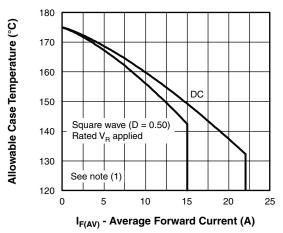
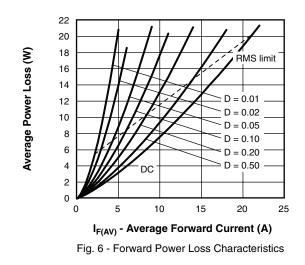


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



Note

 $^{(1)} \mbox{ Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \mbox{ Forward power loss } = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = \mbox{ Inverse power loss } = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = \ Rated \ V_R$

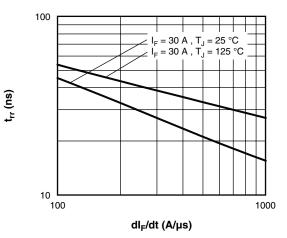
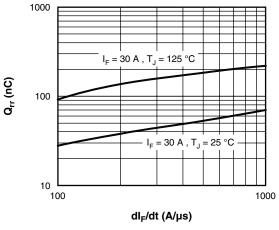
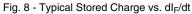


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt







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Fig. 9 - Reverse Recovery Parameter Test Circuit

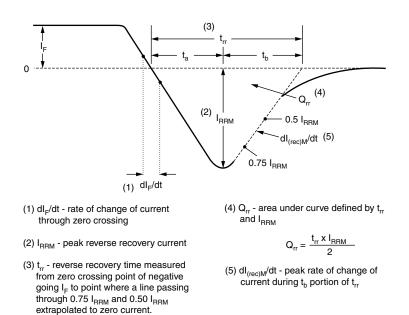
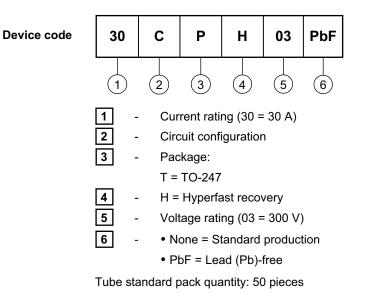


Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS					
Dimensions	http://www.vishay.com/doc?95223				
Part marking information	http://www.vishay.com/doc?95226				

Downloaded from Arrow.com.



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