VS-MBRS320TRPbF

Vishay High Power Products Schottky Rectifier, 3.0 A



ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	3 A	T _J = 25 °C	0.41	0.45			
		6 A		0.45	0.53	V		
		3 A	T _J = 125 °C	0.29	0.36			
		6 A		0.35	0.46			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.04	0.5			
		T _J = 100 °C		8.0	20	mA		
		T _J = 125 °C		23	35			
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		360	-	pF		
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	-	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs		

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MBOL TEST CONDITIONS		UNITS		
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 65 to 150	°C		
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	12	°C/W		
Maximum thermal resistance, junction to ambient	R _{thJA}		46	C/VV		
Approximate weight			0.24	g		
			0.008	OZ.		
Marking device		Case style SMC (similar to DO-214AB)	V	V32		

Notes

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For technical questions, contact: diodestech@vishay.com

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⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB



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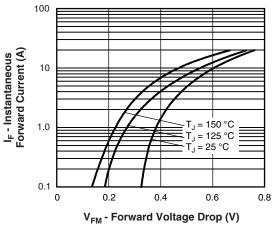


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

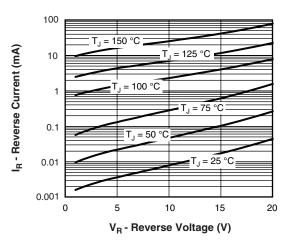


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

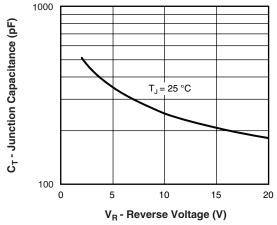


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

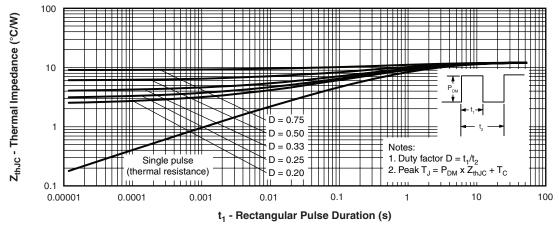


Fig. 4 - - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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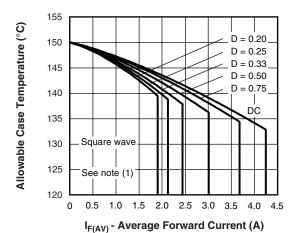


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

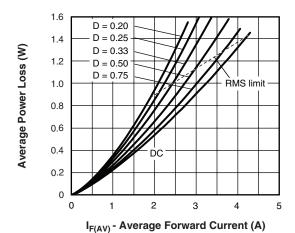


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

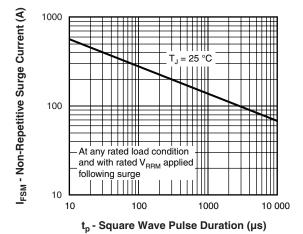


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at (I_{F(AV)}/D) (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R \ (1 - D)$

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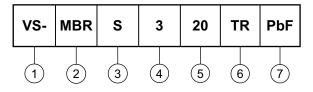
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ORDERING INFORMATION TABLE

Device code



- 1 HPP product suffix
- 2 Schottky MBR series
- 3 S = SMC
 - Current rating (3 = 3 A)
- 5 Voltage rating (20 = 20 V)
- 6 TR = Tape and reel (3000 pieces)
- 7 PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95023			
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			

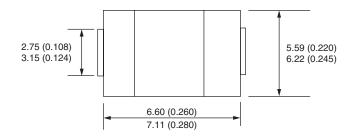
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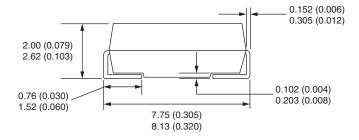


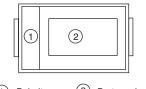
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SMC

DIMENSIONS in millimeters (inches)

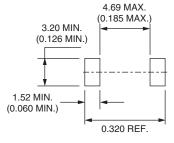






1 Polarity

2 Part number



Soldering pad

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