PACKAGE/ORDERING INFORMATION

MODEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION
SGM4995	SGM4995YDE8G/TR	TDFN-2×2-8L	Tape and Reel, 3000	4995

ABSOLUTE MAXIMUM RATINGS

Supply Voltage……6V
Input Voltage……-0.3V to (V_{CC}) + 0.3V
Storage Temperature Range …… -65℃ to +150℃
Junction Temperature……150℃
Operating Temperature Range……-40℃ to +85℃
Lead Temperature Range (Soldering 10sec)
……260℃
ESD Susceptibility
HBM……2000V
MM……400V

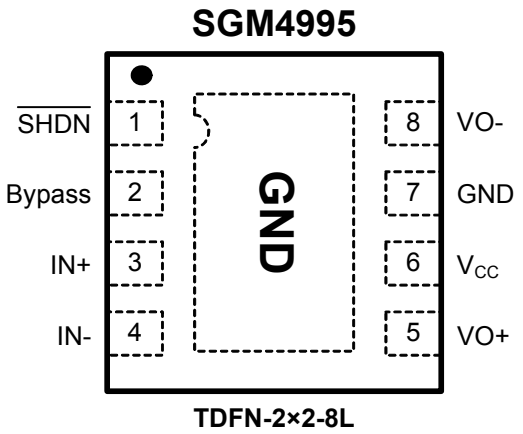
NOTE:
Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

CAUTION

This integrated circuit can be damaged by ESD if you don’t pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.

PIN CONFIGURATION (TOP VIEW)



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ELECTRICAL CHARACTERISTICS

(The following AC specifications apply for 8Ω load, A_V = 1V/V, T_A = +25℃, unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage	V_{CC}			2.5		5.5	V
Shutdown Current	I_{SD}	V_{IN} = 0V, V_{SHDN} = GND			0.02	2	μA
Output Offset Voltage	V_{OS}	V_{IN} = 0V, V_{SHDN} = V_{CC} = 5.0V		-10	2.5	10	mV
		V_{IN} = 0V, V_{SHDN} = V_{CC} = 3.3V		-10	2.0	10	
		V_{IN} = 0V, V_{SHDN} = V_{CC} = 2.6V			2.0		
Quiescent Power Supply Current	I_Q	V_{IN} = 0V, I_O = 0A, V_{SHDN} = V_{CC}	V_{CC} = 5.0V, No Load		4.72	8	mA
			V_{CC} = 5.0V, 8Ω Load		4.75	8.2	
			V_{CC} = 3.3V, No Load		3.70	6	
			V_{CC} = 3.3V, 8Ω Load		3.72		
			V_{CC} = 2.6V, No Load		2.90		
			V_{CC} = 2.6V, 8Ω Load		3.00		
Shutdown Voltage Input High	V_{SDIH}			1.2			V
Shutdown Voltage Input Low	V_{SDIL}					0.4	
Output Power (8Ω)	P_O	f = 1kHz, THD+N = 1%	V_{CC} = 5.0V		1.30		W
			V_{CC} = 3.6V		0.65		
			V_{CC} = 3.0V		0.47		
			V_{CC} = 2.6V		0.34		
		f = 1kHz, THD+N = 10%	V_{CC} = 5.0V		1.60		
			V_{CC} = 3.6V		0.84		
			V_{CC} = 3.0V		0.58		
			V_{CC} = 2.6V		0.42		
Total Harmonic Distortion + Noise	THD+N	P_O = 0.6Wrms, f = 1kHz, V_{CC} = 5.0V			0.042		%
Power Supply Rejection Ratio (1) (2)	PSRR	f = 217Hz	V_{CC} = 5.0V		-62		dB
			V_{CC} = 3.6V		-60		
			V_{CC} = 3.0V		-58		
			V_{CC} = 2.6V		-57		
		f = 1kHz	V_{CC} = 5.0V		-73		
			V_{CC} = 3.6V		-71		
			V_{CC} = 3.0V		-70		
			V_{CC} = 2.6V		-63		
Common Mode Rejection Ratio (2)	CMRR	f = 217Hz, V_{CM} = 200mV_{P-P}, V_{CC} = 5.0V			-74		dB
Wake-Up Time	T_{WU}	C_B = 1μF	V_{CC} = 5.0V		50		ms
			V_{CC} = 3.6V		42		
			V_{CC} = 3.0V		37		
			V_{CC} = 2.6V		32		

NOTES:

1. 10Ω terminated input.
2. PSRR and CMRR are affected by the matching between gain-setting resistor ratios.

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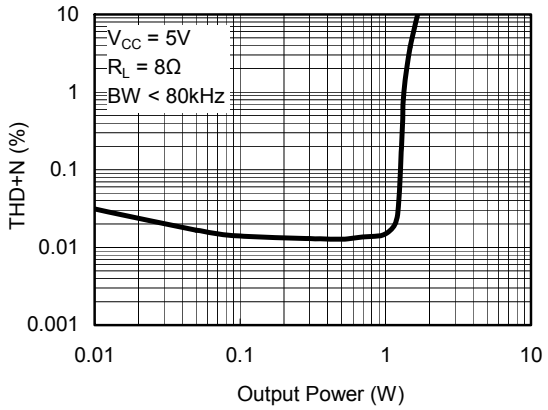
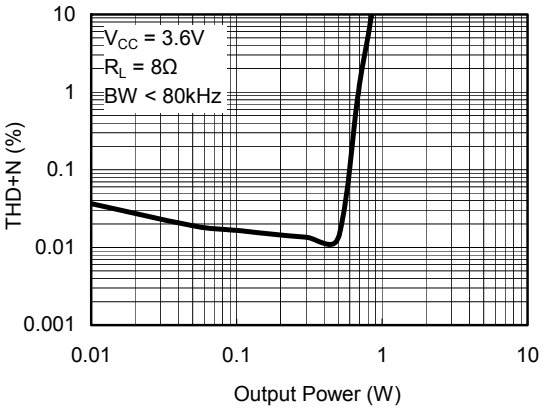
TYPICAL PERFORMANCE CHARACTERISTICS

At T_A = +25℃, A_V = 1, f = 1kHz, C_B = 1μF, unless otherwise noted.

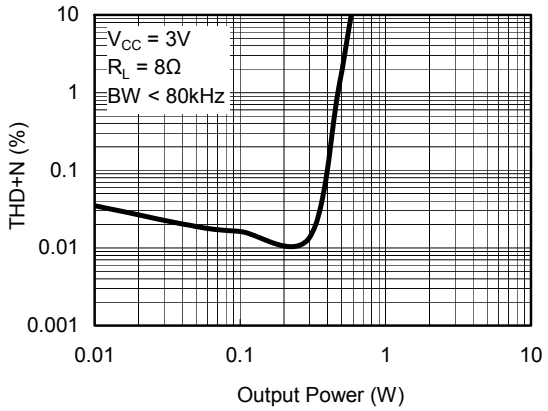
THD+N vs. Output Power



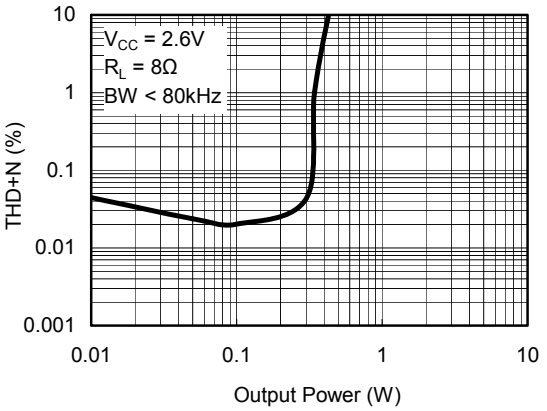
THD+N vs. Output Power



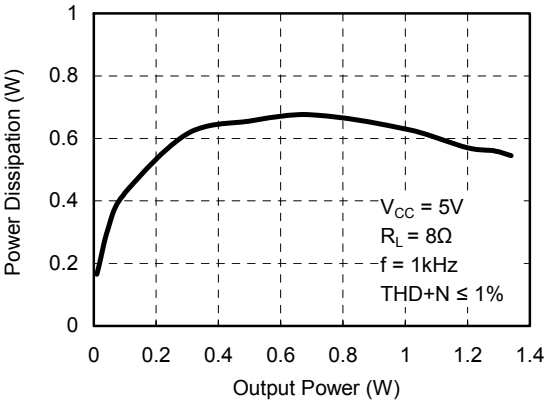
THD+N vs. Output Power



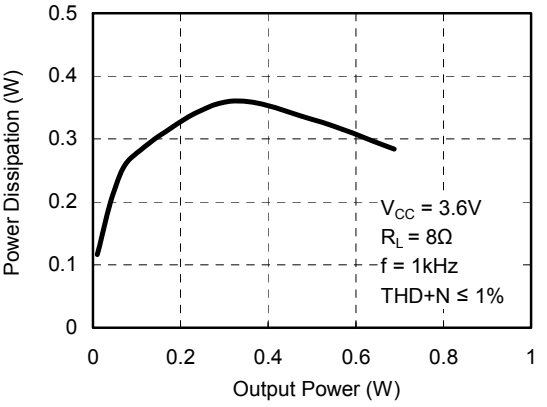
THD+N vs. Output Power



Power Dissipation vs. Output Power



Power Dissipation vs. Output Power



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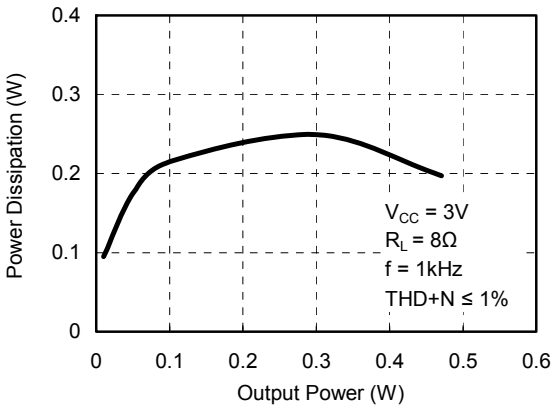
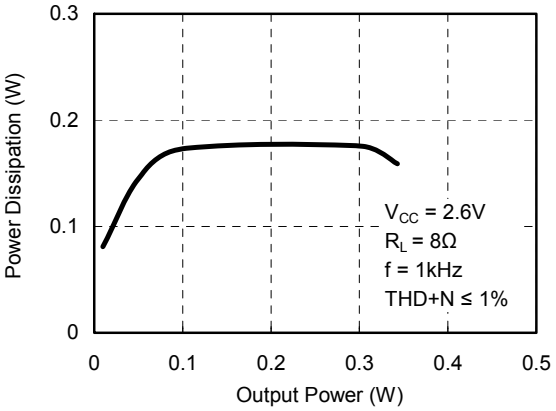
TYPICAL PERFORMANCE CHARACTERISTICS

At T_A = +25℃, A_V = 1, f = 1kHz, C_B = 1μF, unless otherwise noted.

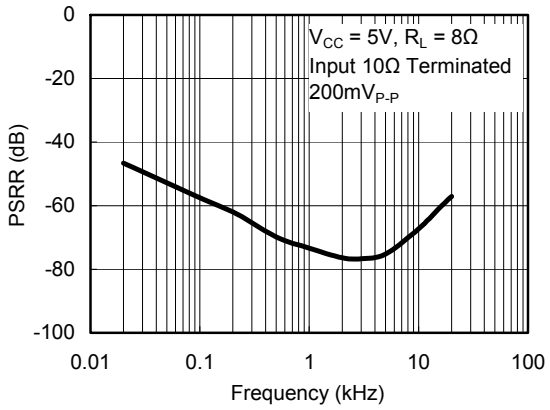
Power Dissipation vs. Output Power



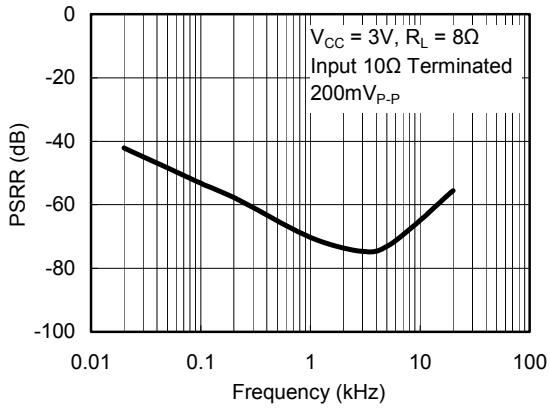
Power Dissipation vs. Output Power



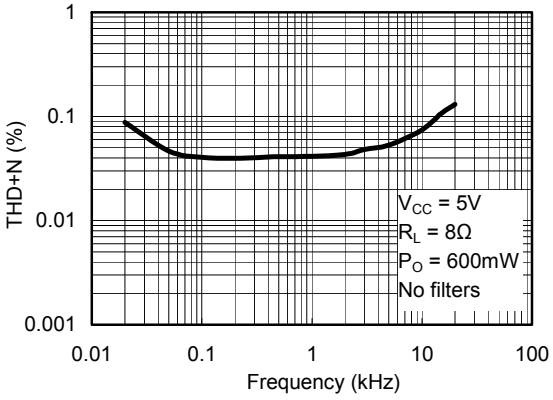
Power Supply Rejection Ratio vs. Frequency



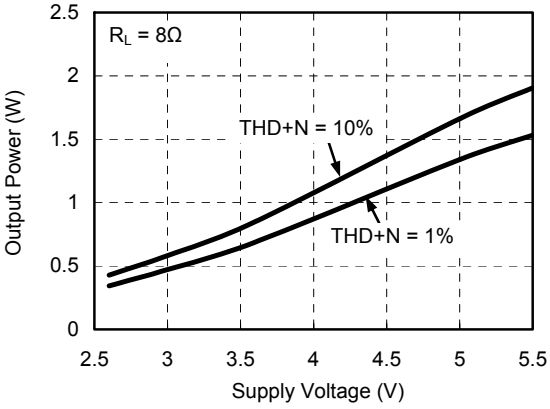
Power Supply Rejection Ratio vs. Frequency



THD+N vs. Frequency



Output Power vs. Supply Voltage



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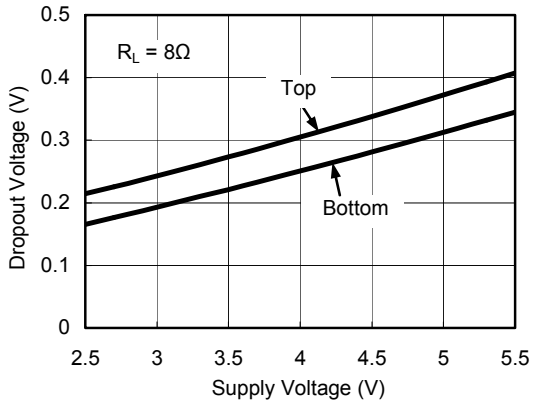
TYPICAL PERFORMANCE CHARACTERISTICS

At T_A = +25℃, A_V = 1, f = 1kHz, C_B = 1μF, unless otherwise noted.

Clipping (Dropout) Voltage vs. Supply Voltage



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APPLICATION CIRCUITS

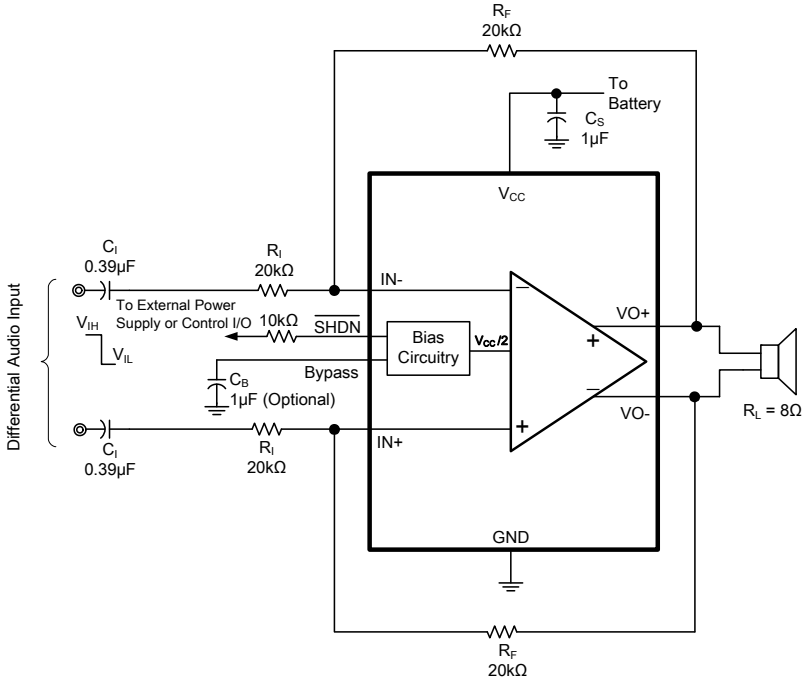

Figure 1. Typical Differential Input Application Schematic

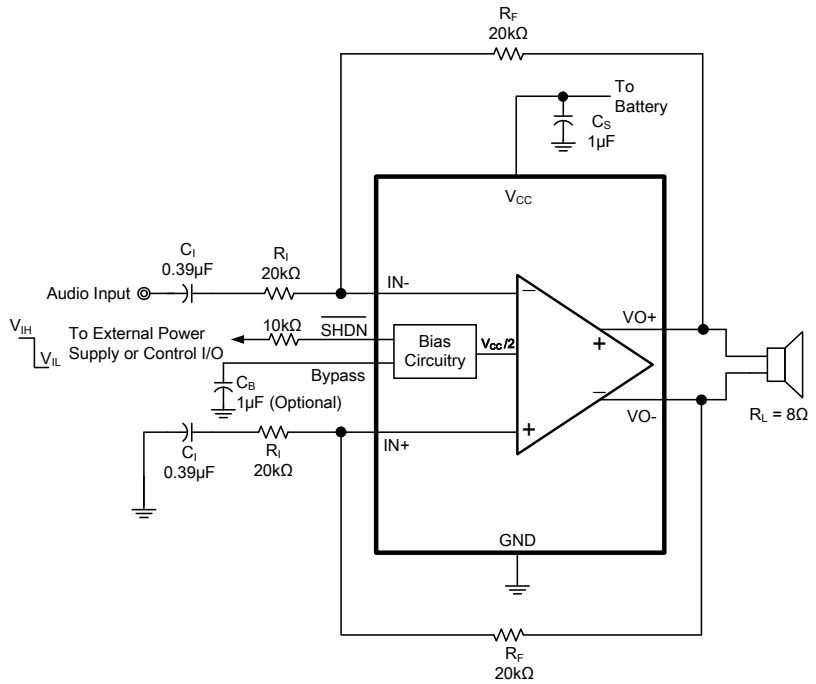

Figure 2. Single-Ended Input Application Schematic

NOTE:
1. A 10kΩ resistor must be serially connected to $\overline{SHDN}$ pin.

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APPLICATION NOTES

PCB Design Recommendations (Thermal Design Considerations)

Thermal Land

The TDFN-2×2-8L thermal land is a metal (normally copper) region centrally located under the package and on top of the PCB. It has a rectangular or square shape and should match the dimensions of the exposed pad on the bottom of the package (1:1 ratio).

For certain high power applications, the PCB land may be modified to a "dog bone" shape that enhances thermal performance. The packages used with the "dog bone" lands will be a dual inline configuration (see Figure 3).

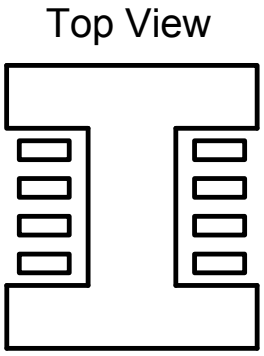

Figure 3. Dog Bone

Thermal Vias

Thermal vias are necessary. They conduct heat from the exposed pad of the package to the ground plane. The number of vias is application specific and is dependent upon electrical requirements and power dissipation.

The via diameter should be 0.2mm to 0.33mm with 1oz. copper via barrel plating. It is important to plug the via to avoid any solder wicking inside the via during the soldering process. The thermal vias can be tented with solder mask on the top surface of the PCB. The solder mask diameter should be at least 75microns (or 3mils) larger than the via diameter. The solder mask thickness should be the same across the entire PCB.

A package thermal performance may be improved by increasing the number of vias.

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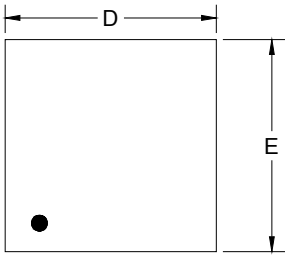
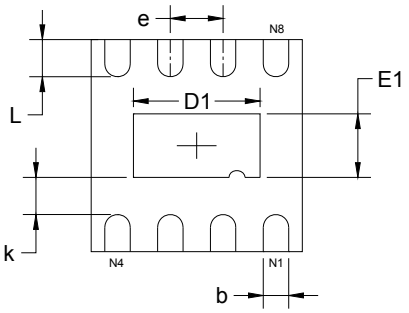
PACKAGE OUTLINE DIMENSIONS

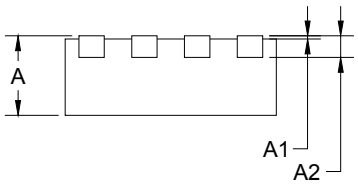
TDFN-2×2-8L



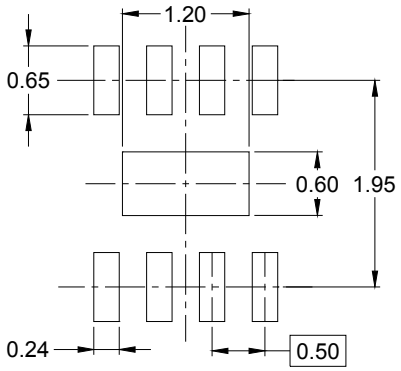
TOP VIEW



BOTTOM VIEW



SIDE VIEW



RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203 REF		0.008 REF	
D	1.900	2.100	0.075	0.083
D1	1.100	1.300	0.043	0.051
E	1.900	2.100	0.075	0.083
E1	0.500	0.700	0.020	0.028
k	0.200 MIN		0.008 MIN	
b	0.180	0.300	0.007	0.012
e	0.500 TYP		0.020 TYP	
L	0.250	0.450	0.010	0.018

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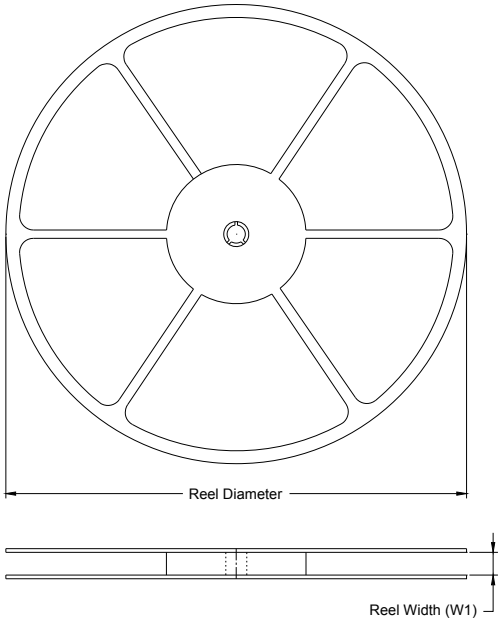
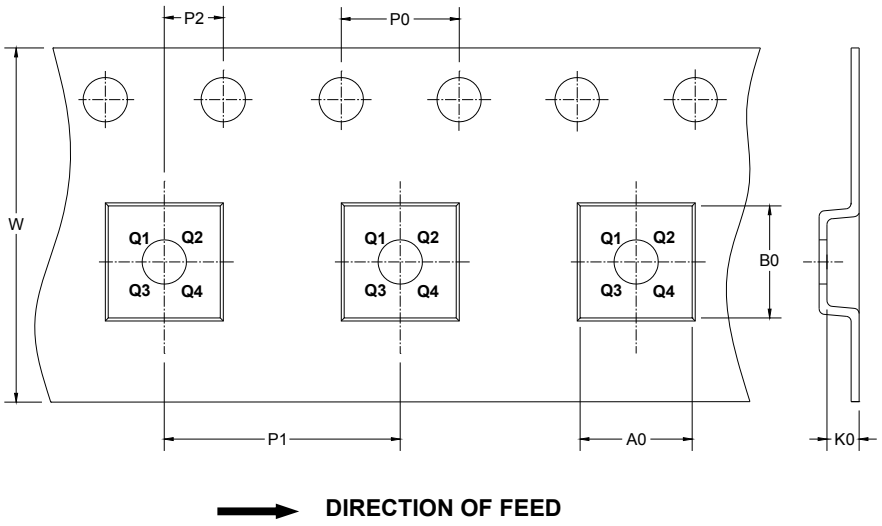
TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TDFN-2×2-8L	7″	9.5	2.30	2.30	1.10	4.00	4.00	2.00	8.00	Q1

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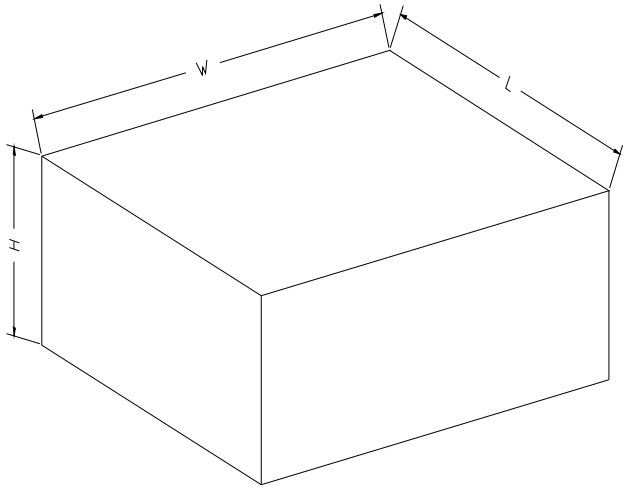
CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7″ (Option)	368	227	224	8
7″	442	410	224	18

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