

ELECTRICAL SPECIFICATIONS

| Input | | |
|--------------------------------------|--|--|
| Input voltage range | (See Note 3) | 10.8 - 13.2 Vdc |
| Input current | No load | 10 mA typical |
| Remote ON/OFF | (See Note 1) | Positive logic |
| Start-up time | | 1 V/ms |
| Undervoltage lockout | | 9.0 - 9.5 V typical |
| Track input voltage | Pin 8 (See Notes 6) | $\pm 0.3 V_{in}$ |
| Output | | |
| Voltage adjustability | (See Note 4) | 1.2 - 5.5 Vdc (Suffix 'W') 0.8 - 1.8 Vdc (Suffix 'L') |
| Setpoint accuracy | | $\pm 2.0\% V_o$ |
| Line regulation | | ± 10 mV typical |
| Load regulation | | ± 12 mV typical |
| Total regulation | | $\pm 3.0\% V_o$ |
| Minimum load | | 0 A |
| Ripple and noise 20 MHz bandwidth | Suffix 'W': $V_o < 2.5$ V $V_o > 2.5$ V Suffix 'L': $V_o < 1.0$ V $V_o > 1.0$ V | 25 mV pk-pk 1% V_o 20 mV pk-pk 30 mV pk-pk |
| Temperature co-efficient | -40 °C to +85 °C | $\pm 0.5\% V_o$ |
| Transient response | (See Note 5) | 70 μ s recovery time Overshoot/undershoot 100 mV |
| Margin adjustment | | $\pm 5.0\% V_o$ |

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated.
 $C_{in} = 560 \mu F$, $C_{out} = 0 \mu F$.

GENERAL SPECIFICATIONS

| | | |
|-------------------------|--------------------------------|--|
| Efficiency | | See Efficiency Table |
| Insulation voltage | | Non-isolated |
| Switching frequency | Over V_{in} and I_o ranges | Suffix 'W': 350 kHz typical Suffix 'L': 250 kHz typical |
| Approvals and standards | | EN60950, UL/cUL60950 |
| Material flammability | | UL94V-0 |
| Dimensions | L x W x H | 25.27 x 15.75 x 9.00 mm 0.995 x 0.620 x 0.354 in |
| Weight | | 5 g (0.18 oz) |
| MTBF | Telcordia SR-332 | 7,092,000 hours |

EMC CHARACTERISTICS

| | |
|-------------------------|-----------------------|
| Electrostatic discharge | EN61000-4-2, IEC801-2 |
| Conducted immunity | EN61000-4-6 |
| Radiated immunity | EN61000-4-3 |

ENVIRONMENTAL SPECIFICATIONS

| | | |
|----------------------------------|--|---------------------------------------|
| Thermal performance (See Note 2) | Operating ambient temperature Non-operating temperature | -40 °C to +85 °C -40 °C to +125 °C |
| MSL ('Z' suffix only) | JEDEC J-STD-020C | Level 3 |
| Protection | | |
| Short-circuit | Auto reset | 20 A typical |

ORDERING INFORMATION

| Model Number ⁽⁹⁾ | Output Power (Max.) | Input Voltage | Output Voltage | Output Current (Min.) | Output Current (Max.) | Efficiency (Typical) | Regulation | |
|-----------------------------|---------------------|-----------------|----------------|-----------------------|-----------------------|----------------------|------------|--------|
| | | | | | | | Line | Load |
| PTH12060L | 55 W | 10.8 - 13.2 Vdc | 0.8 - 1.8 Vdc | 0 A | 10 A | 88% | ±10 mV | ±12 mV |
| PTH12060W | 55 W | 10.8 - 13.2 Vdc | 1.2 - 5.5 Vdc | 0 A | 10 A | 94% | ±10 mV | ±12 mV |

PART NUMBER SYSTEM WITH OPTIONS

| Product Family | Input Voltage | Output Current | Mechanical Package | Output Voltage Code | Pin Option ⁽⁸⁾ | Mounting Options | Pin Option |
|-----------------------------------|---------------|----------------|--------------------|-----------------------------|---------------------------|--|---|
| PTH | 12 | 06 | 0 | W | A | S | T |
| Point-of-Load Alliance compatible | 12 = 12 V | 06 = 10 A | Always 0 | W = Wide L = Low Voltage | | D = Horizontal through-hole (RoHS 6/6) Z = Surface-mount solder ball (RoHS 6/6) | No Suffix = Trays T = Tape and Reel ⁽⁸⁾ |

OUTPUT VOLTAGE ADJUSTMENT

The ultra-wide output voltage trim range offers major advantages to users who select the PTH12060. It is no longer necessary to purchase a variety of modules in order to cover different output voltages. The output voltage can be trimmed in a range of 1.2 Vdc to 5.5 Vdc for suffix 'W' and 0.8 Vdc to 1.8 Vdc for suffix 'L'. When the PTH12060 converter leaves the factory the output has been adjusted to the default voltage of 1.2 V for the PTH12060W and 0.8 V for the PTH12060L.

Efficiency Table: PTH12060W (Io = 8 A)

| Output Voltage | Efficiency |
|----------------|------------|
| Vo = 5.0 V | 94% |
| Vo = 3.3 V | 92% |
| Vo = 2.5 V | 90% |
| Vo = 2.0V | 88% |
| Vo = 1.8 V | 87% |
| Vo = 1.5 V | 85% |
| Vo = 1.2 V | 83% |

Efficiency Table: PTH12060L (Io = 8 A)

| Output Voltage | Efficiency |
|----------------|------------|
| Vo = 1.8 V | 88% |
| Vo = 1.5 V | 87% |
| Vo = 1.2 V | 84% |
| Vo = 1.0 V | 82% |
| Vo = 0.8 V | 79% |

Notes:

1. Remote ON/OFF, Positive Logic

ON: Pin 3 open; or $V > V_{in} - 0.5\text{ V}$

OFF: Pin 3 GND; or $V < 0.8\text{ V}$ (min - 0.2 V).

2. See Figures 1, 2 and 3 for safe operating curves of PTH12060W and Figures 6, 7 and 8 for safe operating curves of PTH12060L.

3. A 560 μF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 800 mA rms of ripple current.

4. An external output capacitor is not required for basic operation. Adding 330 μF of distributed capacitance at the load will improve the transient response.

5. 1 A/ μs load step, 50 to 100% I_{omax} , $C_{\text{out}} = 330\text{ }\mu\text{F}$.

6. If utilized V_{out} will track applied voltage by $\pm 0.3\text{ V}$ (up to V_{o} set point).

7. Tape and reel packaging only available on the surface-mount versions.

8. The pk-pk output ripple voltage is measured with an external 10 μF ceramic capacitor. See Figures 5 and 8 for Standard application schematic.

9. NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at <http://www.artesyn.com> to find a suitable alternative.

PTH12060W CHARACTERISTIC DATA

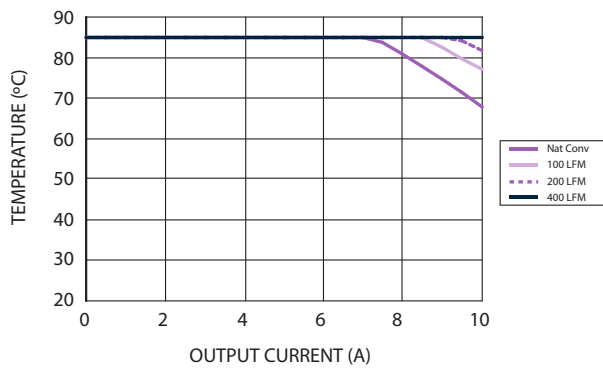


Figure 1 - Safe Operating Area
Vin = 12 V, Output Voltage = 5 V (See Note A)

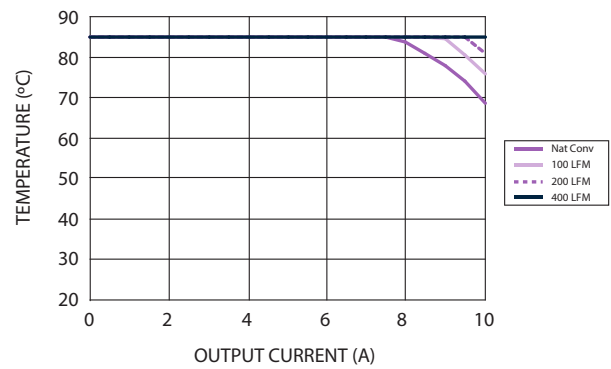


Figure 2 - Safe Operating Area
Vin = 12 V, Output Voltage = 3.3 V (See Note A)

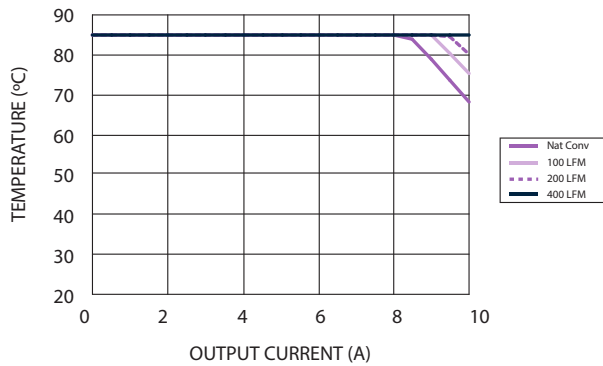


Figure 3 - Safe Operating Area
Vin = 12 V, Output Voltage = 1.8 V (See Note A)

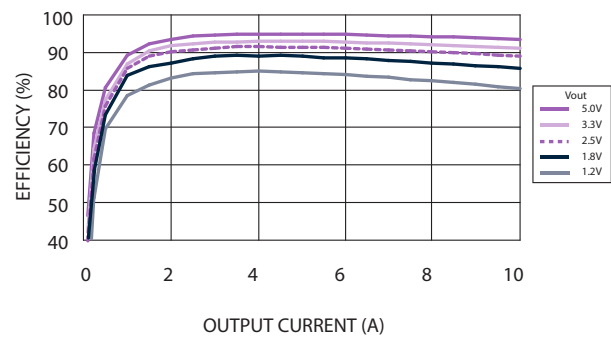


Figure 4 - Efficiency vs Load Current
Vin = 12 V (See Note B)

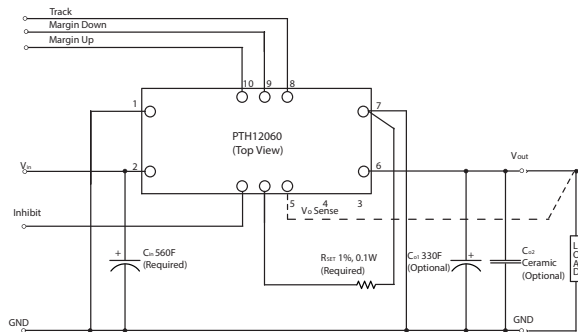


Figure 5 - Standard Application - All Models

Notes:

- SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.

PTH12060L CHARACTERISTIC DATA

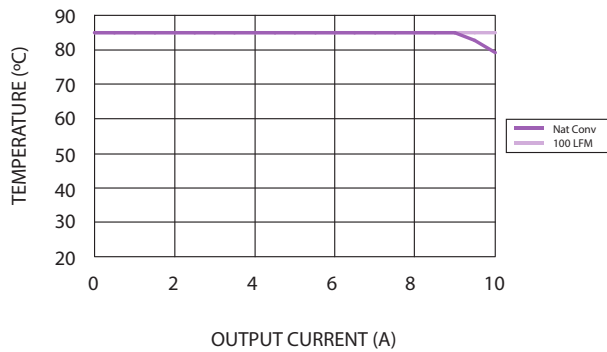


Figure 6 - Safe Operating Area
 $V_{in} = 12\text{ V}$, Output Voltage = 1.8 V (See Note A)

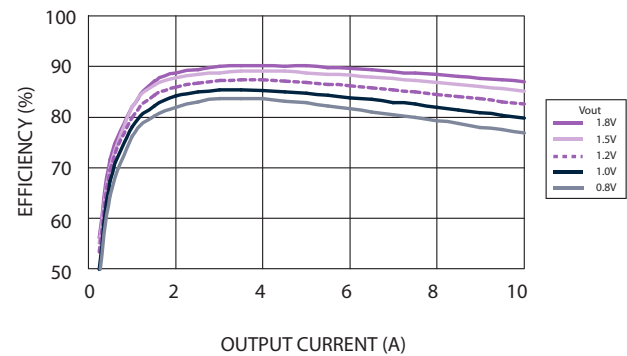


Figure 7 - Efficiency vs Load Current
 $V_{in} = 12\text{ V}$ (See Note B)

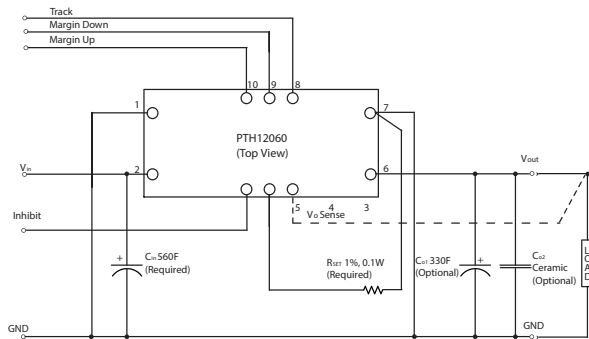


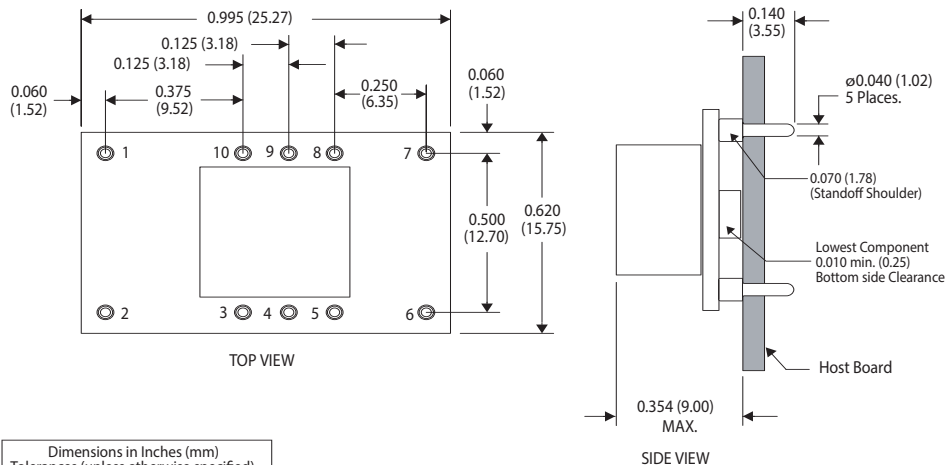
Figure 8 - Standard Application - All Models

Notes:

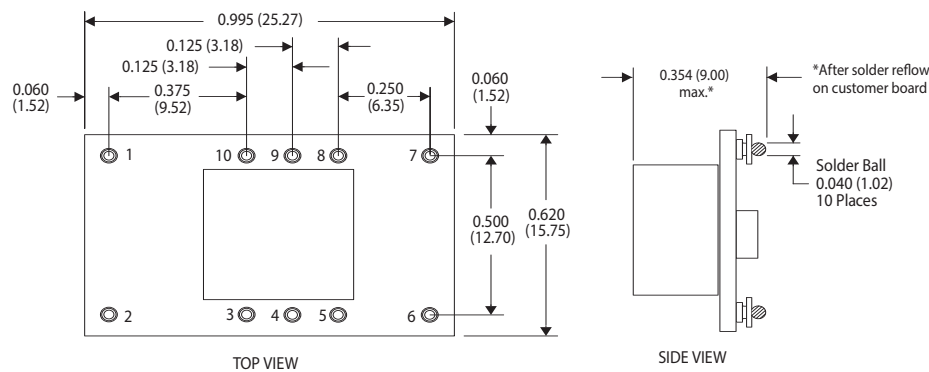
- A. SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- B. Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.

MECHANICAL DRAWINGS

Plated through-hole



Surface-mount



Pin Assignments

| Pin | Function |
|-----|--------------|
| 1 | Ground |
| 2 | Vin |
| 3 | Inhibit* |
| 4 | Vo adjust |
| 5 | Vo sense |
| 6 | Vout |
| 7 | Ground |
| 8 | Track |
| 9 | Margin down* |
| 10 | Margin up* |

*Denotes negative logic:
Open = Normal operation
Ground = Function active



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