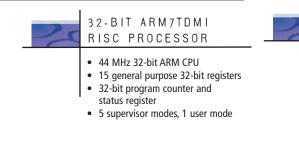
Hardware Specifications



ON-CHIP CACHE

- 8 kB internal cache or 16 kB RAM
- 4-way set associative
- Lockable entries
- Write through

INTEGRAL 10/100 ETHERNET MAC

- 10/100 Mbs MII PHY
- Full- or half-duplex
- Station, broadcast, multicast address detection and filtering
- 128B transmit buffer
- 2 kB receive buffer
- Intelligent receive side-buffer size selection
- External CAM filtering support

10-CHANNEL DMA CONTROLLER

- 2 dedicated to Ethernet transmit/receive
- 4 dedicated to serial transmit/receive
- 2 dedicated to 1284 interface
- 2 dedicated to external peripherals
- Flexible buffer management



IEEE 1284/ENI/GPIO INTERFACE USING SHARED PINS

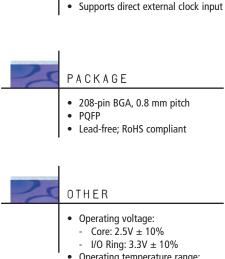
- 4 IEEE 1284 parallel ports, host mode
- 64 K shared RAM ENI interface .
- (8- or 16-bit)
- Full-duplex FIFO mode interface (8- or 16-bit)
- 32 B transmit and 32 B receive FIFOs

SERIAL PORTS

- 2 fully independent HDLC/UART/SPI serial ports
- 32B transmit and 32B receive FIFOs ٠ Internal programmable bit rate generators
- Bit rates from 75 bps to 230 Kbps 16X mode
- Bit rates from 1200 bps to 4 Mbps: 1X mode
- Odd, even, or no parity
- 5, 6, 7 or 8 bits
- 1 or 2 stop bits
- Both internal & external clock support
- Receive side character and buffer gap timers
- 4 receive side data match detectors

EXTERNAL BUS INTERFACE/ MEMORY CONTROLLER

- 44 MHz bus speed
- 5 independent programmable • chip selects
- Supports 8-, 16-, 32-bit peripherals Supports external address decoding
- and cycle termination Supports dynamic bus sizing
- (8-, 16-, & 32-bit) ASYNC and SYNC peripheral timing
- All chip selects support SRAM, EDO DRAM, SDRAM, Flash, EEPROM
- without external glue logic
- Internal DRAM address multiplexing
- Internal refresh controller
- 256 MB addressing per chip select
- Burst-mode support
- 0-15 wait states per chip select •
- Bootstrap support



Operating temperature range: -40° C to 85° C

CLOCK GENERATOR

On-board Phase Locked Loop

Simple external crystal

TIMERS

- timers
- Programmable watch-dog timer

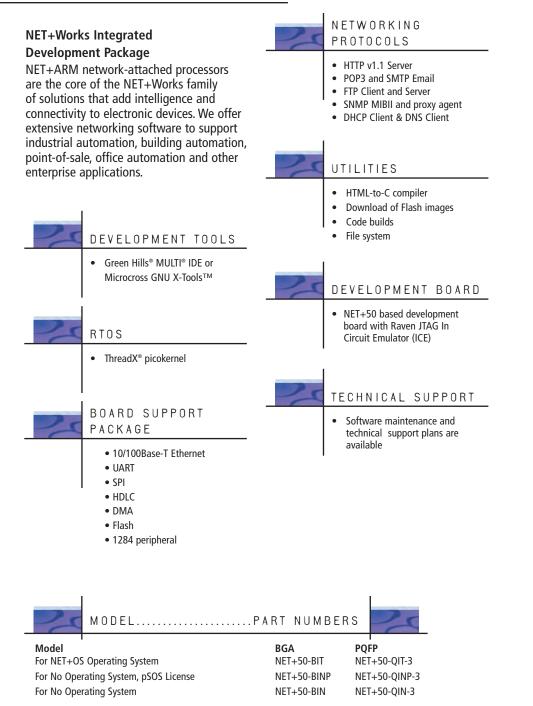
GENERAL PURPOSE I/O

- 40 programmable I/O pins
- 16 general purpose input only pins
- 36 programmable edge or level
- interrupts

- .

 - Programmable bus timer
- 2 independent 27-bit programmable

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Note: OS versions contain paid fees for the run-time license.

FX:

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WHEN MATTERS

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