Applications

The Le87401 integrates two high-power line driver amplifiers. The amplifiers are designed for low distortion for signals up to 86 MHz. A typical PLC application is shown in Figure 2.

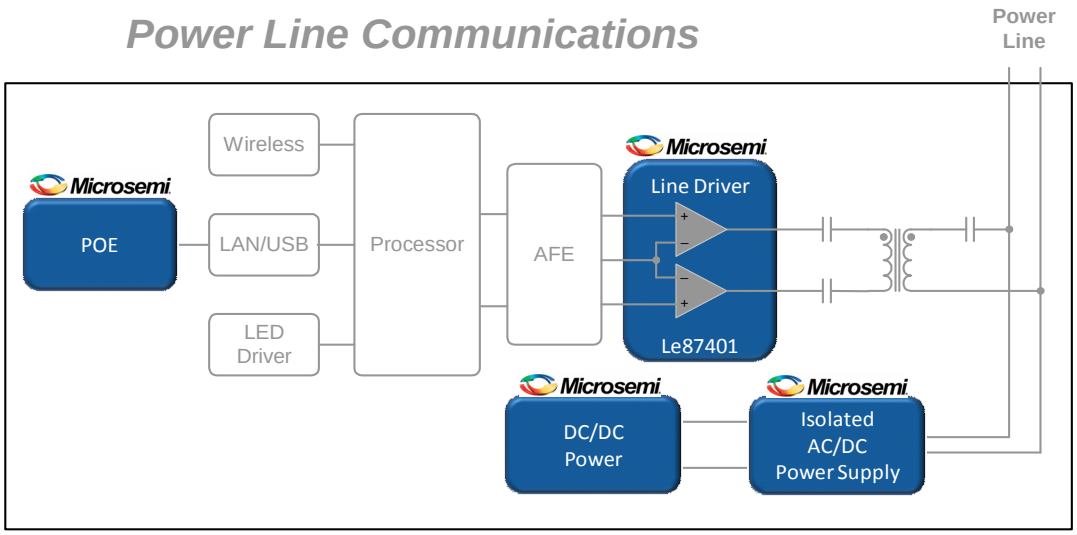


Figure 2 - PLC Application Diagram

Pin Diagrams

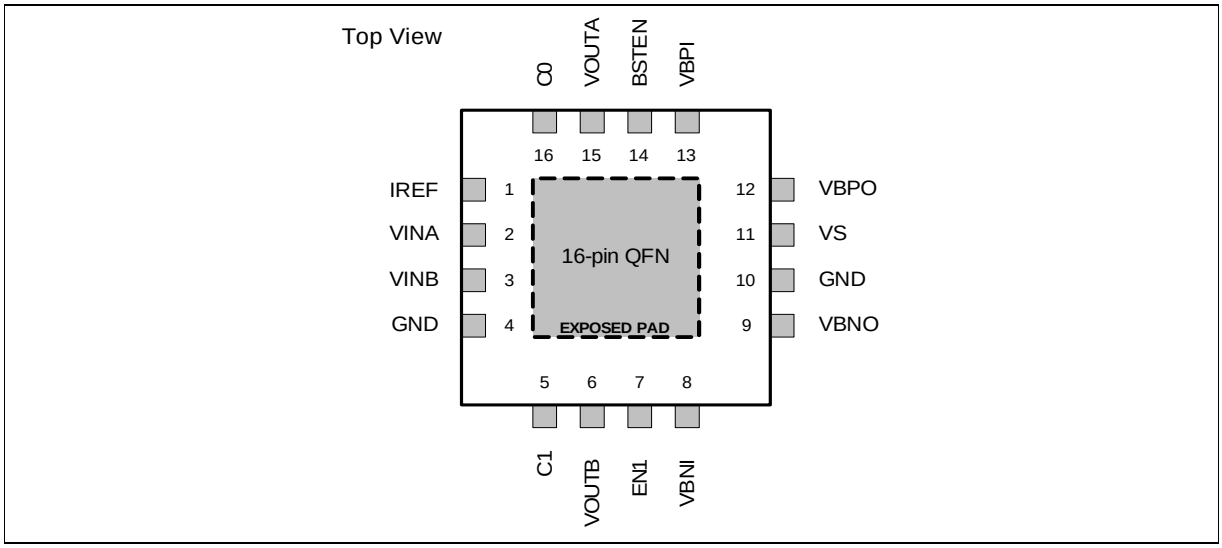
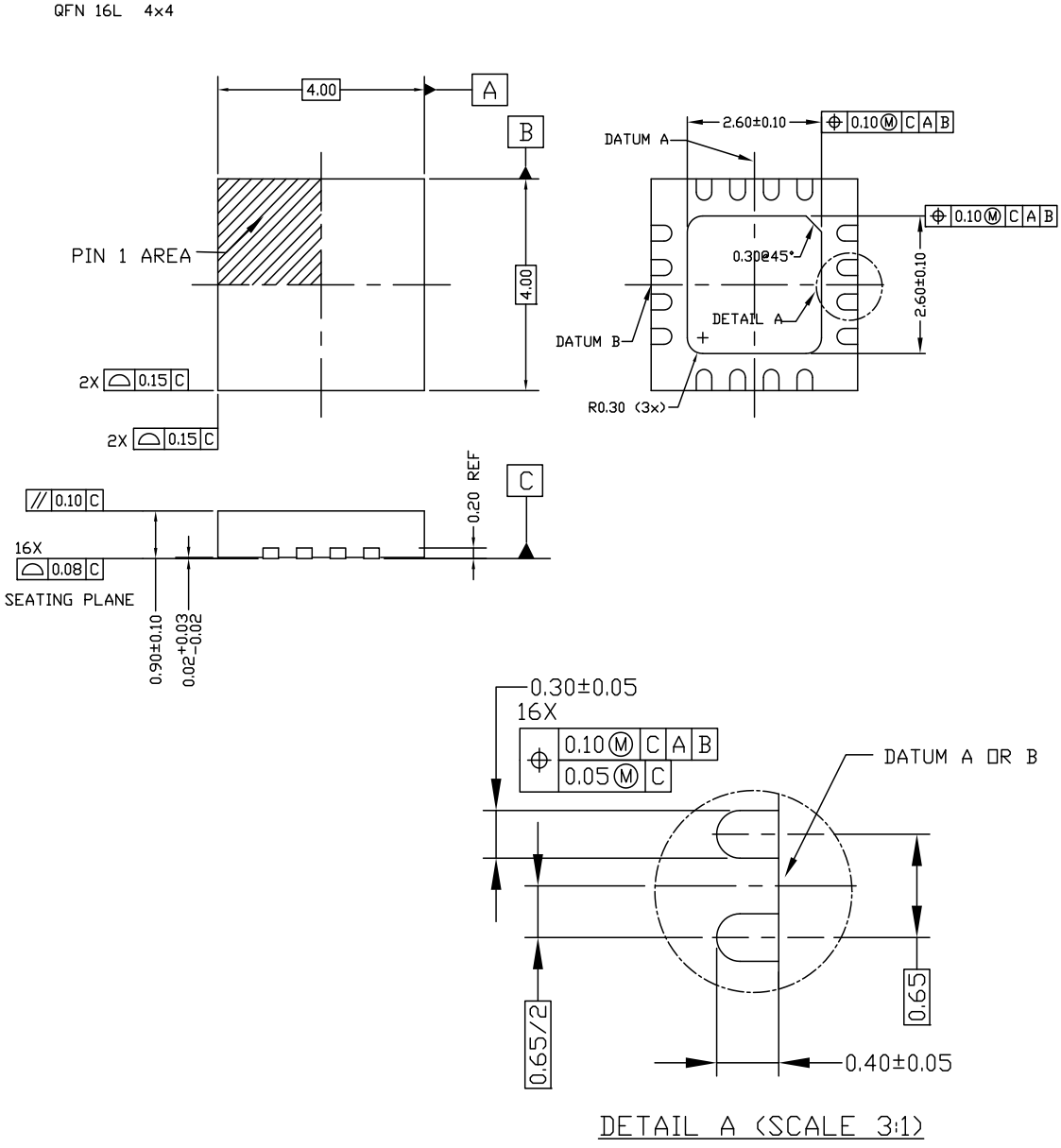


Figure 3 - 16-Pin QFN Diagram

The device incorporates an exposed die pad on the underside of its package. The pad acts as a heat sink and must be connected to a copper plane through thermal vias for proper heat dissipation. It is electrically isolated and may be connected to GND.

Physical Dimensions

16-pin QFN



NOTES:

1. DIMENSIONING AND TOLERANCE IS IN CONFORMANCE TO ASME Y14.5–1994
   ALL DIMENSIONS ARE IN MILLIMETERS ° IN DEGREES

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