

Marking Information

Site 1



F2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2015		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	С		Н	ı	J	K	L	М	N	0	Р	R
	•											
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



F2 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2015	 2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	5	 0	1	2	3	4	5	6	7	8	9

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	X	Y	Z



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	30	V		
Gate-Source Voltage	Vgss	±20	V		
Continuous Prois Current (Note C) Vac. 40V	Steady State	T _A = +25°C T _A = +70°C	l _D	9.3 7.5	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<5s	$T_A = +25$ °C $T_A = +70$ °C	ΙD	11.8 9.4	Α
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%	6)		I _{DM}	50	Α
Maximum Continuous Drain-Source Diode Forward	Is	1.8	Α		
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	18	Α		
Avalanche Energy (Note 7) L = 0.1mH			Eas	16	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	D-	0.73	W	
Total Fower Dissipation (Note 5)	$T_A = +70^{\circ}C$	Pb	0.47	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Roja	174	°C/W	
Thermal Resistance, Junction to Ambient (Note 3)	t < 5s	КӨЈА	112	C/VV	
Total Power Dissipation (Note 6)	T _A = +25°C	D-	2.03	W	
Total Fower Dissipation (Note 0)	$T_A = +70$ °C	Pb	1.30		
Thermal Begistenes, Junction to Ambient (Note 6)	Steady State	Davi	64		
Thermal Resistance, Junction to Ambient (Note 6)	t < 5s	Roja	40	°C/W	
Thermal Resistance, Junction to Case (Note 6)	Steady State	R _O JC	13		
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C		

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	1	μΑ	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	Vgs(TH)	1.0	_	2.2	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
Static Drain-Source On-Resistance	D		_	15	mΩ	Vgs = 10V, ID = 7A
Static Diain-Source On-Resistance	R _{DS(ON)}		_	20	11122	$V_{GS} = 4.5V, I_{D} = 7A$
Diode Forward Voltage	Vsp	_	0.8	1.2	V	Vgs = 0V, Is = 2.2A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	706	_		151/1/ 01/
Output Capacitance	Coss	_	112	_	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	81	_		1 = 1.0WH 12
Gate Resistance	R _G	_	2.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 10V)	QG	_	14	_		
Total Gate Charge (V _{GS} = 4.5V)	QG	_	6.7	_	nC	151/ 1 50
Gate-Source Charge	Q _{GS}	_	1.9	_	nc nc	$V_{DS} = 15V$, $I_{D} = 5A$
Gate-Drain Charge	Q _{GD}	_	2.5	_		
Turn-On Delay Time	t _D (ON)	_	5.4	_		
Turn-On Rise Time	t _R	_	6.8	<u> </u>		$V_{DS} = 15V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)	_	9.7	_	ns	$R_g = 1.7\Omega$, $I_D = 5A$
Turn-Off Fall Time	te	_	4.7	_		

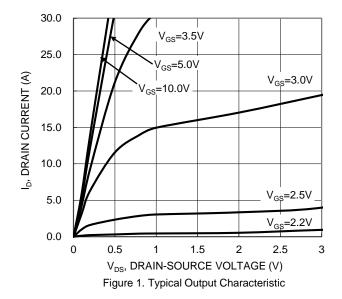
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.

^{7.} I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.





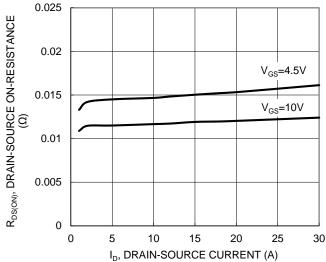


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

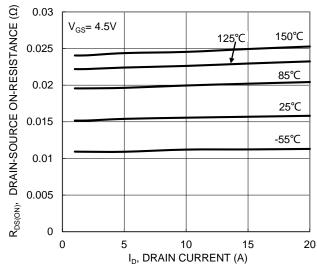


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

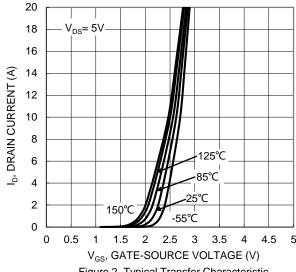


Figure 2. Typical Transfer Characteristic

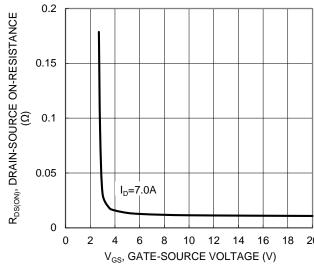


Figure 4. Typical Transfer Characteristic

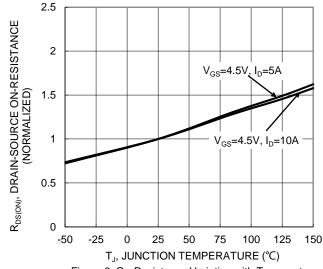


Figure 6. On-Resistance Variation with Temperature



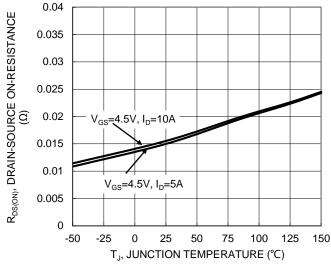
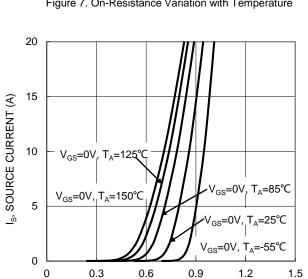
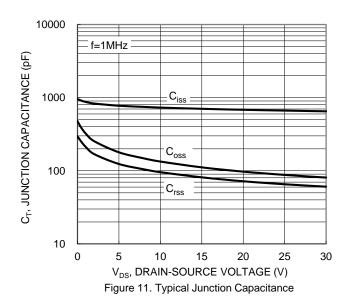


Figure 7. On-Resistance Variation with Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



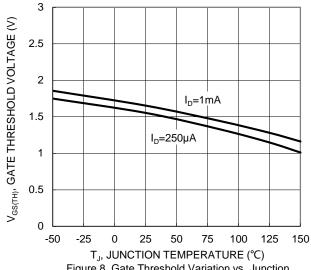


Figure 8. Gate Threshold Variation vs. Junction Temperature

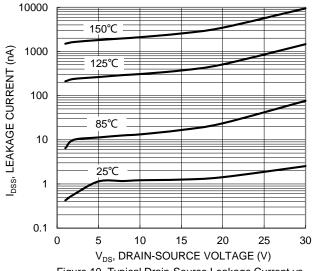


Figure 10. Typical Drain-Source Leakage Current vs. Voltage

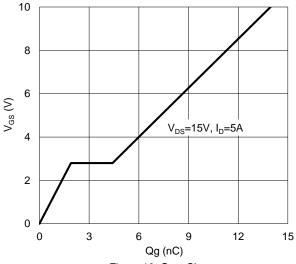
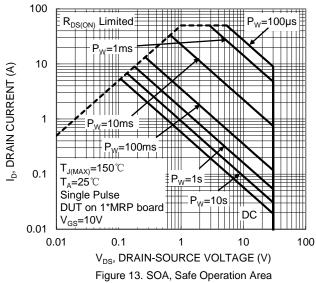
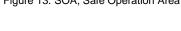


Figure 12. Gate Charge







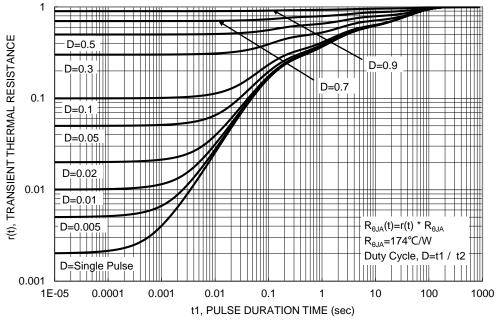


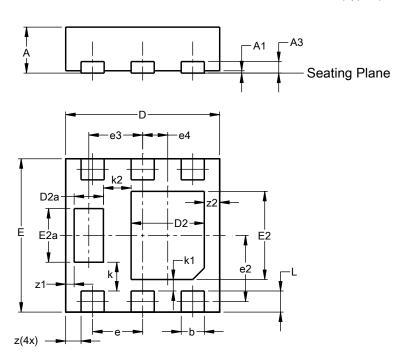
Figure 14. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

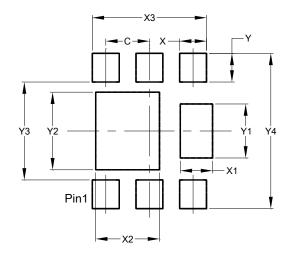


	U-DFN2020-6					
		oe F)				
Dim	Min	Max	Тур			
Α	0.57	0.63	0.60			
A1	0.00	0.05	0.03			
A3	-	-	0.15			
b	0.25	0.35	0.30			
D	1.95	2.05	2.00			
D2	0.85	1.05	0.95			
D2a	0.33	0.43	0.38			
Е	1.95	2.05	2.00			
E2	1.05	1.25	1.15			
E2a	0.65	0.75	0.70			
е		0.65 BS	С			
e2).863 BS	SC			
е3		0.70 BS	С			
e4	().325 BS	SC S			
k		0.37 BS	С			
k1	0.15 BSC					
k2	0.36 BSC					
L	0.225 0.325 0.275					
Z	0.20 BSC					
z 1	0.110 BSC					
z2		0.20 BS	С			
All C	imens	ions in	mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)		
С	0.650		
X	0.400		
X1	0.480		
X2	0.950		
Х3	1.700		
Υ	0.425		
Y1	0.800		
Y2	1.150		
Y3	1.450		
Y4	2.300		



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2020, Diodes Incorporated

www.diodes.com