

# Contents

**1        Pin connection and test/application diagrams ..... 5**

**2        Electrical specifications ..... 6**

      2.1    Absolute maximum ratings ..... 6

      2.2    Thermal data ..... 6

      2.3    Electrical characteristics ..... 7

      2.4    Electrical characteristic curves ..... 8

**3        Application hints ..... 10**

      3.1    SVR ..... 10

      3.2    Input stage ..... 10

      3.3    Standby and muting ..... 10

**4        Package information ..... 11**

**5        Revision history ..... 12**



List of tables

Table 1. Device summary . . . . . 1

Table 2. Absolute maximum ratings . . . . . 6

Table 3. Thermal data. . . . . 6

Table 4. Electrical characteristics . . . . . 7

Table 5. Document revision history . . . . . 12

## List of figures

Figure 1.	Pin connection (top view) . . . . .	5
Figure 2.	Standard test and application circuit. . . . .	5
Figure 3.	Quiescent current vs. supply voltage . . . . .	8
Figure 4.	Output power vs. supply voltage (4 Ohm) . . . . .	8
Figure 5.	Distortion vs. output power (4 Ohm) . . . . .	8
Figure 6.	Distortion vs. frequency (4 Ohm) . . . . .	8
Figure 7.	Supply voltage rejection vs. frequency . . . . .	8
Figure 8.	Crosstalk vs. frequency . . . . .	8
Figure 9.	Output noise vs. source resistance . . . . .	9
Figure 10.	Total power dissipation & efficiency (4 Ohm, sine). . . . .	9
Figure 11.	Power dissipation vs. average output power (4 Ohm, audio program simulation) . . . . .	9
Figure 12.	ITU R-ARM frequency response, weighting filter for transient pop. . . . .	9
Figure 13.	Flexiwatt25 mechanical data and package dimensions . . . . .	11

# 1 Pin connection and test/application diagrams

Figure 1. Pin connection (top view)

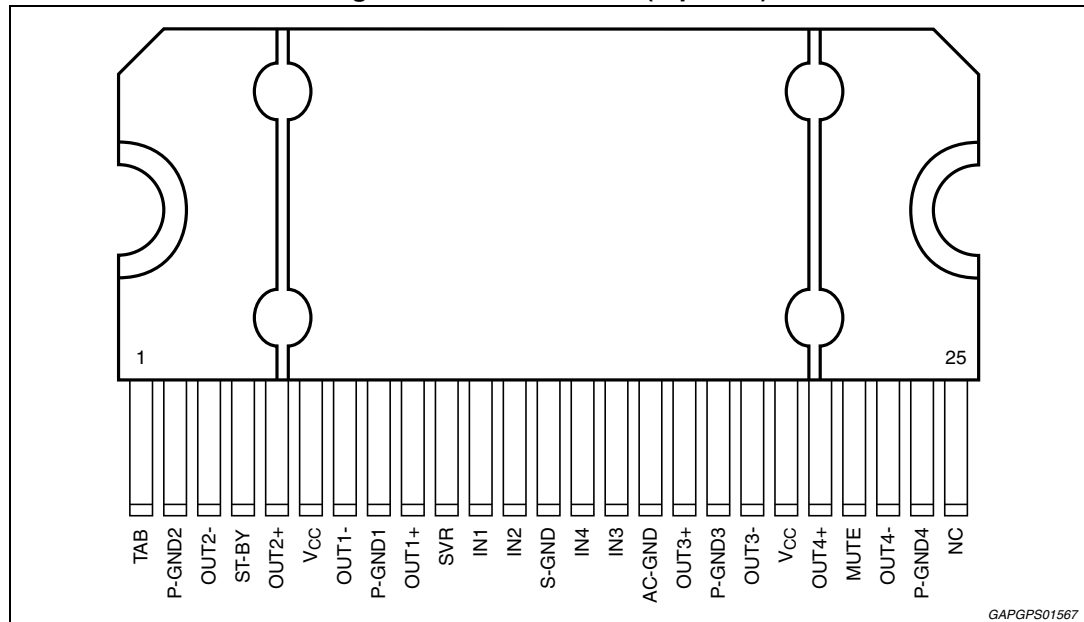
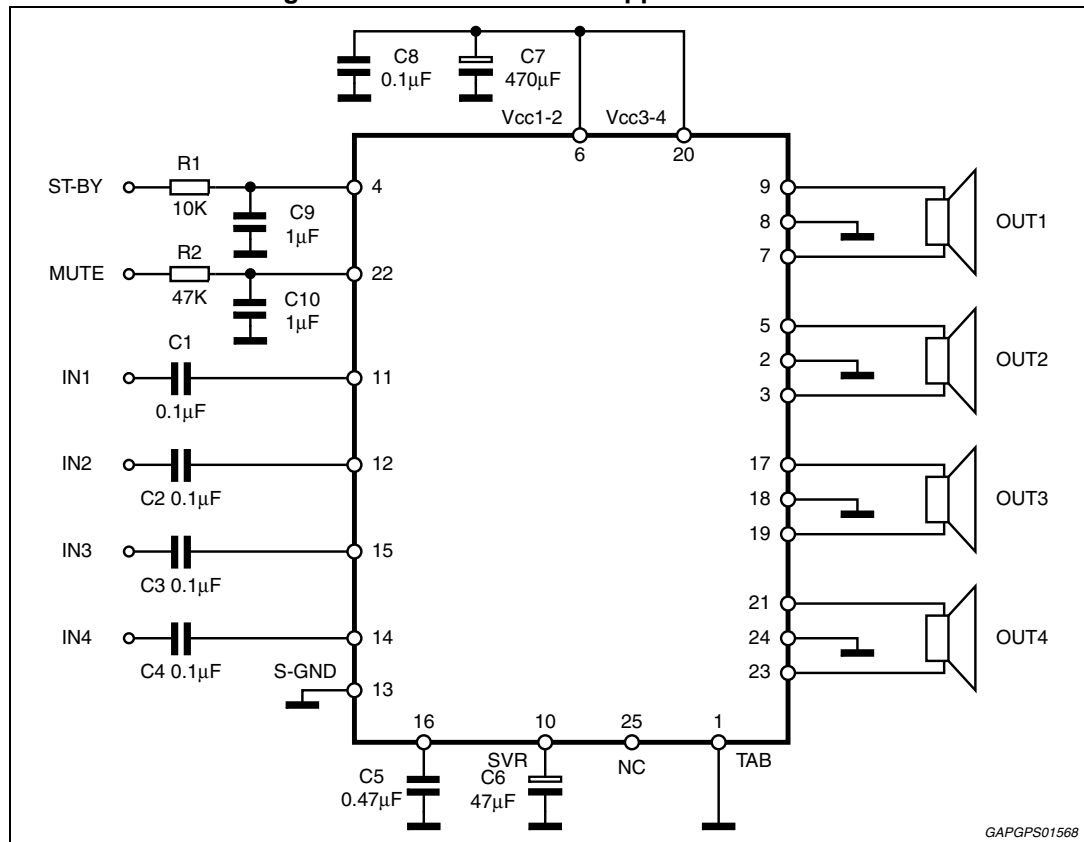


Figure 2. Standard test and application circuit



## 2 Electrical specifications

### 2.1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
$V_S$	Operating supply voltage	18	V
$V_{S(DC)}$	DC supply voltage	28	V
$V_{S(pk)}$	Peak supply voltage (t = 50 ms)	50	V
$I_O$	Output peak current: Repetitive (duty cycle 10 % at f = 10 Hz)	4.5	A
	Non repetitive (t = 100 $\mu$ s)	5.5	
$P_{tot}$	Power dissipation, ( $T_{case} = 70\text{ }^{\circ}\text{C}$ )	80	W
$T_j$	Junction temperature	150	$^{\circ}\text{C}$
$T_{stg}$	Storage temperature	– 55 to 150	$^{\circ}\text{C}$

### 2.2 Thermal data

Table 3. Thermal data

Symbol	Parameter	Value	Unit
$R_{th\ j-case}$	Thermal resistance junction-to-case max.	1	$^{\circ}\text{C/W}$

## 2.3 Electrical characteristics

$V_S = 14.4\text{ V}$ ;  $f = 1\text{ kHz}$ ;  $R_g = 600\ \Omega$ ;  $R_L = 4\ \Omega$ ;  $T_{\text{amb}} = 25\text{ °C}$ ; Refer to the test and application diagram ([Figure 2](#)), unless otherwise specified.

**Table 4. Electrical characteristics**

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
$I_{q1}$	Quiescent current	$R_L = \infty$	120	190	350	mA
$V_{OS}$	Output offset voltage	Play mode	-	-	$\pm 100$	mV
$dV_{OS}$	During mute ON/OFF output offset voltage	ITU R-ARM weighted see <a href="#">Figure 12</a>	-80	-	+80	mV
$G_v$	Voltage gain	-	25	26	27	dB
$P_o$	Output power	THD = 10 %; $V_S = 14.4\text{ V}$	22	26	-	W
$P_{o\text{ max}}$	Max.output power <sup>(1)</sup>	$V_S = 14.4\text{ V}$	37	41	-	W
		$V_S = 15.2\text{ V}$	-	45	-	
THD	Distortion	$P_o = 4\text{ W}$	-	0.04	0.15	%
$e_{No}$	Output noise	"A" Weighted	-	50	70	$\mu\text{V}$
		Bw = 20 Hz to 20 kHz	-	70	100	$\mu\text{V}$
SVR	Supply voltage rejection	$f = 100\text{ Hz}$ ; $V_r = 1\text{ Vrms}$	50	65	-	dB
$f_{ch}$	High cut-off frequency	$P_o = 0.5\text{ W}$	100	200	-	kHz
$R_i$	Input Impedance	-	70	100	-	k $\Omega$
$C_T$	Cross talk	$f = 1\text{ kHz}$ ; $P_o = 4\text{ W}$	60	70	-	dB
		$f = 10\text{ kHz}$ ; $P_o = 4\text{ W}$	-	60	-	dB
$I_{SB}$	Standby current consumption	$V_{\text{St-by}} = 0\text{ V}$	-	-	20	$\mu\text{A}$
$V_{SB\text{ out}}$	Standby OUT threshold voltage	(Amp: ON)	3.5	-	-	V
$V_{SB\text{ IN}}$	Standby IN threshold voltage	(Amp: OFF)	-	-	1.5	V
$A_M$	Mute attenuation	$P_{\text{Oref}} = 4\text{ W}$	80	90	-	dB
$V_{M\text{ out}}$	Mute OUT threshold voltage	(Amp: play)	3.5	-	-	V
$V_{M\text{ in}}$	Mute IN threshold voltage	(Amp: mute)	-	-	1.5	V
$V_{AM\text{ in}}$	$V_S$ automute threshold	(Amp: mute); Att. $\geq 80\text{ dB}$ ; $P_{\text{Oref}} = 4\text{ W}$ (Amp: play); Att. $< 0.1\text{ dB}$ ; $P_o = 0.5\text{ W}$	-	7.6	6.5 8.5	V
$I_{\text{pin22}}$	Muting pin current	$V_{\text{MUTE}} = 1.2\text{ V}$ (Source current)	5	11	20	$\mu\text{A}$

1. Saturated square wave output.

2.4 Electrical characteristic curves

Figure 3. Quiescent current vs. supply voltage

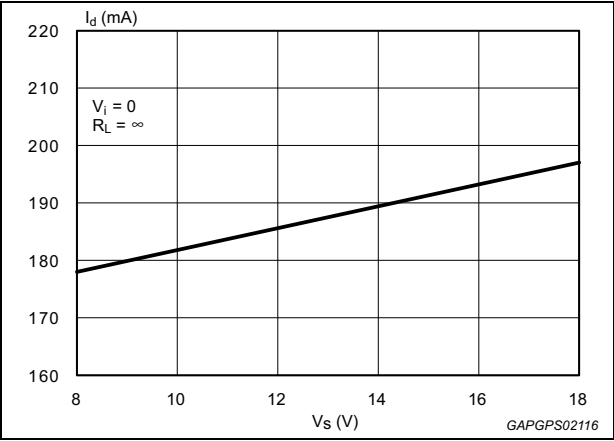


Figure 4. Output power vs. supply voltage (4 Ohm)

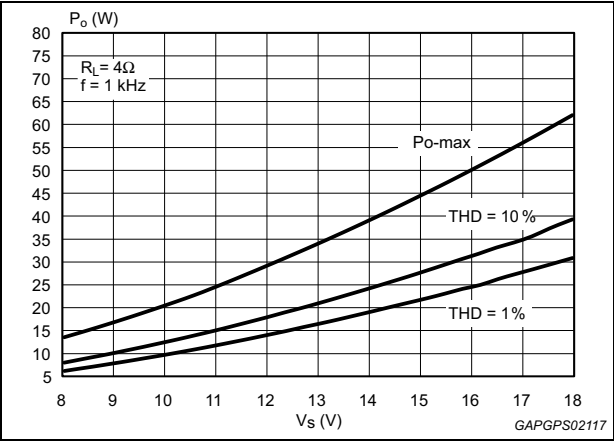


Figure 5. Distortion vs. output power (4 Ohm)

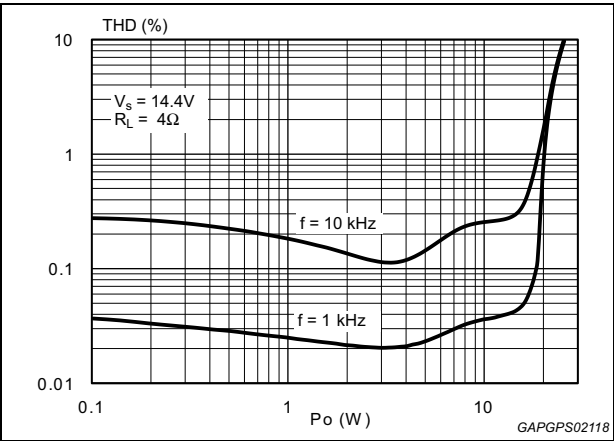


Figure 6. Distortion vs. frequency (4 Ohm)

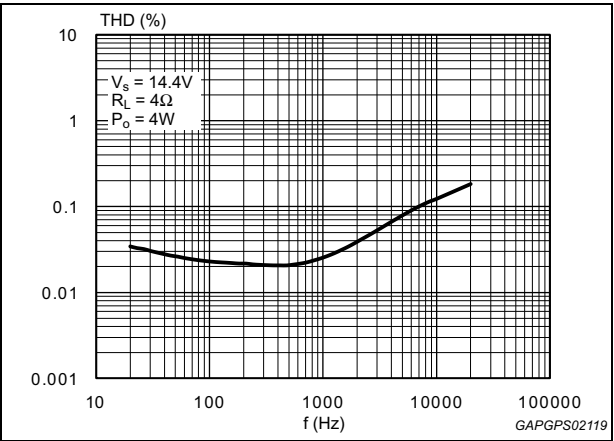


Figure 7. Supply voltage rejection vs. frequency

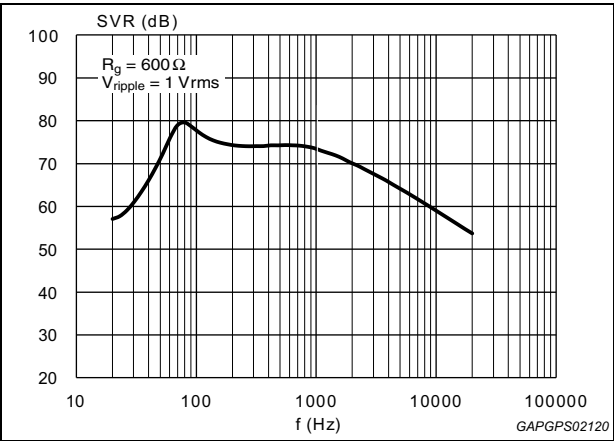


Figure 8. Crosstalk vs. frequency

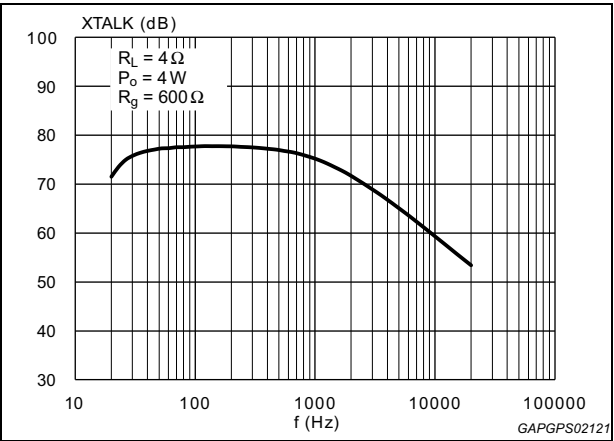


Figure 9. Output noise vs. source resistance

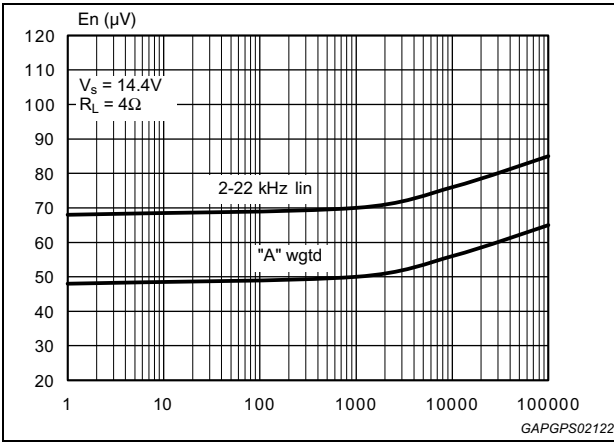


Figure 10. Total power dissipation & efficiency (4 Ohm, sine)

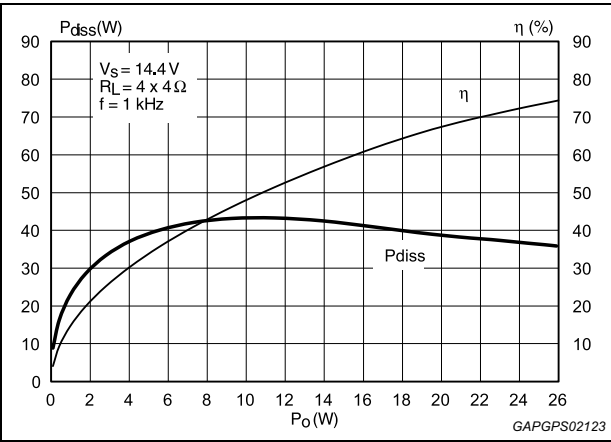


Figure 11. Power dissipation vs. average output power (4 Ohm, audio program simulation)

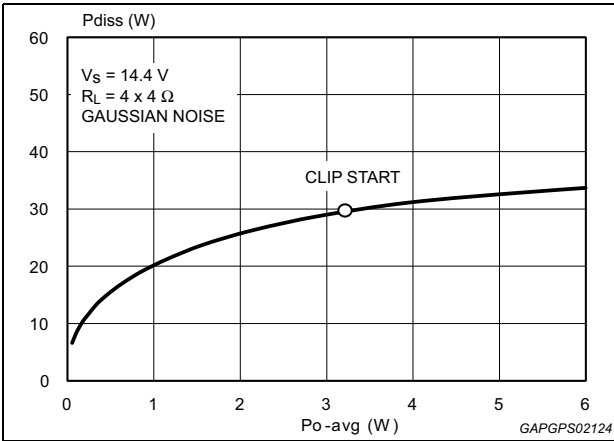
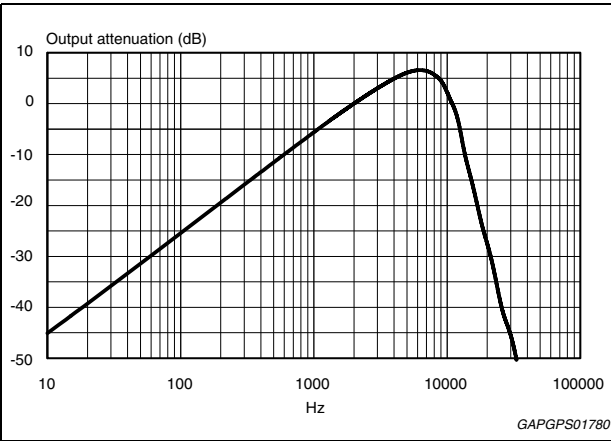


Figure 12. ITU R-ARM frequency response, weighting filter for transient pop





## 3 Application hints

Ref. to the circuit of [Figure 2](#).

### 3.1 SVR

Besides its contribution to the ripple rejection, the SVR capacitor governs the turn ON/OFF time sequence and, consequently, plays an essential role in the pop optimization during ON/OFF transients.

To conveniently serve both needs, **its minimum recommended value is 10  $\mu$ F**.

### 3.2 Input stage

The TDA7388's inputs are ground-compatible and can stand very high input signals ( $\pm 8$  Vpk) without any performances degradation.

If the standard value for the input capacitors (0.1  $\mu$ F) is adopted, the low frequency cut-off amounts to 16 Hz.

### 3.3 Standby and muting

If standby and muting are not used, a straight connection to  $V_S$  of their respective pins would be admissible.

Conventional/low-power transistors can be employed to drive muting and standby pins in absence of true CMOS ports or microprocessors. R-C cells have always to be used in order to smooth down the transitions for preventing any audible transient noises.

Since a DC current of about 10  $\mu$ A normally flows out of pin 22, the maximum allowable muting-series resistance ( $R_2$ ) is 70 k $\Omega$ , which is sufficiently high to permit a muting capacitor reasonably small (about 1  $\mu$ F).

If  $R_2$  is higher than recommended, the involved risk is that the voltage at pin 22 may rises to above the 1.5 V threshold voltage and the device consequently fails to turn OFF when the mute line is brought down.

About the stand-by, the time constant to be assigned in order to obtain a virtually pop-free transition has to be slower than 2.5 V/ms.

## 4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com).

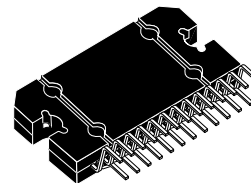
ECOPACK® is an ST trademark.

**Figure 13. Flexiwatt25 mechanical data and package dimensions**

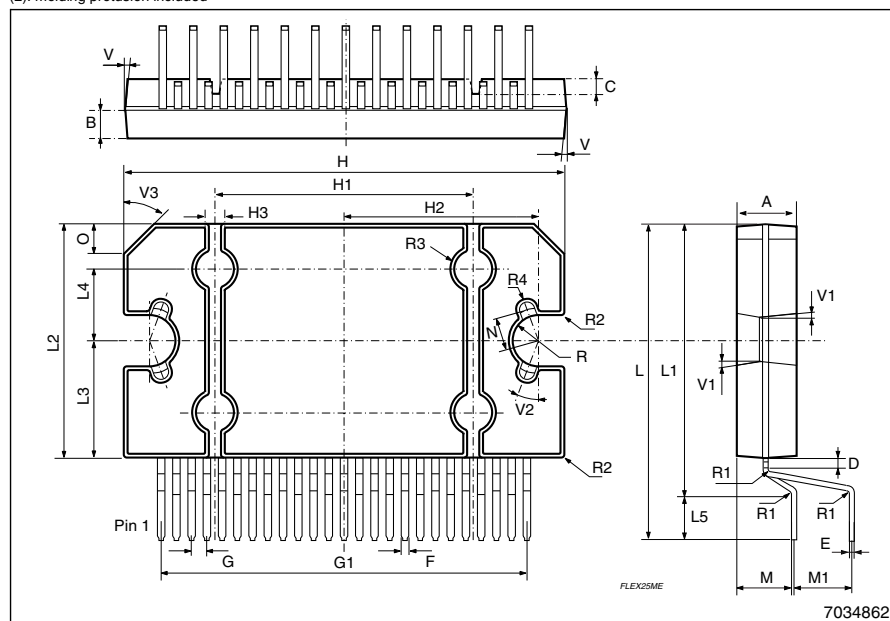
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.45	4.50	4.65	0.175	0.177	0.183
B	1.80	1.90	2.00	0.070	0.074	0.079
C		1.40			0.055	
D	0.75	0.90	1.05	0.029	0.035	0.041
E	0.37	0.39	0.42	0.014	0.015	0.016
F (1)			0.57			0.022
G	0.80	1.00	1.20	0.031	0.040	0.047
G1	23.75	24.00	24.25	0.935	0.945	0.955
H (2)	28.90	29.23	29.30	1.139	1.150	1.153
H1		17.00			0.669	
H2		12.80			0.503	
H3		0.80			0.031	
L (2)	22.07	22.47	22.87	0.869	0.884	0.904
L1	18.57	18.97	19.37	0.731	0.747	0.762
L2 (2)	15.50	15.70	15.90	0.610	0.618	0.626
L3	7.70	7.85	7.95	0.303	0.309	0.313
L4		5			0.197	
L5		3.5			0.138	
M	3.70	4.00	4.30	0.145	0.157	0.169
M1	3.60	4.00	4.40	0.142	0.157	0.173
N		2.20			0.086	
O		2			0.079	
R		1.70			0.067	
R1		0.5			0.02	
R2		0.3			0.12	
R3		1.25			0.049	
R4		0.50			0.019	
V	5° (Typ.)					
V1	3° (Typ.)					
V2	20° (Typ.)					
V3	45° (Typ.)					

(1): dam-bar protusion not included  
(2): molding protusion included

### OUTLINE AND MECHANICAL DATA



### Flexiwatt25 (vertical)



GAPGPS00669

## 5 Revision history

**Table 5. Document revision history**

Date	Revision	Changes
06-Dec-2007	1	Initial release.
12-Jul-2010	2	Document status promoted from preliminary data to datasheet.
26-Apr-2012	3	Modified <a href="#">Features on page 1</a> . Updated <a href="#">Table 4: Electrical characteristics on page 7</a> .
20-Jun-2012	4	Updated <a href="#">Section 3.3: Standby and muting</a> .
11-Mar-2013	5	Added <a href="#">Section 2.4: Electrical characteristic curves</a> .
17-Sep-2013	6	Updated Disclaimer.

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