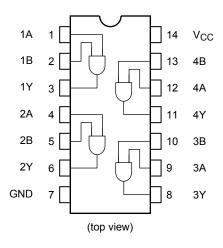
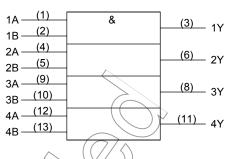
Pin Assignment

IEC Logic Symbol





Truth Table

Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Α	В	Υ
L	L	L
L	Н	L
I	Ш	L
Н	Н	Н

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	(VCC)	-0.5 to 7.0	V
DC input voltage	VIN	=0.5 to VCC + 0.5	V
DC output voltage	V _O UT	0.5 to V _{CC} + 0.5	V
Input diode current	JIK <	±20	mA
Output diode current	lok_	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±100	mA
Power dissipation	PD	180	mW
Storage temperature	Tstg	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	٧
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dV	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V
input rise and rail time	avav	0 to 20 (V _{CC} = 5 ± 0.5 V)	\ \ \

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

				/ / /					
Characteristics Symbol		Test Condition		$\left(\right)$	Га = 25°C	; ((√= ∖85°C	Unit
STALLAGION SYMBOL	Cymbol		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Min	Тур.	Max	Min	Max	Onic
			2.0	1.50	-(1.50	_	
High-level input voltage	V _{IH}	-	3.0	2.10	_		2.10	_	V
ŭ			5.5	3.85	$(\mathcal{H} \langle$	_	3.85	-	
			2.0			0.50	_	0.50	
Low-level input voltage	V_{IL}		3.0	_ \	_	0.90	_	0.90	V
ŭ			5.5	/	<i>) </i>	1.65	_	1.65	
			2.0	1.9	2.0	_	1.9	_	
		I _{OH} = -50 μA	3.0	2.9	3.0	_	2.9	_	
High-level output	Voh	VIN	4.5	4.4	4.5	1	4.4	-	V
voltage	VOH (=VIH I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	V
		I _{OH} = -24 mA	4.5	3.94	_	_	3.80	_	
	(()=	I _{OH} ≠ -75 mA (Note)	5.5	-	_	1	3.85	1	
			2.0	_	0.0	0.1	_	0.1	
		toL = 50 μA	3.0	_	0.0	0.1	_	0.1	
Low-level output	V _{QL}	V _{IN} = V _{IH} or	4.5	-	0.0	0.1	_	0.1	V
voltage		V _{IL} I _{OL} = 12 mA	3.0	_	_	0.36	_	0.44	v
		I _{OL} = 24 mA	4.5	_	_	0.36	_	0.44	
))	IOL = 75 mA (Note)	5.5	_	_	_	_	1.65	
Input leakage current	IIN	V _{IN} = V _{CC} or GND	5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc >	V _{IN} = V _{CC} or GND	5.5	ı	_	4.0	_	40.0	μΑ

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

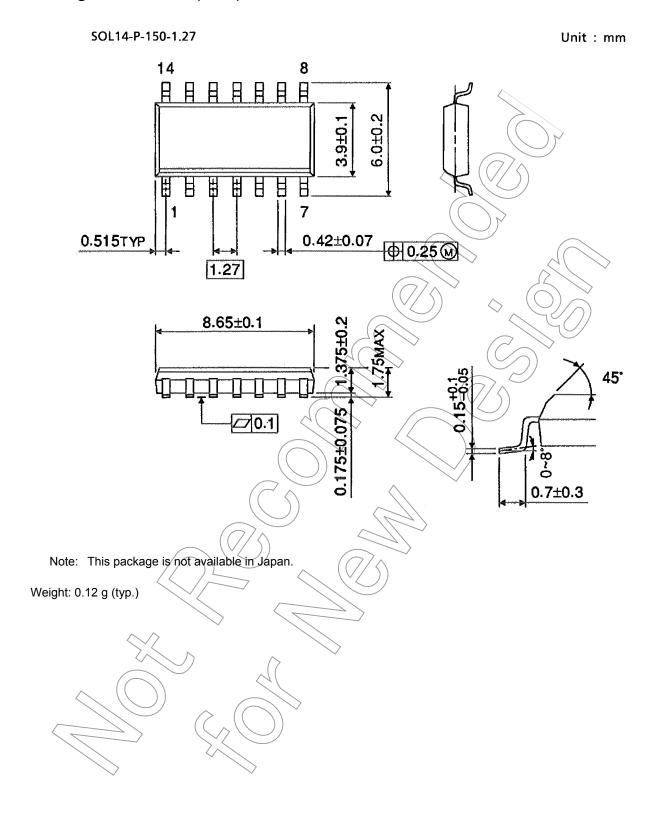
Characteristics	Symbol Test Condition			Ta = 25°C		Ta = −40 to 85°C		Unit	
	-,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay time	t _{pLH}	_	3.3 ± 0.3 5.0 ± 0.5	_	5.8 4.5	9.8 7.0	1.0 1.0	11.3 8.0	ns
Input capacitance	C _{IN}	_	I	_	5	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)	_	71) /~	-	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:



Package Dimensions (Note)



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