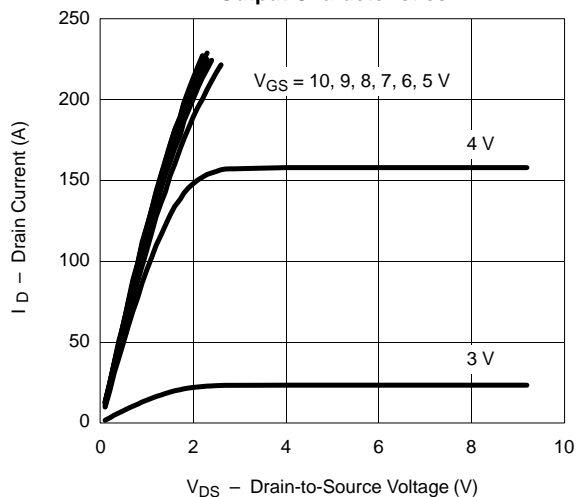
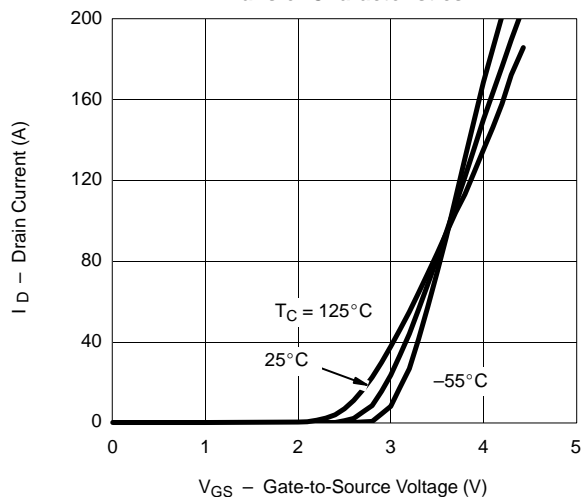
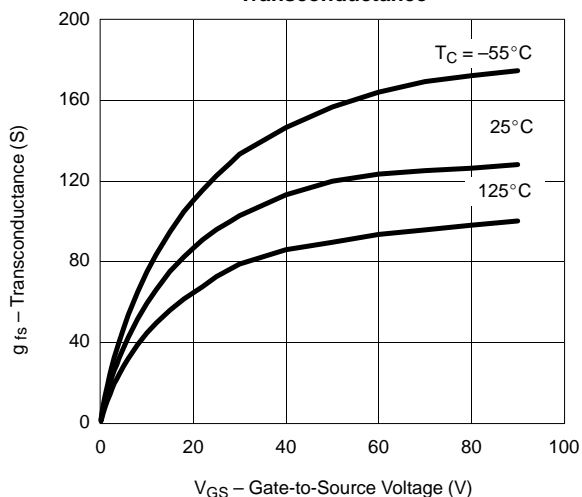
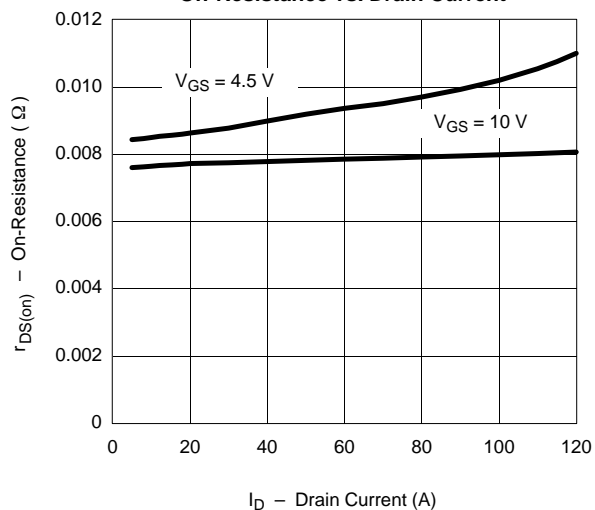
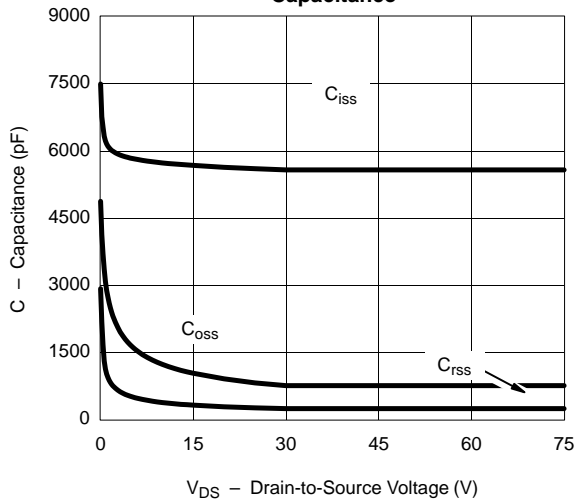
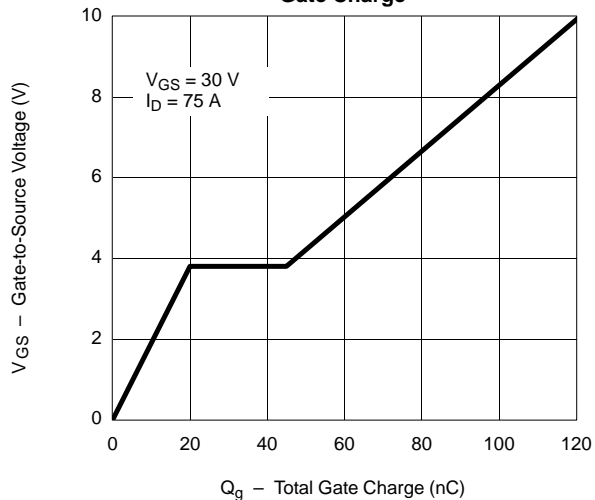


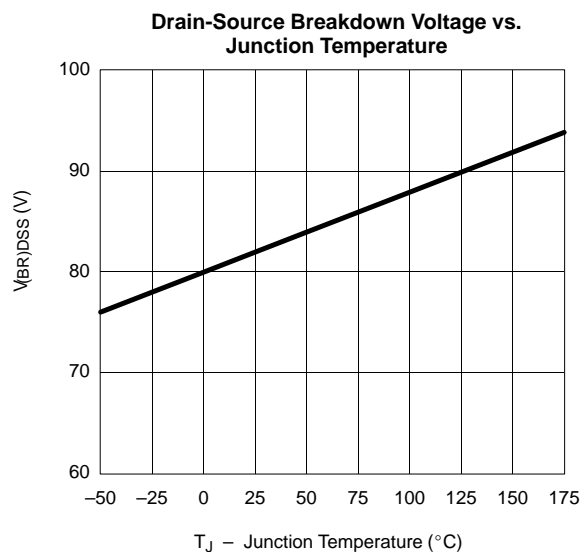
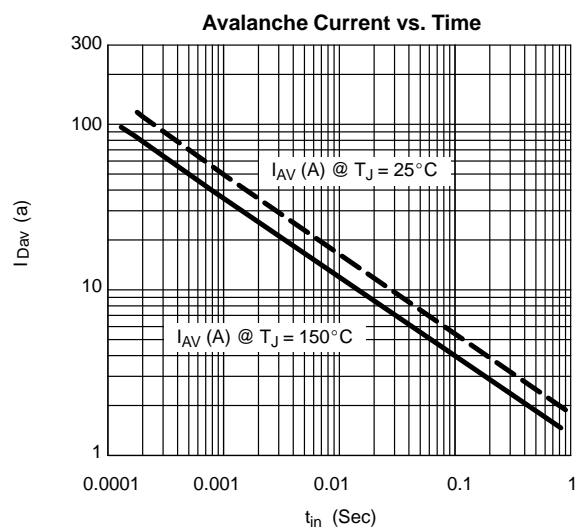
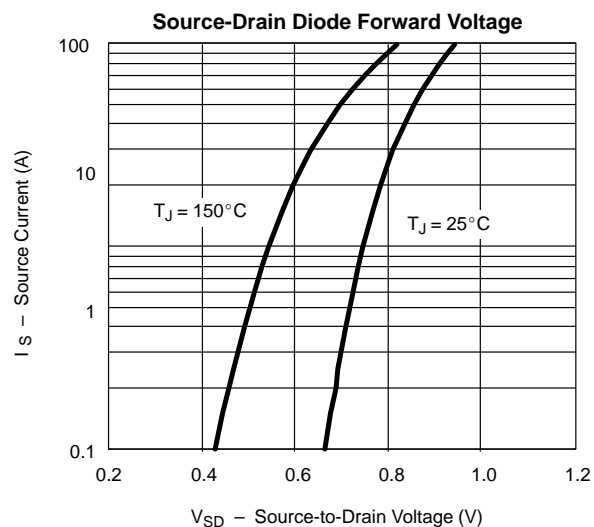
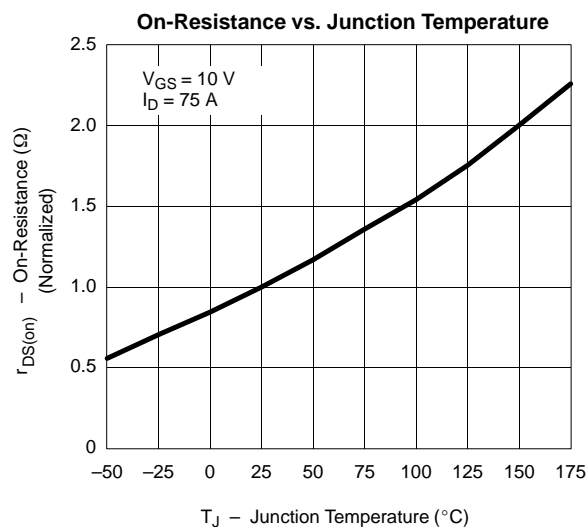
**SPECIFICATIONS (T<sub>J</sub> = 25°C UNLESS OTHERWISE NOTED)**

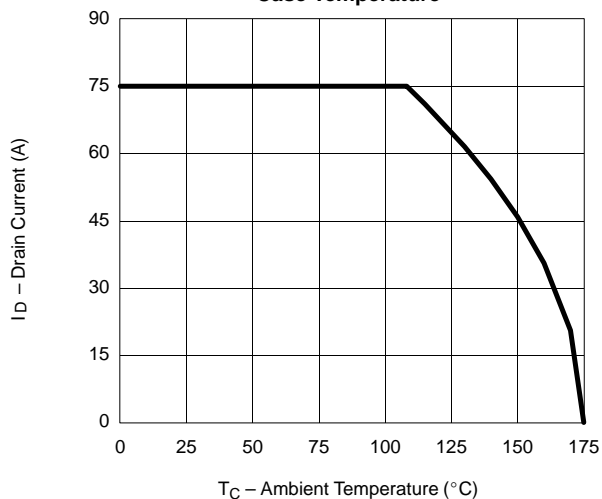
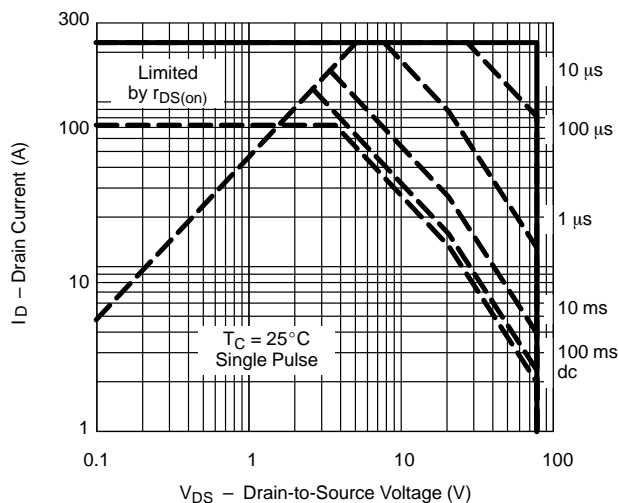
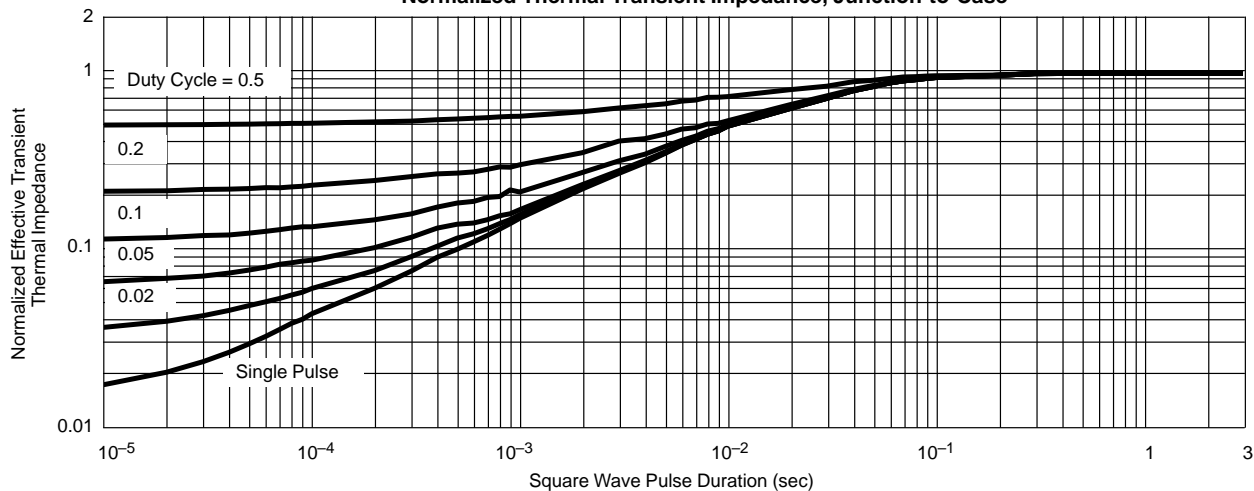
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>DS</sub> = 0 V, I <sub>D</sub> = 250 μA	75			V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1		3	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125°C			50	
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175°C			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	120			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.0076	0.009	Ω
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A			0.011	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125°C			0.016	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175°C			0.021	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	30			S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		5600		pF
Output Capacitance	C <sub>oss</sub>			820		
Reverse Transfer Capacitance	C <sub>rss</sub>			275		
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 75 A		121	150	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			20		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			25		
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 30 V, R <sub>L</sub> = 0.47 Ω I <sub>D</sub> ≅ 75 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 2.5 Ω		11	20	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			10	20	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			107	200	
Fall Time <sup>c</sup>	t <sub>f</sub>			22	40	
Source-Drain Diode Ratings and Characteristics (T <sub>C</sub> = 25°C) <sup>b</sup>						
Continuous Current	I <sub>S</sub>				75	A
Pulsed Current	I <sub>SM</sub>				240	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 75 A, V <sub>GS</sub> = 0 V		1.0	1.3	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 75 A, di/dt = 100 A/μs		80	120	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>			4	9	A
Reverse Recovery Charge	Q <sub>rr</sub>				0.32	0.54

**Notes**

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.  
b. Guaranteed by design, not subject to production testing.  
c. Independent of operating temperature.

**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)****Output Characteristics****Transfer Characteristics****Transconductance****On-Resistance vs. Drain Current****Capacitance****Gate Charge**

**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**

**THERMAL RATINGS****Maximum Drain Current vs.  
Case Temperature****Safe Operating Area****Normalized Thermal Transient Impedance, Junction-to-Case**



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