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SiS439DNT

Vishay Siliconix

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static				•		
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = -250 μA	-30	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$		-	-22	-	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	- I _D = -250 μA	-	5	-	mV/°C
Gate-source threshold voltage	V _{GS(th})	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	-1.2	-	-2.8	V
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	-1	μA
		V _{DS} = -30 V, V _{GS} = 0 V, T _J = 55 °C	-	-	-10	
On-state drain current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	-20	-	-	А
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = -10 V, I _D = -14 A	-	0.0091	0.0110	Ω
		V _{GS} = -4.5 V, I _D = -11 A	-	0.0156	0.0195	
Forward transconductance ^a	g _{fs}	V _{DS} = -15 V, I _D = -14 A	-	37	-	S
Dynamic		•				
Input capacitance	C _{iss}	V _{DS} = -15 V, V _{GS} = 0 V, f = 1 MHz	-	2135	-	pF
Output capacitance	C _{oss}		-	395	-	
Reverse transfer capacitance	C _{rss}		-	335	-	
Total gate charge	Qg	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = -14.4 \text{ A}$	-	45	68	nC
		V _{DS} = -15 V, V _{GS} = -4.5 V, I _D = -14.4 A	-	23	35	
Gate-source charge	Q _{qs}		-	7.2	-	
Gate-drain charge	Q _{gd}		-	10.4	-	
Gate resistance	R _q	f = 1 MHz	0.4	1.8	3.6	Ω
Turn-on delay time	t _{d(on)}	$V_{DD} = -15 \text{ V}, \text{ R}_{\text{L}} = 1.5 \ \Omega$ $I_{D} \cong -10 \text{ A}, \text{ V}_{\text{GEN}} = -4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \ \Omega$	-	38	60	ns
Rise time	t _r		-	33	50	
Turn-off delay time	t _{d(off)}		-	27	41	
Fall time	t _f		-	12	20	
Turn-on delay time	t _{d(on)}	V_{DD} = -15 V, R _L = 1.5 Ω I _D \cong -10 A, V _{GEN} = -10 V, R _g = 1 Ω	-	14	21	
Rise time	tr		-	5	10	
Turn-off delay time	t _{d(off)}		-	36	54	
Fall time	t _f		-	6	12	
Drain-Source Body Diode Characterist	lics				<u>.</u>	
Continuous source-drain diode current	IS	T _C = 25 °C	-	-	-50	A
Pulse diode forward current (t = 100 µs)	I _{SM}		-	-	-90	
Body diode voltage	V _{SD}	I _F = -10 A	-	-0.8	-1.2	V
Body diode reverse recovery time	t _{rr}	I _F = -10 A, di/dt = 100 A/μs, T _J = 25 °C	-	22	35	ns
Body diode reverse recovery charge	Q _{rr}		-	15	25	nC
Reverse recovery fall time	t _a		-	13	-	ns
Reverse recovery rise time	t _b	1	-	9	-	

Notes

a. Pulse test: pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

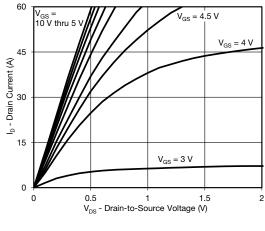
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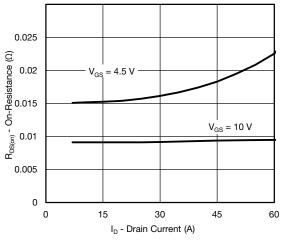


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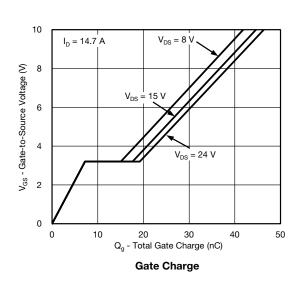
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

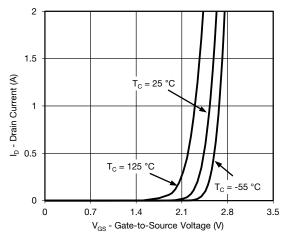


Output Characteristics

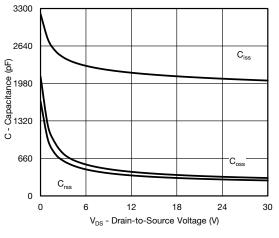


On-Resistance vs. Drain Current and Gate Voltage

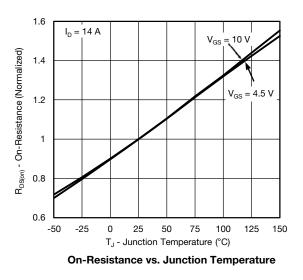




Transfer Characteristics



Capacitance



S17-1451-Rev. B, 18-Sep-17

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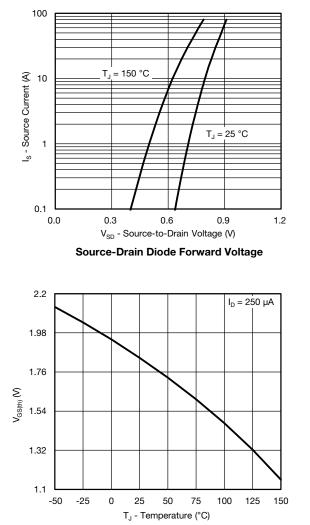
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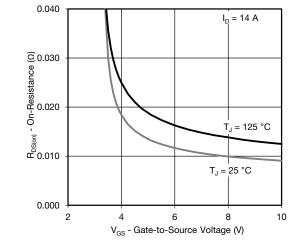
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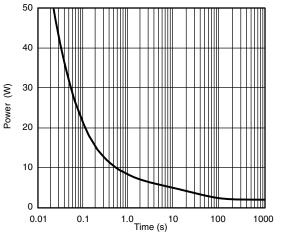
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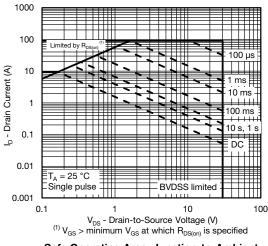




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient

4

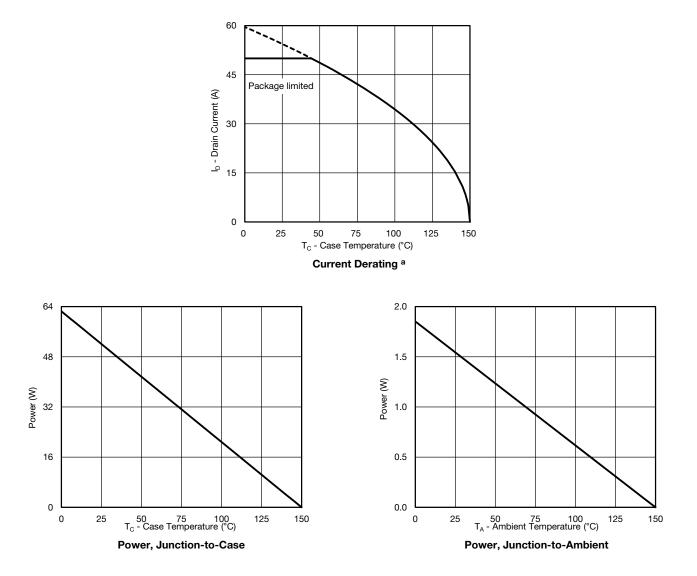
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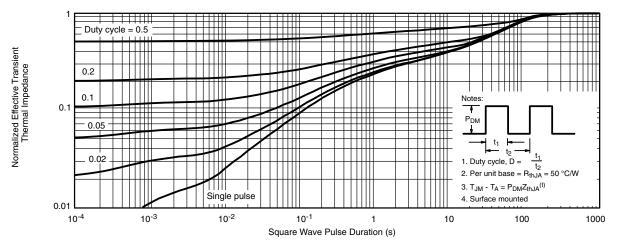
Note

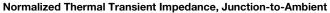
a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

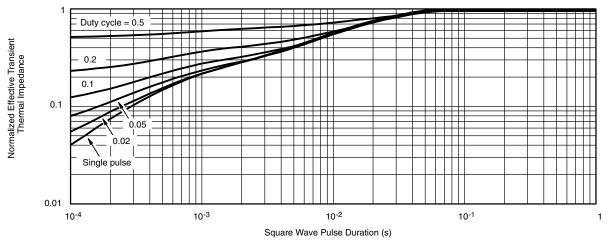


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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62869.

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