

# Maximum Ratings and Electrical Characteristics (@TA = +25°C unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	SF2DDF	SF2GDF	SF2JDF	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	200	400	600	V
Average Rectified Output Current @T <sub>T</sub> = +88°C (Note 5)	lo		2.0		Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	50		А	
Maximum Instantaneous Forward Voltage @ I <sub>F</sub> = 2A	VF	1.1	1.3	1.7	V
Maximum DC Reverse Current @ $T_A = +25$ °C at Rated DC Blocking Voltage @ $T_A = +100$ °C (Note 7)	I <sub>R</sub>	5 100		μА	
Typical Total Capacitance (Note 8)	C <sub>T</sub>		50		pF
Maximum Reverse Recovery Time (Note 9)	t <sub>RR</sub>		35		ns

## **Thermal Characteristics**

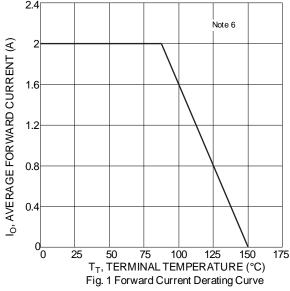
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Terminal (Note 6)	R <sub>ÐJT</sub>	30	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	R <sub>ÐJA</sub>	56	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes:

- 5.Device mounted on FR-4 substrate, 1" x 1", 2oz, single-sided, PCBs with 0.1" x 0.15" copper pad.
  6. Device mounted on FR-4 substrate, 0.4" x 0.5", 2oz, single-sided, PCBs with 0.2" x 0.25" copper pad.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Measured at 1.0MHZ and applied reverse voltage of 4.0V DC.

- 9. Measured with  $I_F$ =0.5A,  $I_R$ =1A,  $I_{RR}$ =0.25A.





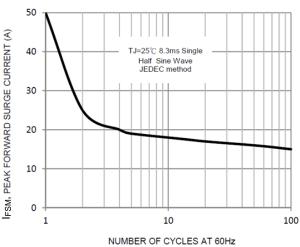


Fig 3. Maximum Non-Repetitive Forward Surge

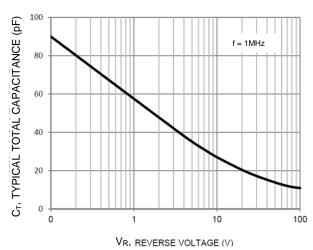


Fig 5. Typical Total Capacitance

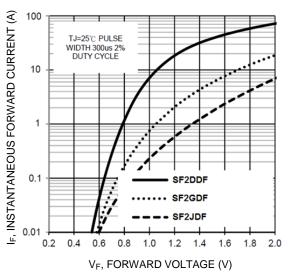
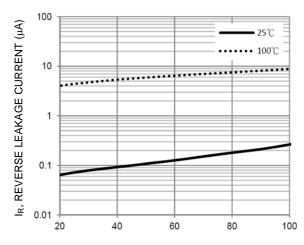


Fig 2. Typical Forward Characterstics



PERCENTAGE RATED PEAK REVERSE VOLTAGE (%)

Fig 4. Typical Reverse Charactersitcs

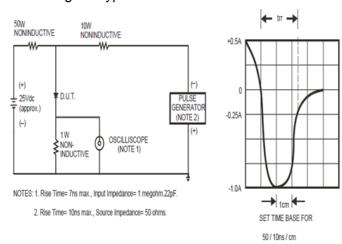


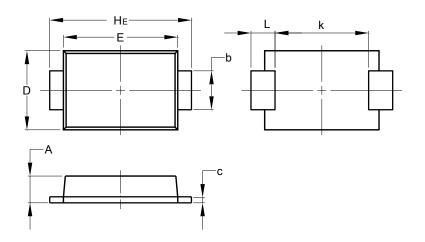
Fig 6. Reverse Recovery Time Characteristic and Test Circuit



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

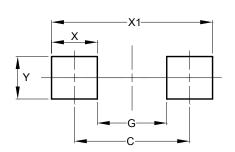
## **D-FLAT**



D-FLAT				
Dim	Min	Max		
Α	0.90	1.10		
b	1.25	1.65		
С	0.10	0.40		
D	2.25	2.95		
Е	3.95	4.60		
k	2.80	-		
HE	5.00	5.60		
L	0.50	1.30		
All Dimensions in mm				

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



**D-FLAT** 

Dimensions	Value	
Dimensions	(in mm)	
С	4.65	
G	2.80	
Х	1.85	
X1	6.50	
Y	1.70	



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