

1.5V Absolute Maximum Ratings (Above which the useful life may be impaired. For user guidelines only, not tested.)

Storage Temperature.....	-65°C to +150°C
V _{DD} Voltage	-0.5V to +2.6V
Output Voltage (max. 3.6V)	-0.5V to V _{DD} +0.5V
Input Voltage	-0.5V to 3.6V

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

1.5V DC Characteristics (Over Operating Range: V_{DD} = 1.5V ± 0.1V, T_A = -40° to 85°C)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. (2)	Max.	Units
V _{DD}	Supply Voltage			1.4	1.5	1.6	V
V _{IH}	Input HIGH Voltage	Logic HIGH level		0.65 x V _{DD}			V
V _{IL}	Input LOW Voltage	Logic LOW level		-0.3		0.35 x V _{DD}	
I _I	Input Current	V _{DD} = Max, V _{in} = V _{DD} or GND	I pins			15	μA
V _{OH}	Output High Voltage	V _{DD} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OH} = -2mA	1.05			V
			I _{OH} = -8mA	0.75			
V _{OL}	Output LOW Voltage	V _{DD} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OL} = 1mA			0.4	
			I _{OL} = 2mA			0.35	
			I _{OL} = 8mA			0.35	V

Notes:

- For Max. or Min. conditions, use appropriate operating range values.
- Typical values are at V_{CC} = 1.5V, +25°C ambient and maximum loading.

1.5V AC Characteristics (Over Operating Range: V_{DD} = 1.5V ± 0.1V, T_A = -40° to 85°C)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ	Max.	Units
F _{IN}	Input Frequency		0		200	MHz
t _R /t _F	CLKn Rise/Fall Time	20% to 80%			1.5	ns
t _{PLH} , t _{PHL} ⁽²⁾	Propagation Delay BUF_IN to CLKn	C _L = 5pF, 125 MHz Outputs are measured @ V _{dd} /2	1.0	1.5	3.0	ns
t _{SK(O)} ⁽³⁾	Output to Output Skew between any two outputs of the same device @ same transition				100	ps
t _{SK(T)} ⁽³⁾	Part to Part Skew between two identical outputs of different parts on the same board ⁽⁴⁾				300	
t _{dc_in}	Duty Cycle In @ 1ns edge rate		45		55	%
t _{dc_out}	Duty Cycle Out		40		60	

Notes:

- See test circuit and waveforms.
- Minimum limits are guaranteed but not tested on Propagation Delays.
- Skew measured at worse cast temperature (max. temp).
- Identical conditions: loading, transitions, supply voltage, temperature, package type and speed grade.

1.2V Absolute Maximum Ratings (Above which the useful life may be impaired. For user guidelines only, not tested.)

Storage Temperature.....	-65°C to +150°C
V _{DD} Voltage	-0.5V to +2.5V
Output Voltage (max 2.5V)	-0.5V to V _{DD} +0.5V
Input Voltage	-0.5V to 3.6V

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

1.2V DC Characteristics (Over Operating Range: V_{DD} = 1.2V ± 0.1V, T_A = -40° to 85°C)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. (2)	Max.	Units
V _{DD}	Supply Voltage			1.1	1.2	1.3	V
V _{IH}	Input HIGH Voltage	Logic HIGH level		0.65*V _{DD}			V
V _{IL}	Input LOW Voltage	Logic LOW level		-0.3		0.35*V _{DD}	
I _I	Input Current ⁽³⁾	V _{DD} = Max, V _{in} = V _{DD} or GND	I pin			15	μA
V _{OH}	Output High Voltage	V _{DD} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OH} = -2mA	0.85			V
			I _{OH} = -8mA	0.55			
V _{OL}	Output LOW Voltage	V _{DD} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OL} = 2mA			0.35	
			I _{OL} = -8mA			0.45	

Notes:

- For Max. or Min. conditions, use appropriate operating V_{DD} and T_A values.
- Typical values are at V_{CC} = 1.2V, +25°C ambient and maximum loading.

1.2V AC Characteristics (Over Operating Range: V_{DD} = 1.2V ± 0.1V, T_A = -40° to 85°C)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ	Max.	Units
F _{IN}	Input Frequency		0		200	MHz
t _R /t _F	CLKn Rise/Fall Time	20% to 80%			1.5	ns
t _{PLH} , t _{PHL} ⁽²⁾	Propagation Delay BUF_IN to CLKn	C _L = 5pF, 125 MHz Outputs are measured @ V _{DD} /2	1.0	2.0	3.5	ns
t _{SK(O)} ⁽³⁾	Output to Output Skew between any two outputs of the same device @ same transition				100	ps
t _{SK(T)} ⁽³⁾	Part to Part Skew between two identical outputs of different parts on the same board ⁽⁴⁾				300	
t _{dc_in}	Duty Cycle In @ 1ns edge rate		45		55	%
t _{dc_out}	Duty Cycle Out		40		60	

Notes:

- See test circuit and waveforms.
- Minimum limits are guaranteed but not tested on Propagation Delays.
- Skew measured at worse cast temperature (max. temp).
- Identical conditions: loading, transitions, supply voltage, temperature, package type and speed grade.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
I _{DDQ}	Quiescent Power Supply Current	V _{DD} = 1.5V	V _{IN} = GND or V _{DD}			10	μA
		V _{DD} = 1.2V				10	
I _{DD_TOT}	Total Power Supply Current	V _{DD} = 1.5V	All Outputs Toggling, C _L = 5pF, F _{IN} = 125MHz			15	mA
		V _{DD} = 1.2V				10	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics.
- Typical values are at V_{CC} = 1.2V or 1.5V, and +25°C ambient.

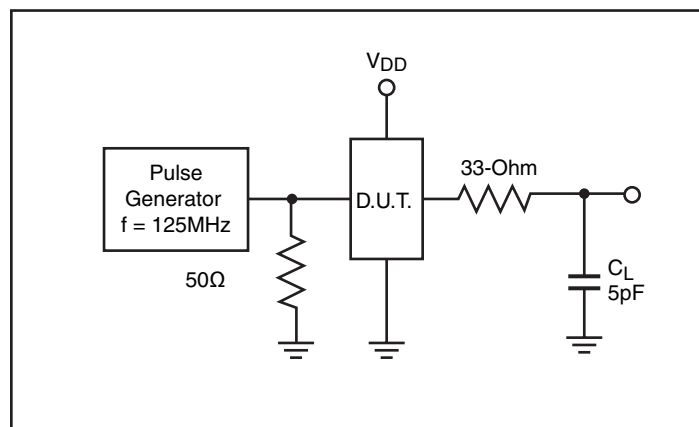
Capacitance (T_A = 25°C, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Typ	Max.	Units
C _{IN}	Input Capacitance	V _{IN} = 0V	2.0	4	pF
C _{OUT}	Output Capacitance	V _{OUT} = 0V	1.7	6	

Note:

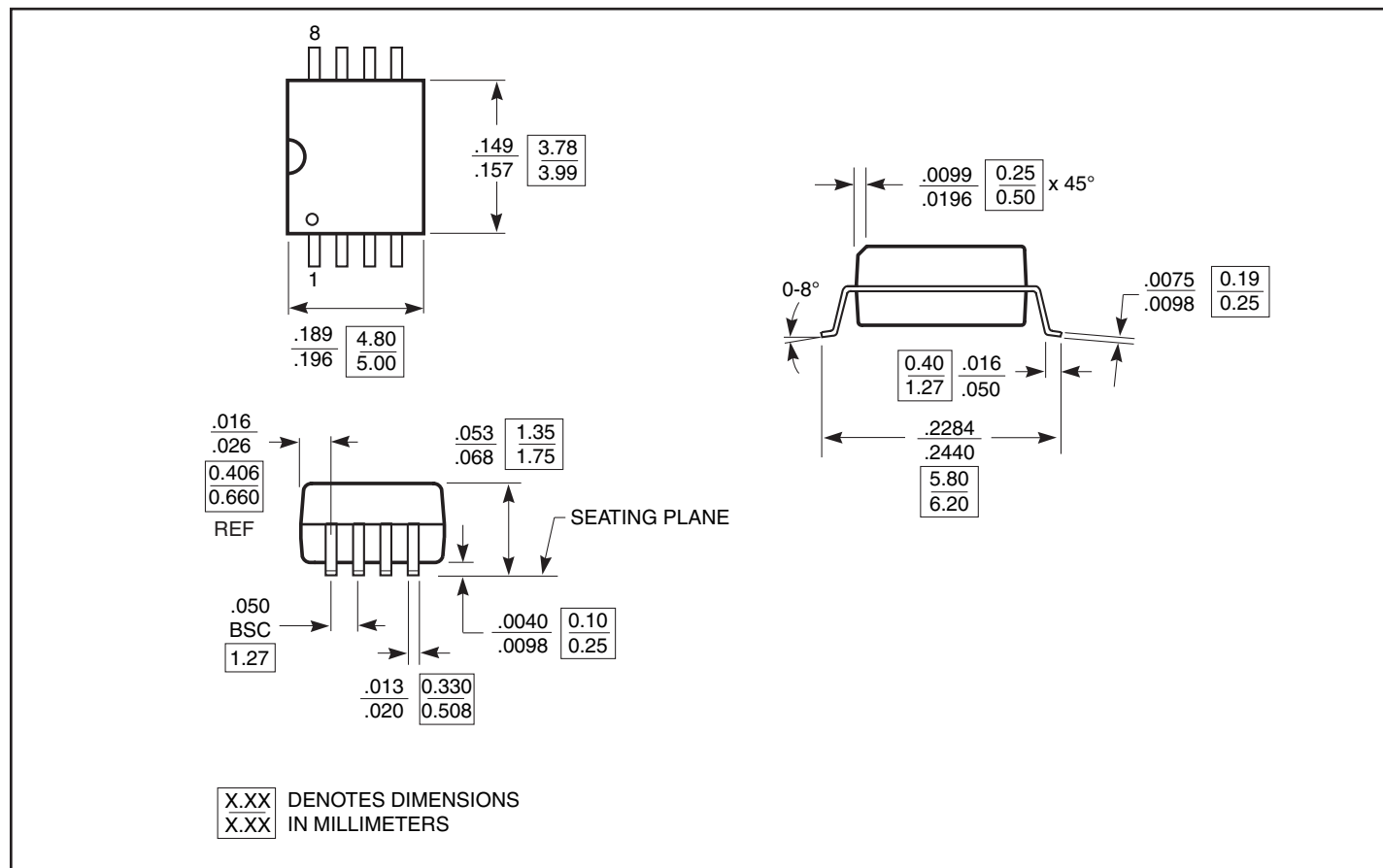
- This parameter is determined by device characterization but is not production tested.

Test Circuits for All Outputs



Definitions:

C_L = Load capacitance: includes jig and probe capacitance.

Packaging Mechanical: 8-Pin SOIC (W)

Ordering Information(1,2,3)

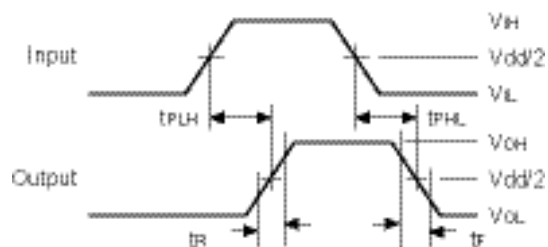
Ordering Code	Package Code	Package Type
PI6CL10804WE	W	Pb-free & Green, 8-pin 153-mil wide SOIC

Notes:

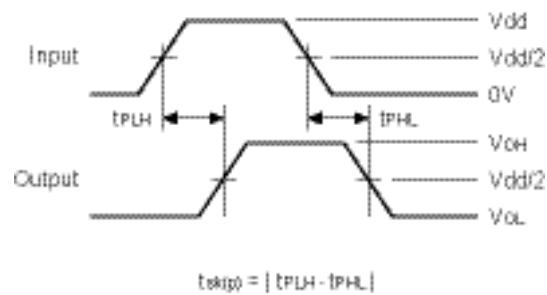
1. Thermal Characteristics can be found on the web at www.pericom.com/packaging/
2. E = Pb-free and Green
3. X suffix = Tape/Reel

Switching Waveforms

Propagation Delay



Pulse Skew – $t_{SK(P)}$



Output Skew – $t_{SK(O)}$

