

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	150	v
RMS Reverse Voltage	V <sub>R(RMS)</sub>	106	V
Average Rectified Output Current (See also Figure 5)	lo	4	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	180	А

## **Thermal Characteristics**

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point	$R_{\theta JS}$		2.0	°C/W
Thermal Resistance Junction to Ambient Air (Note 6) $T_A = +25^{\circ}C$	R <sub>θJA</sub>	90	—	°C/W
Thermal Resistance Junction to Ambient Air (Note 7) $T_A = +25^{\circ}C$	R <sub>θJA</sub>	60	—	°C/W
Thermal Resistance Junction to Ambient Air (Note 8) $T_A = +25^{\circ}C$	R <sub>0JA</sub>	40	—	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +175		°C

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 9)	V <sub>(BR)R</sub>	150			V	I <sub>R</sub> = 10μA
Forward Voltage	V <sub>F</sub>		0.71 0.57 0.77 0.63	0.76 0.64 0.81 0.70	V	$\begin{split} I_F &= 4A, \ T_J = +25^{\circ}C \\ I_F &= 4A, \ T_J = +125^{\circ}C \\ I_F &= 8A, \ T_J = +25^{\circ}C \\ I_F &= 8A, \ T_J = +125^{\circ}C \end{split}$
Reverse Leakage Current (Note 9)	I <sub>R</sub>		0.3 0.35 0.4	10 0.8 4.5	μA mA mA	$T_{J} = +25^{\circ}C, V_{R} = 150V$ $T_{J} = +125^{\circ}C, V_{R} = 100V$ $T_{J} = +125^{\circ}C, V_{R} = 150V$

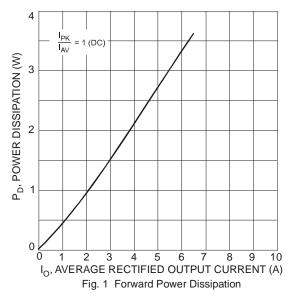
Notes:

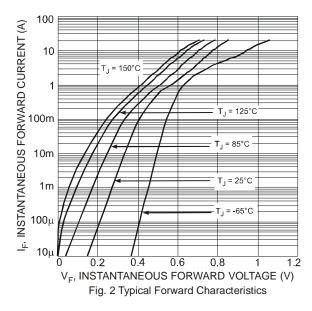
6. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.

7. Polyimide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.

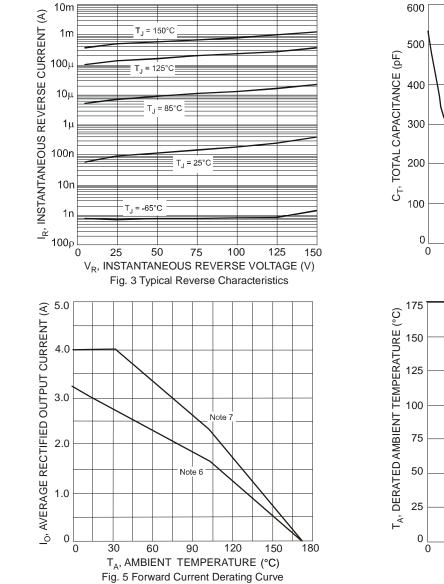
8. Polyimide PCB, 2 oz. Copper. Cathode pad dimensions 9.4mm x 7.2mm. Anode pad dimensions 2.7mm x 1.6mm.

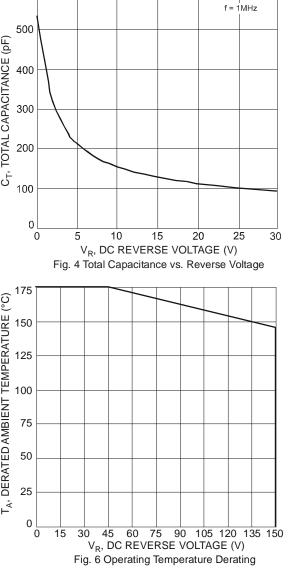
9. Short duration pulse test used to minimize self-heating effect.









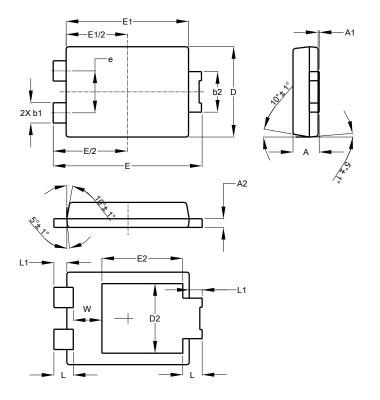




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5

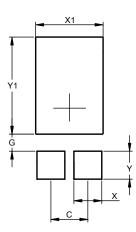


PowerDI5				
Dim	Min	Max	Тур	
Α	1.05	1.15	1.10	
A1	0.00	0.05		
A2	0.33	0.43	0.381	
b1	0.80	0.99	0.89	
b2	1.70	1.88	1.78	
D	3.90	4.05	3.966	
D2			3.054	
E	6.40	6.60	6.504	
е			1.84	
E1	5.30	5.45	5.37	
E2			3.549	
L	0.75	0.95	0.85	
L1	0.50	0.65	0.57	
W	1.10	1.41	1.255	
All Dimensions in mm				

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI5



Dimensions	Value (in mm)
С	1.840
G	0.852
Х	1.390
X1	3.360
Ý	1.400
Y1	4.860



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