Low-Dropout, 300mA Linear Regulators in SOT23

ABSOLUTE MAXIMUM RATINGS

IN, $\overline{SHDN}$, POK, to GND ....-0.3V to +7V
OUT, BP to GND ....-0.3 to (V_{IN} + 0.3V)
Output Short-Circuit Duration ....Continuous
Continuous Power Dissipation (T_A = +70°C)
5-Pin SOT23 (derate 9.1mW/°C above +70°C)...........727mW

Operating Temperature Range ....-40°C to +85°C
Junction Temperature ....+150°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10s) ....+500°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{IN} = V_{OUT} + 1V, $\overline{SHDN}$ = IN, T_A = -40°C to +85°C, unless otherwise noted.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage	V_{IN}		2.5		5.5	V
Input Undervoltage Lockout		V_{IN} rising (2% typical hysteresis)	2.15		2.4	V
Output Voltage Accuracy		T_A = +25°C, I_{OUT} = 100mA	-1.2		+1.2	%
		I_{OUT} = 100µA to 300mA, T_A = 0°C to +85°C	-2		+2	
		I_{OUT} = 100µA to 300mA	-3		+3	
Maximum Output Current		Continuous	300			mA
		10ms pulse		500		
Current Limit		V_{OUT} = 0	300			mA
		V_{OUT} > 93% of nominal value	420			
Ground-Pin Current		No load		55	100	µA
		I_{OUT} = 300mA		65		
Dropout Voltage (Note 2)		V_{OUT} = +3.3V, I_{OUT} = 1mA		0.5		mV
		V_{OUT} = +3.3V, I_{OUT} = 200mA		100	200	
		V_{OUT} = +3.3V, I_{OUT} = 300mA		150		
Line Regulation		V_{IN} = 2.5V or (V_{OUT} + 0.4V) to 5.5V, I_{OUT} = 5mA	-0.15	0	0.15	%/V
Output Noise		MAX8887: 10Hz to 100kHz, C_{BP} = 0.01µF, C_{OUT} = 2.2µF, ESR_{COUT} < 0.1Ω		42		$µV_{RMS}$
		MAX8888: 10Hz to 100kHz, C_{OUT} = 2.2µF, ESR_{COUT} < 0.1Ω		360		
PSRR		MAX8887: f < 1kHz, C_{BP} = 0.01µF, C_{OUT} = 4.7µF, ESR_{COUT} < 0.1Ω		60		dB
		MAX8888: f < 1kHz, C_{OUT} = 2.2µF, ESR_{COUT} < 0.1Ω		40		

ELECTRICAL CHARACTERISTICS (continued)

(V_{IN} = V_{OUT} + 1V, $\overline{SHDN}$ = IN, T_A = -40°C to +85°C, unless otherwise noted.) (Note 1)

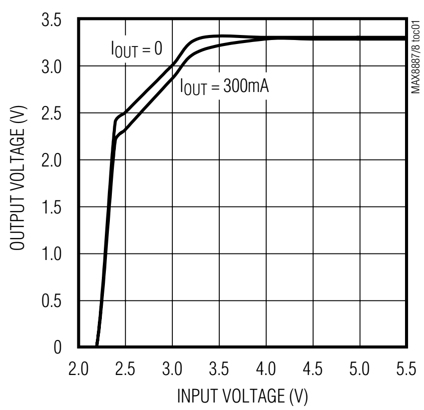
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
SHUTDOWN						
Shutdown Supply Current		$\overline{SHDN}$ = GND, V_{IN} = 5.5V		0.1	2	µA
$\overline{SHDN}$ Input Threshold	V_{IH}	2.5V ≤ V_{IN} ≤ 5.5V	1.6			V
	V_{IL}	2.5V ≤ V_{IN} ≤ 5.5V			0.6	
$\overline{SHDN}$ Input Bias Current		$\overline{SHDN}$ = IN or GND		10	100	nA
OUT Discharge Resistance in Shutdown		$\overline{SHDN}$ = GND		650	1100	Ω
POK (MAX8888 ONLY)						
POK Trip Level, Referred to OUT Set Point		V_{OUT} falling (1% typical hysteresis)	90	92.5	95	%
Operating IN Voltage Range for Valid POK			1.0		5.5	V
POK Output Voltage Low	V_{OL}	I_{SINK} = 1mA			0.1	V
POK Output Leakage Current		V_{POK} = 5.5V, $\overline{SHDN}$ = IN			100	nA
THERMAL PROTECTION						
Thermal Shutdown Temperature				170		°C
Thermal Shutdown Hysteresis				20		°C

Note 1: All parts are 100% tested at T_A = +25°C. Limits over the operating temperature range are guaranteed by design.
Note 2: Typical and maximum dropout voltage for different output voltages are shown in the *Typical Operating Characteristics* curves.

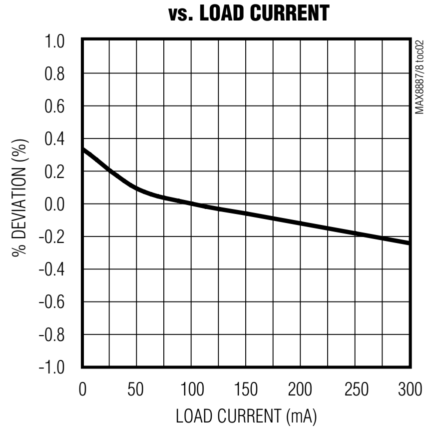
Typical Operating Characteristics

(*Typical Operating Circuit*, T_A = +25°C, unless otherwise noted.)

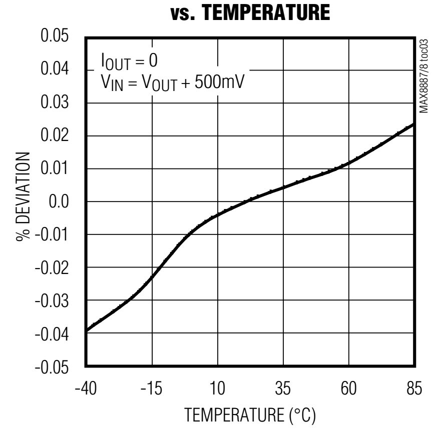
OUTPUT VOLTAGE vs. INPUT VOLTAGE



OUTPUT VOLTAGE ACCURACY vs. LOAD CURRENT



OUTPUT VOLTAGE ACCURACY vs. TEMPERATURE

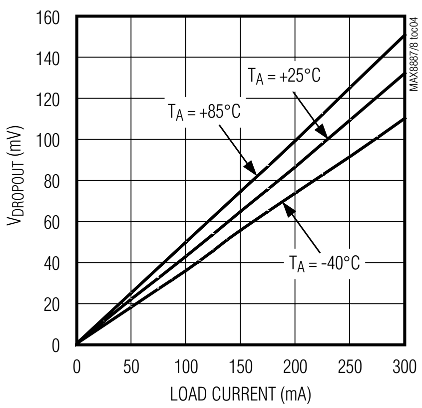


Low-Dropout, 300mA Linear Regulators in SOT23

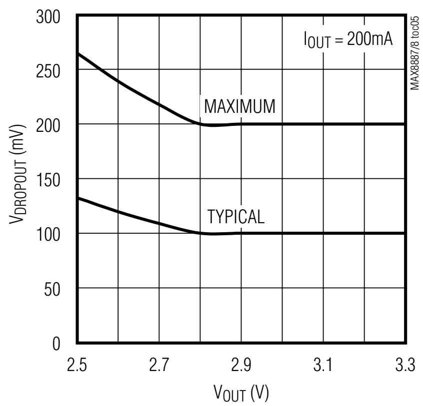
Typical Operating Characteristics (continued)

(*Typical Operating Circuit*, T_A = +25°C, unless otherwise noted.)

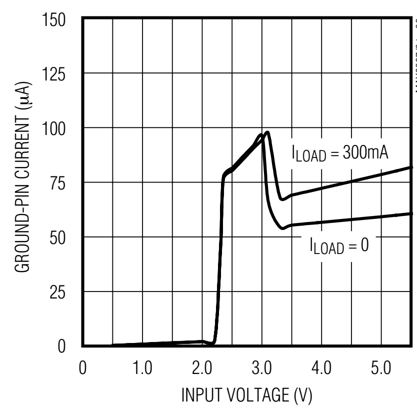
DROPOUT VOLTAGE vs. LOAD CURRENT



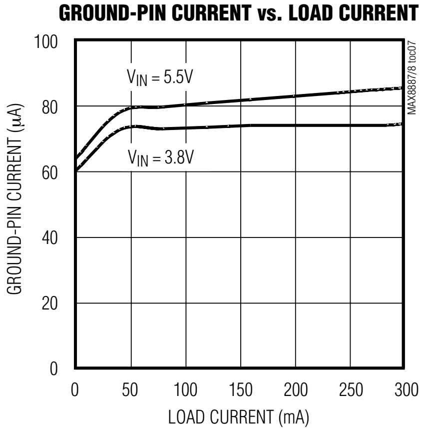
DROPOUT VOLTAGE vs. OUTPUT VOLTAGE



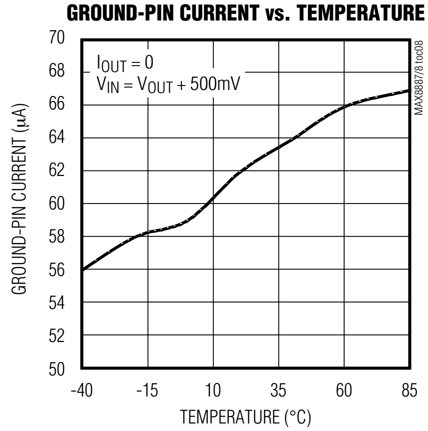
GROUND-PIN CURRENT vs. INPUT VOLTAGE



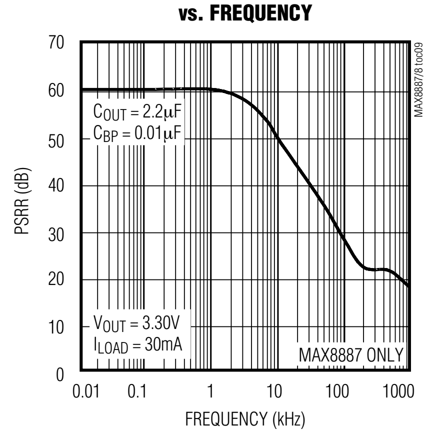
GROUND-PIN CURRENT vs. LOAD CURRENT



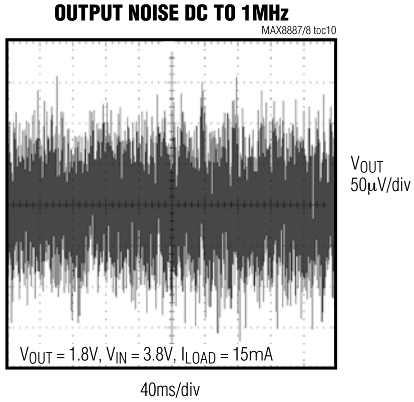
GROUND-PIN CURRENT vs. TEMPERATURE



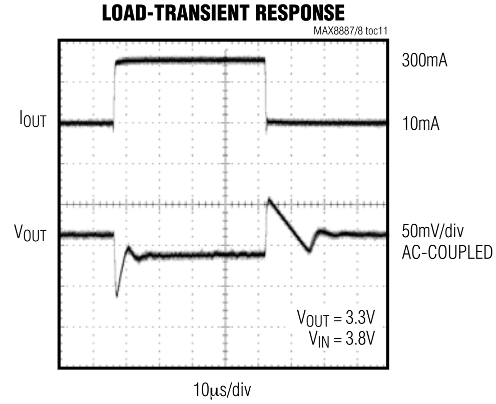
POWER-SUPPLY REJECTION RATIO vs. FREQUENCY



MAX8887
OUTPUT NOISE DC TO 1MHz



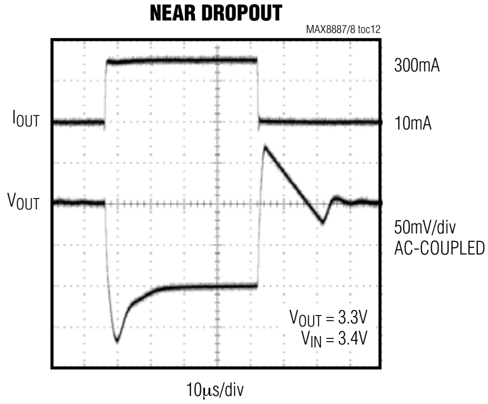
LOAD-TRANSIENT RESPONSE



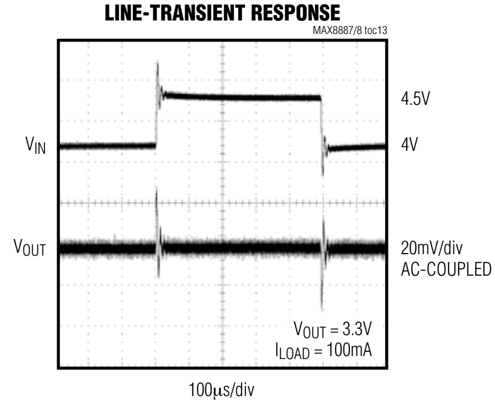
Typical Operating Characteristics (continued)

(*Typical Operating Circuit*, T_A = +25°C, unless otherwise noted.)

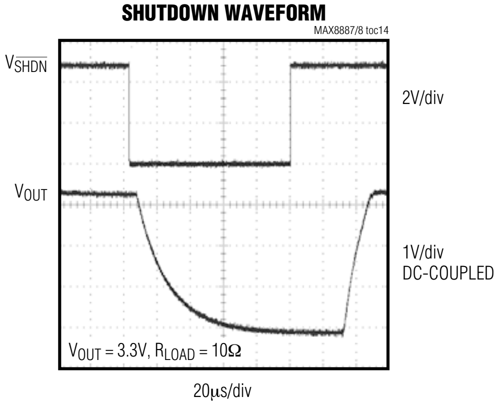
LOAD-TRANSIENT RESPONSE NEAR DROPOUT



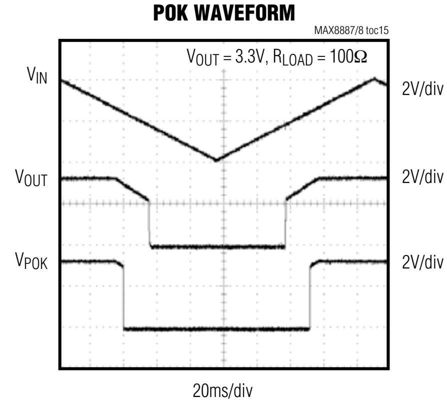
LINE-TRANSIENT RESPONSE



SHUTDOWN WAVEFORM



POK WAVEFORM



Pin Description

MAX8887	MAX8888	NAME	FUNCTION
1	1	IN	Regulator Input. Supply voltage can range from 2.5V to 5.5V. Bypass with 2.2µF capacitor to GND (see the *Capacitor Selection and Regulator Stability* section).
2	2	GND	Ground
3	3	$\overline{SHDN}$	Active-Low Shutdown Input. A logic low reduces the supply current to below 0.1µA. In shutdown, POK and OUT are driven low. Connect to IN for normal operation.
—	4	POK	Open-Drain Active-Low POK Output. POK remains low while the output voltage (V_{OUT}) is below the reset threshold. Connect a 100kΩ pullup resistor to OUT to obtain a logic level output. POK is driven low in shutdown. If not used, leave this pin unconnected.
4	—	BP	Reference Bypass. Bypass with a low-leakage 0.01µF ceramic capacitor.
5	5	OUT	Regulator Output. Sources up to 300mA guaranteed. Bypass with 2.2µF (<0.2Ω typical ESR) ceramic capacitor to GND.

Low-Dropout, 300mA Linear Regulators in SOT23

Detailed Description

The MAX8887/MAX8888 are low-dropout, low-quiescent-current linear regulators designed primarily for battery-powered applications. The devices supply loads up to 300mA and are available in several fixed output voltages in the 1.5V to 3.3V range. The MAX8887 is optimized for low-noise operation, while the MAX8888 includes an open-drain POK output flag. As illustrated in Figure 1, the MAX8888 consists of a 1.25V reference, error amplifier, p-channel pass transistor, and internal feedback voltage divider.

Internal p-Channel Pass Transistor

The MAX8887/MAX8888 feature a 0.5Ω p-channel MOSFET pass transistor. Unlike similar designs using PNP pass transistors, p-channel MOSFETs require no base drive, which reduces quiescent current. PNP-based regulators also waste considerable current in dropout when the pass transistor saturates and uses high base drive currents under large loads. The MAX8887/MAX8888 do not suffer from these problems and consume only 55µA of quiescent current under heavy loads as well as in dropout.

Output Voltage Selection

The MAX8887/MAX8888 are supplied with various factory-set output voltages ranging from 1.5V to 3.3V. The part number's two-digit suffix identifies the nominal output voltage. For example, the MAX8887EZK33 has a preset output voltage of 3.3V (see the *Ordering Information*).

Shutdown

Drive $\overline{SHDN}$ low to enter shutdown. During shutdown, the output is disconnected from the input and supply current drops to 0.1µA. When in shutdown, POK and OUT are driven low. $\overline{SHDN}$ can be pulled as high as 6V, regardless of the input and output voltages.

Power-OK Output

The power-OK output (POK) pulls low when OUT is less than 93% of the nominal regulation voltage. Once OUT exceeds 93% of the nominal voltage, POK goes high impedance. POK is an open-drain n-channel output. To obtain a logic level output, connect a pullup resistor from POK to OUT. A 100kΩ resistor works well for most applications. POK can be used as a power-on-reset (POR) signal to a microcontroller (µC) or to drive other logic. Adding a capacitor from POK to ground creates POK delay. When the MAX8887 is shut down, POK is held low independent of the output voltage. If unused, leave POK grounded or unconnected.

Current Limit

The MAX8887/MAX8888 monitor and control the pass transistor's gate voltage, limiting the output current to 0.8A (typ). This current limit is reduced to 500mA (typ) when the output voltage is below 93% of the nominal value to provide foldback current limiting.

Thermal Overload Protection

Thermal overload protection limits total power dissipation in the MAX8887/MAX8888. When the junction temperature exceeds T_J =+170°C, a thermal sensor turns off the pass transistor, allowing the device to cool. The thermal sensor turns the pass transistor on again after the junction temperature cools by 20°C, resulting in a pulsed output during continuous thermal overload conditions. Thermal overload protection protects the MAX8887/MAX8888 in the event of fault conditions. For continuous operation, do not exceed the absolute maximum junction-temperature rating of T_J=+150°C.

Operating Region and Power Dissipation

The MAX8887/MAX8888's maximum power dissipation depends on the thermal resistance of the IC package and circuit board. The temperature difference between the die junction and ambient air, and the rate of air flow. The power dissipated in the device is $P = I_{OUT} \times (V_{IN} - V_{OUT})$. The maximum allowed power dissipation is 727mW or:

$$P_{MAX} = (T_{J(MAX)} - T_A) / (\theta_{JC} + \theta_{CA})$$

where $T_{J(MAX)}$-T_A is the temperature difference between the MAX8887/MAX8888 die junction and the surrounding air; θ_{JC} is the thermal resistance from the junction to the case; and θ_{CA} is the thermal resistance from the case through PC board, copper traces, and other materials to the surrounding air.

Refer to Figure 2 for the MAX8887/MAX888 valid operating region.

Noise Reduction

For the MAX8887 only, an external 0.01µF bypass capacitor at BP creates a lowpass filter for noise reduction. The MAX8887 exhibits 42µV_{RMS} of output voltage noise with C_{BP} = 0.01µF and C_{OUT} = 2.2µF (see the *Typical Operating Characteristics*).

Applications Information

Capacitor Selection and Regulator Stability

Connect a 2.2µF ceramic capacitor between IN and ground and a 2.2µF ceramic capacitor between OUT and ground. The input capacitor (C_{IN}) lowers the source impedance of the input supply. Reduce noise and improve load-transient response, stability, and power-supply rejection by using a larger ceramic output capacitor such as 4.7µF.

The output capacitor's (C_{OUT}) equivalent series resistance (ESR) affects stability and output noise. Use output capacitors with an ESR of 0.1Ω or less to ensure

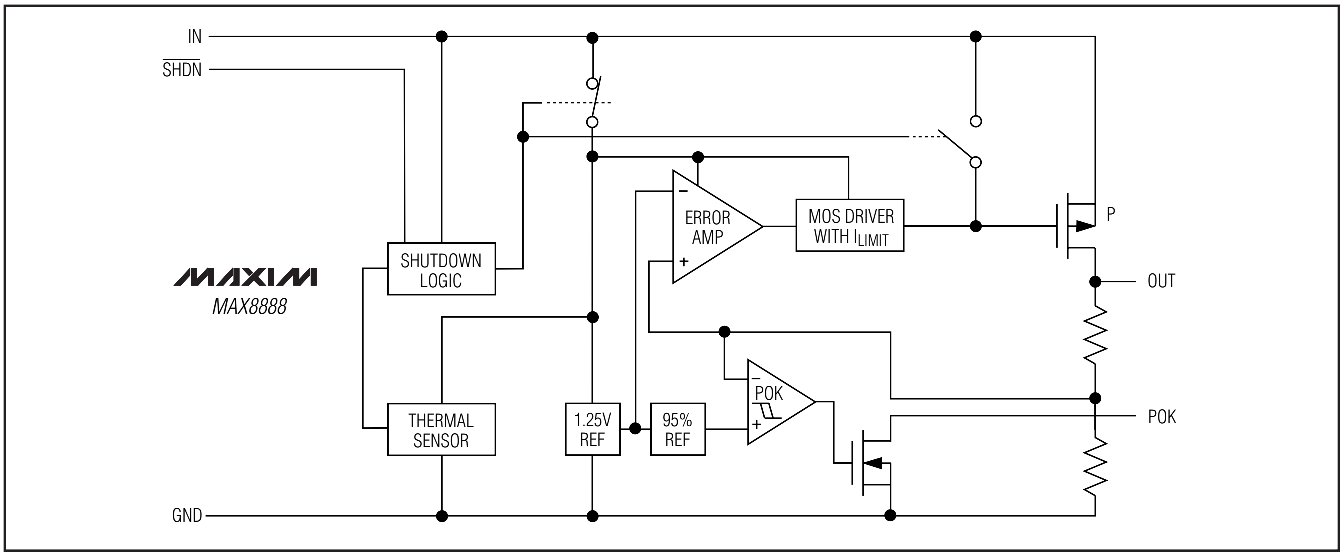


Figure 1. Functional Diagram

stability and optimum transient response. Surface-mount ceramic capacitors have very low ESR and are commonly available in values up to 10µF. Connect C_{IN} and C_{OUT} as close to the MAX8887/MAX8888 as possible to minimize the impact of PC board trace inductance.

Noise, PSRR, and Transient Response

The MAX8887/MAX8888 are designed to operate with low dropout voltages and low quiescent currents in battery-powered systems while still maintaining excellent noise, transient response, and AC rejection. See the *Typical Operating Characteristics* for a plot of power-supply rejection ratio (PSRR) versus frequency. When operating from noisy sources, improved supply-noise rejection and transient response can be achieved by increasing the values of the input and output bypass capacitors and through passive filtering techniques.

Input-Output (Dropout) Voltage

A regulator's minimum input-to-output voltage differential (dropout voltage) determines the lowest usable supply voltage at which the output is regulated. In battery-powered systems, this determines the useful end-of-life battery voltage. The MAX8887/MAX8888 use a p-channel MOSFET pass transistor. Its dropout voltage is a function of drain-to-source on-resistance ($R_{DS(ON)}$) multiplied by the load current (see the *Typical Operating Characteristics*).

$$V_{DROPOUT} = V_{IN} - V_{OUT} = R_{DS(ON)} \times I_{OUT}$$

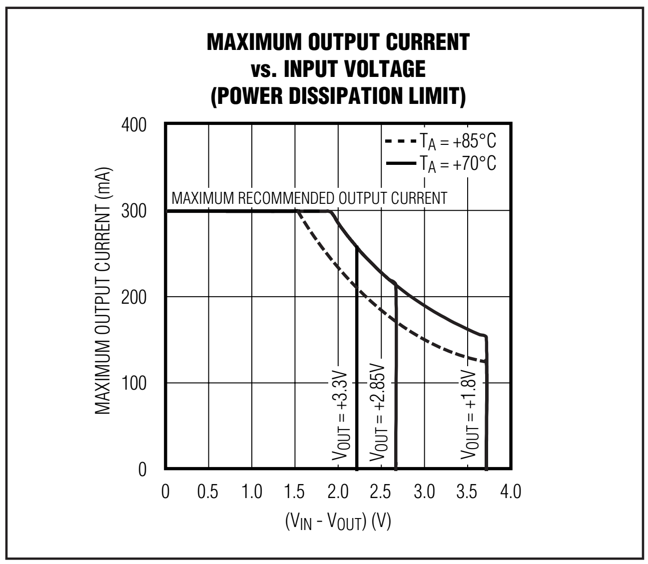


Figure 2. Power Operating Regions: Maximum Output Current vs. Input Voltage

Low-Dropout, 300mA Linear Regulators in SOT23

Chip Information

TRANSISTOR COUNT: 620
PROCESS: BiCMOS

Package Information

For the latest package outline information and land patterns, go to **www.maxim-ic.com/packages**. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
5 SOT23	Z5-1	**21-0113**

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	12/00	Initial release	—
1	12/01	Revised *Output Voltage Selection* section.	6
2	6/04	Revised *Absolute Maximum Ratings*.	2
3	11/06	Updated *Ordering Information* and package outlines.	1, 8
4	7/09	Revised *Ordering Information*.	1