

Regulated 5V Charge-Pump DC-DC Converter

ABSOLUTE MAXIMUM RATINGS

V_{IN} to GND-0.3V to +5.5V
 V_{OUT} to GND-0.3V to +5.5V
 $SHDN$ to GND-0.3V to (V_{IN} + 0.3V)
 I_{OUT} Continuous (Note 1).....120mA
 Continuous Power Dissipation (T_A = +70°C)
 Plastic DIP (derate 9.09mW/°C above +70°C)727mW
 SO (derate 5.88mW/°C above +70°C)471mW
 CERDIP (derate 8.00mW/°C above +70°C)640mW

Operating Temperature Ranges

MAX619C0°C to +70°C
 MAX619E-40°C to +85°C
 MAX619MJA-55°C to +125°C
 Storage Temperature Range-65°C to +165°C
 Lead Temperature (soldering, 10sec)+300°C

Note 1: The MAX619 is not short-circuit protected.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{IN} = 2V to 3.6V, C_1 = C_2 = 0.22 μ F, C_3 = C_4 = 10 μ F, T_A = T_{MIN} to T_{MAX} , unless otherwise noted. Typical values are at T_A = +25°C.)

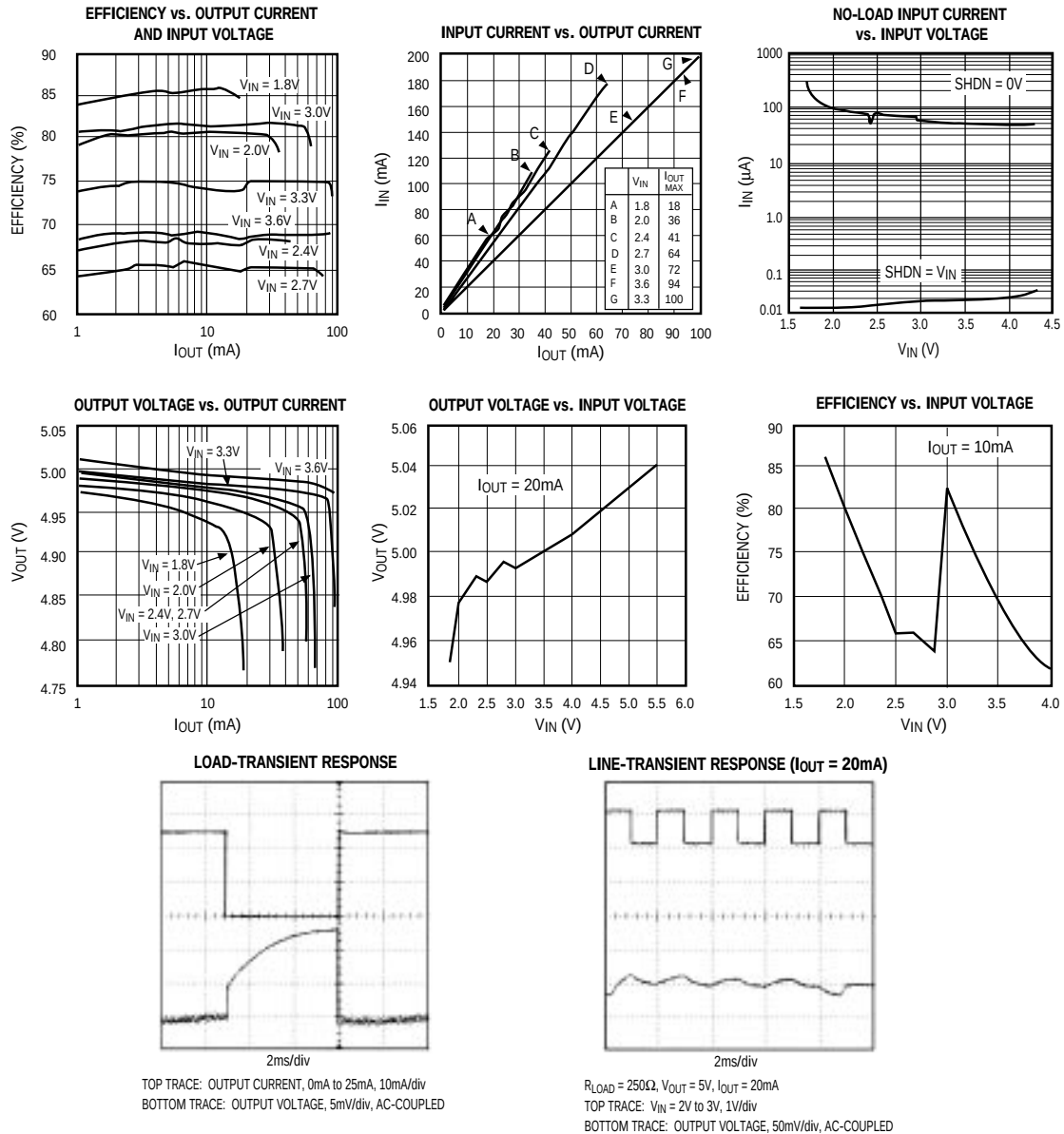
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage	V_{IN}		2		3.6	V
Output Voltage	V_{OUT}	$2.0V \leq V_{IN} \leq 3.6V$, $0mA \leq I_{OUT} \leq 20mA$	4.8	5.0	5.2	V
		$3.0V \leq V_{IN} \leq 3.6V$, $0mA \leq I_{OUT} \leq 50mA$, MAX619C				
		$3.0V \leq V_{IN} \leq 3.6V$, $0mA \leq I_{OUT} \leq 45mA$, MAX619E				
		$3.0V \leq V_{IN} \leq 3.6V$, $0mA \leq I_{OUT} \leq 40mA$, MAX619M				
Output Ripple	V_{RIPPLE}	No load to full load		100		mV
No-Load Supply Current	I_{IN}	$2V \leq V_{IN} \leq 3.6V$, $I_{OUT} = 0mA$		75	170	μA
Shutdown Supply Current		$2V \leq V_{IN} \leq 3.6V$, $I_{OUT} = 0mA$, $V_{SHDN} = V_{IN}$		0.02	1	μA
					10	
Efficiency	Eff	$V_{IN} = 3V$, $I_{OUT} = 20mA$		82		%
		$V_{IN} = 3V$, $I_{OUT} = 30mA$		82		
		$V_{IN} = 2V$, $I_{OUT} = 20mA$		80		
Switching Frequency		At full load		500		kHz
SHDN Input Threshold	V_{IH}		$0.7 \times V_{IN}$			V
	V_{IL}			0.4		
SHDN Input Current	I_{IH}	$V_{SHDN} = V_{IN}$			± 1	μA
					± 10	

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Typical Operating Characteristics

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

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Pin Description

PIN	NAME	FUNCTION
1	C1+	Positive Terminal for C1
2	IN	Input Supply Voltage
3	OUT	+5V Output Voltage. $V_{OUT} = 0V$ when in shutdown mode.
4	C2+	Positive Terminal for C2
5	C2-	Negative Terminal for C2
6	GND	Ground
7	SHDN	Active-High CMOS Logic-Level Shutdown Input
8	C1-	Negative Terminal for C1

Detailed Description

Operating Principle

The MAX619 provides a regulated 5V output from a 2V to 3.6V (two battery cells) input. Internal charge pumps and external capacitors generate the 5V output, eliminating the need for inductors. The output voltage is regulated to $5V \pm 4\%$ by a pulse-skipping controller that turns on the charge pump when the output voltage begins to droop.

To maintain the greatest efficiency over the entire input voltage range, the MAX619's internal charge pump operates as a voltage doubler when V_{IN} ranges from 3.0V to 3.6V, and as a voltage tripler when V_{IN} ranges from 2.0V to 2.5V. When V_{IN} ranges from 2.5V to 3.0V,

the MAX619 switches between doubler and tripler mode on alternating cycles, making a $2.5 \times V_{IN}$ charge pump. To further enhance efficiency over the input range, an internal comparator selects the higher of V_{IN} or V_{OUT} to run the MAX619's internal circuitry. Efficiency with $V_{IN} = 2V$ and $I_{OUT} = 20mA$ is typically 80%.

Figure 1 shows a detailed block diagram of the MAX619. In tripler mode, when the S1 switches close, the S2 switches open and capacitors C1 and C2 charge up to V_{IN} . On the second half of the cycle, C1 and C2 are connected in series between IN and OUT when the S1 switches open and the S2 switches close, as shown in Figure 1. In doubler mode, only C2 is used.

During one oscillator cycle, energy is transferred from the input to the charge-pump capacitors, and then from the charge-pump capacitors to the output capacitor and load. The number of cycles within a given time frame increases as the load increases or as the input supply voltage decreases. In the limiting case, the charge pumps operate continuously, and the oscillator frequency is nominally 500kHz.

Shutdown Mode

The MAX619 enters low-power shutdown mode when SHDN is a logic high. SHDN is a CMOS-compatible input. In shutdown mode, the charge-pump switching action is halted, OUT is disconnected from IN, and V_{OUT} falls to 0V. Connect SHDN to ground for normal operation. When $V_{IN} = 3.6V$, V_{OUT} typically reaches 5V in 0.5ms under no-load conditions after SHDN goes low.

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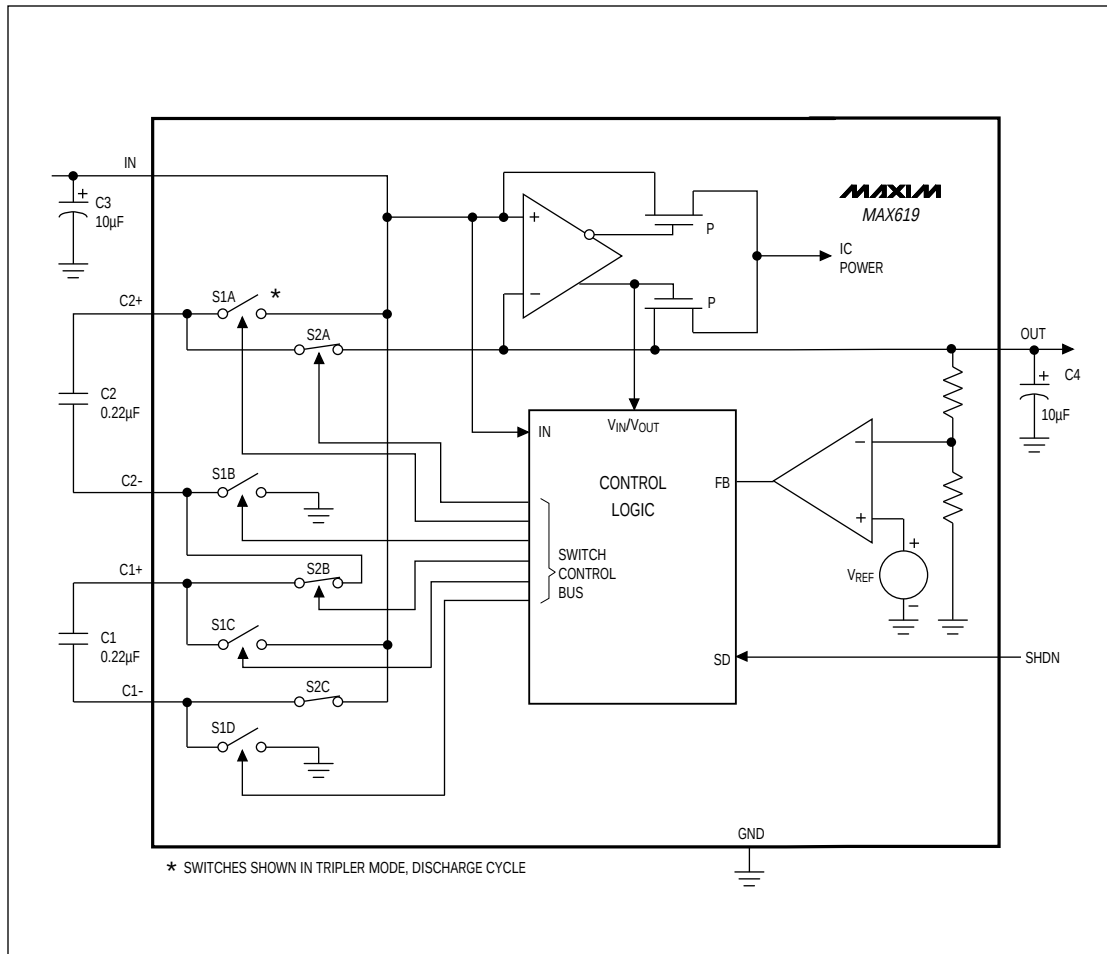


Figure 1. Block Diagram

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Applications Information

Capacitor Selection

Charge-Pump Capacitors C1 and C2

The values of charge-pump capacitors C1 and C2 are critical to ensure adequate output current and avoid excessive peak currents. Use values in the range of 0.22 μ F to 1.0 μ F. Larger capacitors (up to 50 μ F) can be used, but larger capacitors will increase output ripple. Ceramic or tantalum capacitors are recommended.

Input and Output Capacitors, C3 and C4

The type of input bypass capacitor (C3) and output filter capacitor (C4) used is not critical, but it does affect performance. Tantalums, ceramics, or aluminum electrolytics are suggested. For smallest size, use Sprague 595D106X0010A2 surface-mount capacitors, which measure 3.7mm x 1.8mm (0.146in x 0.072in). For lowest ripple, use large, low effective-series-resistance (ESR) ceramic or tantalum capacitors. For lowest cost, use aluminum electrolytic or tantalum capacitors.

Figure 2 shows the component values for proper operation using minimal board space. The input bypass capacitor (C3) and output filter capacitor (C4) should both be at least 10 μ F when using aluminum electrolytics or Sprague's miniature 595D series of tantalum chip capacitors.

When using ceramic capacitors, the values of C3 and C4 can be reduced to 2 μ F and 1 μ F, respectively. If the input supply source impedance is very low, C3 may not be necessary.

Many capacitors exhibit 40% to 50% variation over temperature. Compensate for capacitor temperature coefficient by selecting a larger nominal value to ensure proper operation over temperature. Table 1 lists capacitor suppliers.

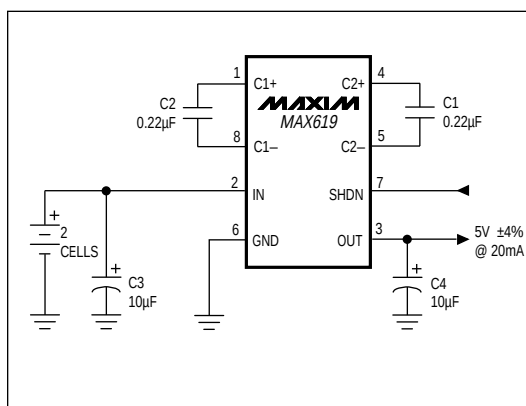


Figure 2. Two-Cell to 5V Application Circuit

Table 1. Capacitor Suppliers

SUPPLIER	PHONE NUMBER	FAX NUMBER	CAPACITOR	CAPACITOR TYPE*
Murata Erie	(814) 237-1431	(814) 238-0490	GRM42-6Z5U10M50	0.1 μ F ceramic (SM)
			GRM42-6Z5U22M50	0.22 μ F ceramic (SM)
			RPI123Z5U105M50V	1.0 μ F ceramic (TH)
			RPE121Z5U104M50V	0.1 μ F ceramic (TH)
Sprague Electric (smallest size)	(603) 224-1961 (207) 327-4140	(603) 224-1430 (207) 324-7223	595D106X0010A2	10 μ F tantalum (SM)

* **Note:** (SM) denotes surface-mount component, (TH) denotes through-hole component.

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Layout Considerations

The MAX619's high oscillator frequency makes good layout important. A good layout ensures stability and helps maintain the output voltage under heavy loads. For best performance, use very short connections to the capacitors.

Paralleling Devices

Two MAX619s can be placed in parallel to increase output drive capability. The IN, OUT, and GND pins can be paralleled, but C1 and C2 pins cannot. The input bypass capacitor and output filter capacitor are, to some extent, shared when two circuits are paralleled. If the circuits are physically close together, it may be possible to use a single bypass and a single output capacitor, each with twice the value of the single circuit. If the MAX619s cannot be placed close together, use separate bypass and output capacitors. The amount of output ripple observed will determine whether single input bypass and output filter capacitors can be used.

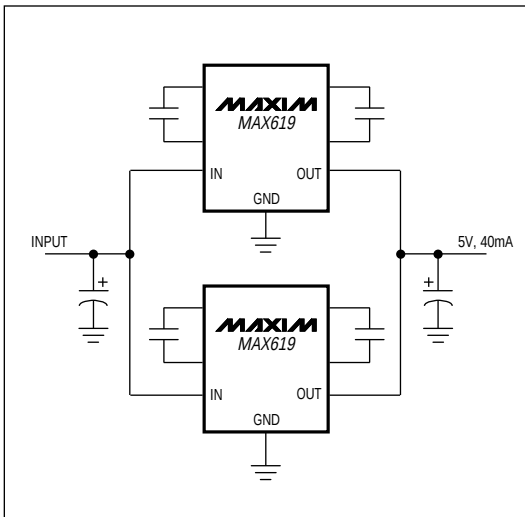
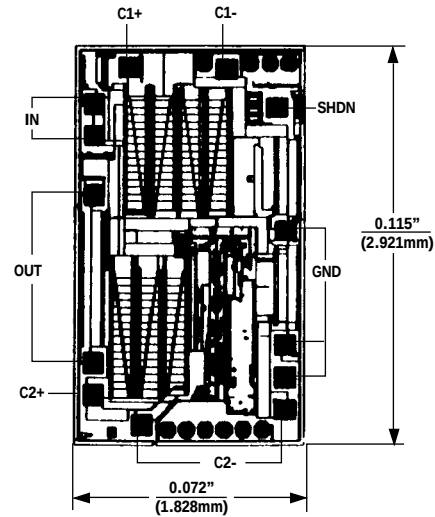


Figure 3. Paralleling Two MAX619s

Chip Topography



TRANSISTOR COUNT: 599;
SUBSTRATE CONNECTED TO GND.

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