Precision, 8-Channel/Dual 4-Channel, High-Performance, CMOS Analog Multiplexers

ABSOLUTE MAXIMUM RATINGS

Voltage Referenced to V-
V+ .....-0.3V, 44V
GND .....-0.3V, 25V
Digital Inputs, NO, COM (Note 1).....(V- - 2V) to (V+ + 2V) or 30mA, (whichever occurs first)
Continuous Current (any terminal).....30mA
Peak Current, NO or COM
(pulsed at 1ms, 10% duty cycle max) 100mA

Continuous Power Dissipation (T_A = +70°C)
Plastic DIP (derate 10.53mW/°C above +70°C) 842mW
Narrow SO (derate 8.70mW/°C above +70°C) 696mW
CERDIP (derate 10.00mW/°C above +70°C).....800mW
TSSOP (derate 6.7mW/°C above +70°C) 457mW
Operating Temperature Ranges
MAX30_C_ _ .....0°C to +70°C
MAX30_E_ _.....-40°C to +85°C
MAX30_MJE .....-55°C to +125°C
Storage Temperature Range .....-65°C to +150°C
Lead Temperature (soldering, 10sec) .....+300°C

Note 1: Signals on NO, COM, EN, A0, A1, or A2 exceeding V+ or V- are clamped by internal diodes. Limit forward current to maximum current ratings.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Dual Supplies

(V+ = +15V, V- = -15V, GND = 0V, V_{AH} = +2.4V, V_{AL} = +0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS				MIN	TYP (Note 2)	MAX	UNITS
SWITCH									
Analog Signal Range	V_{NO}, V_{COM}	(Note 3)				-15		15	V
On-Resistance	R_{ON}	I_{NO} = -1.0mA, V_{COM} = ±10V		T_A = +25°C			60	100	Ω
				T_A = T_{MIN} to T_{MAX}				125	
On-Resistance Matching Between Channels	ΔR_{ON}	I_{NO} = -1.0mA, V_{COM} = ±10V (Note 4)		T_A = +25°C			1.5	5	Ω
				T_A = T_{MIN} to T_{MAX}				8	
On-Resistance Flatness	R_{FLAT}	I_{NO} = -1.0mA, V_{COM} = ±5V or 0V		T_A = +25°C			1.8	7	Ω
				T_A = T_{MIN} to T_{MAX}				10	
NO-Off Leakage Current (Note 5)	$I_{NO(OFF)}$	V_{COM} = +10V, V_{NO} = ±10V, V_{EN} = 0V		T_A = +25°C		-0.5	0.01	0.5	nA
				T_A = T_{MIN} to T_{MAX}	C, E	-2.5		2.5	
					M	-5.0		5.0	
COM-Off Leakage Current (Note 5)	$I_{COM(OFF)}$	V_{NO} = ±10V, V_{COM} = +10V, V_{EN} = 0V	MAX308	T_A = +25°C		-0.75	0.02	0.75	nA
				T_A = T_{MIN} to T_{MAX}	C, E	-10		10	
					M	-20		20	
		V_{NO} = +10V, V_{COM} = ±10V, V_{EN} = 0V	MAX309	T_A = +25°C		-0.75	0.02	0.75	
				T_A = T_{MIN} to T_{MAX}	C, E	-5		5	
					M	-10		10	
COM-On Leakage Current (Note 5)	$I_{COM(ON)}$	V_{COM} = ±10V, V_{NO} = ±10V, sequence each switch on	MAX308	T_A = +25°C		-0.75	0.02	0.75	nA
				T_A = T_{MIN} to T_{MAX}	C, E	-10		10	
					M	-20		20	
			MAX309	T_A = +25°C		-0.75	0.02	0.75	
				T_A = T_{MIN} to T_{MAX}	C, E	-5		5	
					M	-10		10	

ELECTRICAL CHARACTERISTICS—Dual Supplies (continued)

(V+ = +15V, V- = -15V, GND = 0V, V_{AH} = +2.4V, V_{AL} = +0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS			MIN	TYP (Note 2)	MAX	UNITS
INPUT								
Input Current with Input Voltage High	I_{AH}	V_A = 2.4V or 15V			-1.0		1.0	µA
Input Current with Input Voltage Low	I_{AL}	V_{EN} = 0V or 2.4V, V_A = 0V			-1.0		1.0	µA
SUPPLY								
Power-Supply Range					±5		±20	V
Positive Supply Current	I+	V_{EN} = V_A = 0V or 4.5V		T_A = +25°C		16	30	µA
				T_A = T_{MIN} to T_{MAX}			75	
		V_{EN} = 2.4V, $V_{A(ALL)}$ = 0V or 2.4V		T_A = +25°C		0.075	0.5	mA
				T_A = T_{MIN} to T_{MAX}				
Negative Supply Current	I-	V_{EN} = 2.4V, $V_{A(ALL)}$ = 0V or 2.4V		T_A = +25°C	-1		1	µA
				T_A = T_{MIN} to T_{MAX}	-10		10	
DYNAMIC								
Transistion Time	t_{TRANS}	Figure 2		T_A = +25°C		85	175	ns
				T_A = T_{MIN} to T_{MAX}			250	
Break-Before-Make Interval	t_{OPEN}	Figure 4		T_A = +25°C	10	40		ns
Enable Turn-On Time	$t_{ON(EN)}$	Figure 3		T_A = +25°C		85	150	ns
				T_A = T_{MIN} to T_{MAX}			225	
Enable Turn-Off Time	$t_{OFF(EN)}$	Figure 3		T_A = +25°C			150	ns
Charge Injection (Note 3)	Q	C_L = 1.0nF, V_{NO} = 0V, R_S = 0Ω, Figure 5		T_A = T_{MIN} to T_{MAX}			300	
				T_A = +25°C		2	10	pC
Off Isolation (Note 6)	V_{ISO}	V_{EN} = 0V, R_L = 1kΩ, f = 100kHz, Figure 6		T_A = +25°C		-75		dB
Crosstalk Between Channels	V_{CT}	V_{EN} = 2.4V, f = 100kHz, V_{GEN} = $1V_{P-P}$, R_L = 1kΩ, Figure 7		T_A = +25°C		-92		dB
Logic Input Capacitance	C_{IN}	f = 1MHz		T_A = +25°C		8		pF
NO-Off Capacitance	$C_{NO(OFF)}$	f = 1MHz, V_{EN} = V_{NO} = 0V, Figure 8		T_A = +25°C		3		pF
COM-Off Capacitance	$C_{COM(OFF)}$	f = 1MHz, V_{EN} = 0.8V V_{COM} = 0V, Figure 8	MAX308	T_A = +25°C		26		pF
			MAX309			14		
COM-On Capacitance	$C_{COM(ON)}$	f = 1MHz, V_{EN} = 2.4V V_{COM} = 0V, Figure 8	MAX308	T_A = +25°C		37		pF
			MAX309			25		

Precision, 8-Channel/Dual 4-Channel, High-Performance, CMOS Analog Multiplexers

ELECTRICAL CHARACTERISTICS—Single Supply

(V+ = +12V, V- = 0V, GND = 0V, V_{AH} = +2.4V, V_{AL} = +0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP (Note 2)	MAX	UNITS
SWITCH							
Analog Signal Range	V_{NO}, V_{COM}	(Note 3)		0		12	V
On-Resistance	R_{ON}	I_{NO} = -1.0mA V_{COM} = 3V or 10V	T_A = +25°C		120	175	Ω
DYNAMIC							
Transition Time (Note 3)	t_{TRANS}	V_{NO1} = 8V, V_{NO8} = 0V, V_{IN} = 2.4V, Figure 2	T_A = +25°C		115	450	ns
Enable Turn-On Time (Note 3)	$t_{ON(EN)}$	V_{INH} = 2.4V, V_{INL} = 0V, V_{NO1} = 5V, Figure 3	T_A = +25°C		100	600	ns
Enable Turn-Off Time (Note 3)	$t_{OFF(EN)}$	V_{INH} = 2.4V, V_{INL} = 0V, V_{NO1} = 5V, Figure 3	T_A = +25°C		75	300	ns
Charge Injection (Note 3)	Q	C_L = 1.0nF, V_{NO} = 0V, R_S = 0Ω	T_A = +25°C		2	10	pC

Note 2: The algebraic convention where the most negative value is a minimum and the most positive value a maximum is used in this data sheet.

Note 3: Guaranteed by design.

Note 4: $\Delta R_{ON} = R_{ON(MAX)} - R_{ON(MIN)}$. On-resistance match between channels and flatness are guaranteed only with specified voltages. Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured at the extremes of the specified analog signal range.

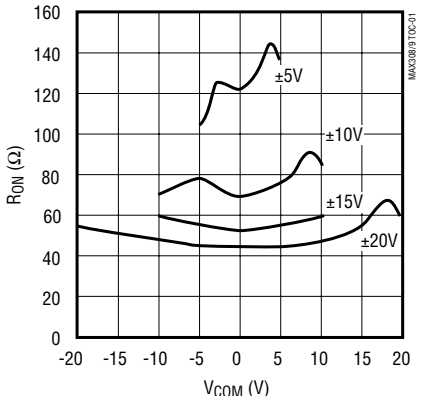
Note 5: Leakage parameters are 100% tested at the maximum rated hot temperature and guaranteed by correlation at +25°C.

Note 6: Off isolation = 20log V_{COM}/V_{NO}, where V_{COM} = output and V_{NO} = input to off switch.

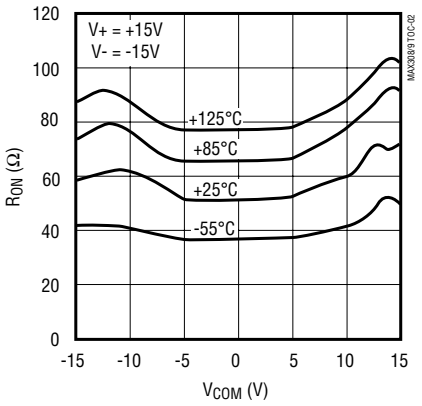
Typical Operating Characteristics

(T_A = +25°C, unless otherwise noted.)

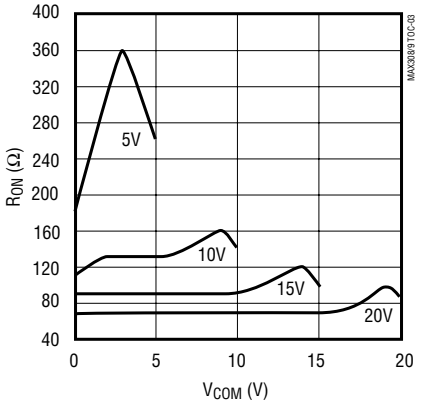
ON-RESISTANCE vs. V_{COM} (DUAL SUPPLIES)



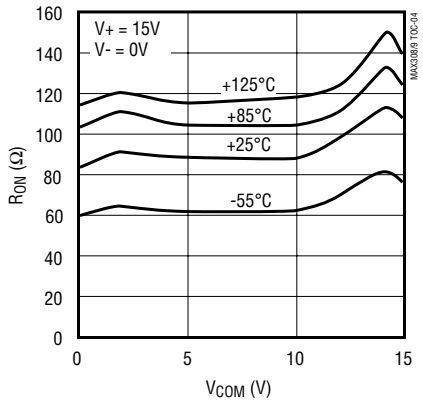
ON-RESISTANCE vs. V_{COM} AND TEMPERATURE (DUAL SUPPLIES)



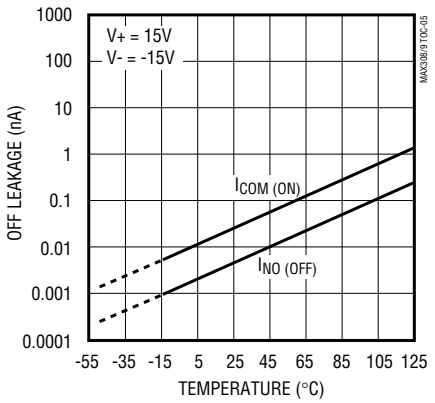
ON-RESISTANCE vs. V_{COM} (SINGLE SUPPLY)



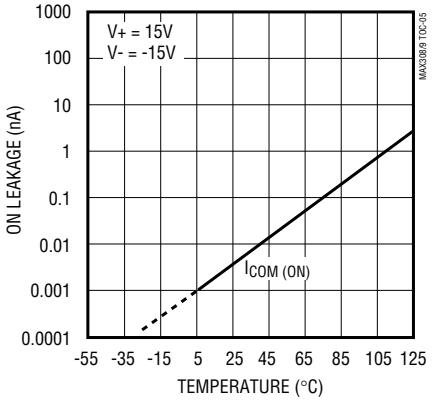
ON-RESISTANCE vs. V_{COM} AND TEMPERATURE (SINGLE SUPPLY)



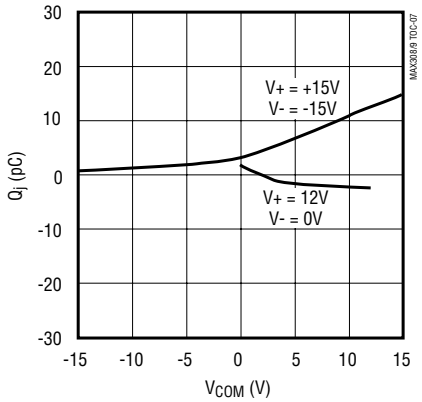
OFF LEAKAGE vs. TEMPERATURE



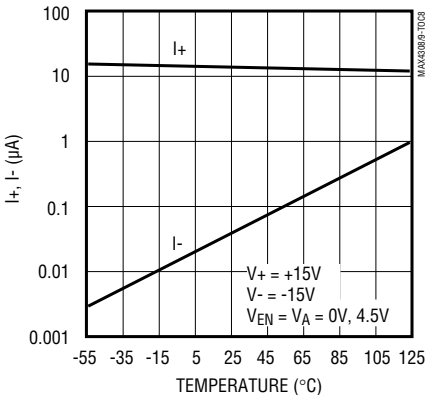
ON LEAKAGE vs. TEMPERATURE



CHARGE INJECTION vs. V_{COM}



SUPPLY CURRENT vs. TEMPERATURE



Precision, 8-Channel/Dual 4-Channel, High-Performance, CMOS Analog Multiplexers

Pin Description

PIN		NAME	FUNCTION
MAX308	MAX309		
1, 15, 16	—	A0, A2, A1	Address Inputs
—	1, 16	A0, A1	Address Inputs
2	2	EN	Enable Input
3	3	V-	Negative Supply Voltage Input
4–7	—	NO1–NO4	Analog Inputs—Bidirectional
—	4–7	NO1A–NO4A	Analog Inputs—Bidirectional
8	—	COM	Analog Output—Bidirectional
—	8, 9	COMA, COMB	Analog Outputs—Bidirectional
9–12	—	NO8–NO5	Analog Inputs—Bidirectional
—	10–13	NO4B–NO1B	Analog Inputs—Bidirectional
13	14	V+	Positive Supply Voltage Input
14	15	GND	Ground

Applications Information

Operation with Supply Voltages Other than 15V

Using supply voltages less than ±15V will reduce the analog signal range. The MAX308/MAX309 switches operate with ±5V to ±20V bipolar supplies or with a +5V to +30V single supply. Connect V- to GND when operating with a single supply. Both device types can also operate with unbalanced supplies, such as +24V and -5V. The *Typical Operating Characteristics* graphs show typical on-resistance with 20V, 15V, 10V, and 5V supplies. (Switching times increase by a factor of two or more for operation at 5V.)

Overvoltage Protection

Proper power-supply sequencing is recommended for all CMOS devices. Do not exceed the absolute maximum ratings, because stresses beyond the listed ratings may cause permanent damage to the devices. Always sequence V+ on first, then V-, followed by the logic inputs, NO, or COM. If power-supply sequencing is not possible, add two small signal diodes in series with supply pins for overvoltage protection (Figure 1). Adding diodes reduces the analog signal range to 1V below V+ and 1V above V-, but does not affect the devices' low switch resistance and low leakage characteristics. Device operation is unchanged, and the difference between V+ and V- should not exceed +44V.

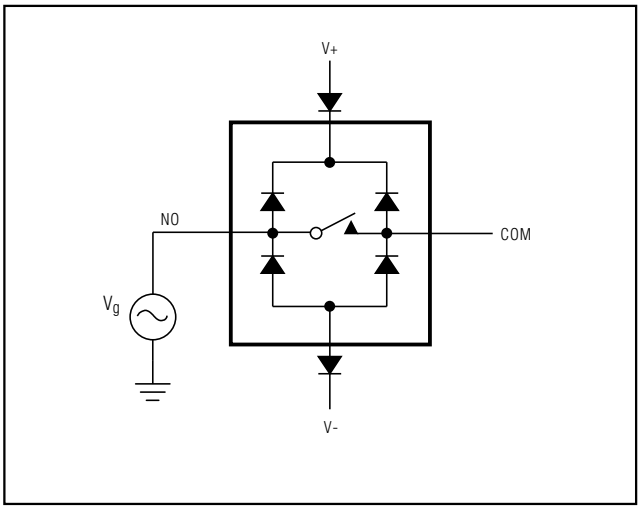


Figure 1. Overvoltage Protection Using External Blocking Diodes

Test Circuits/Timing Diagrams

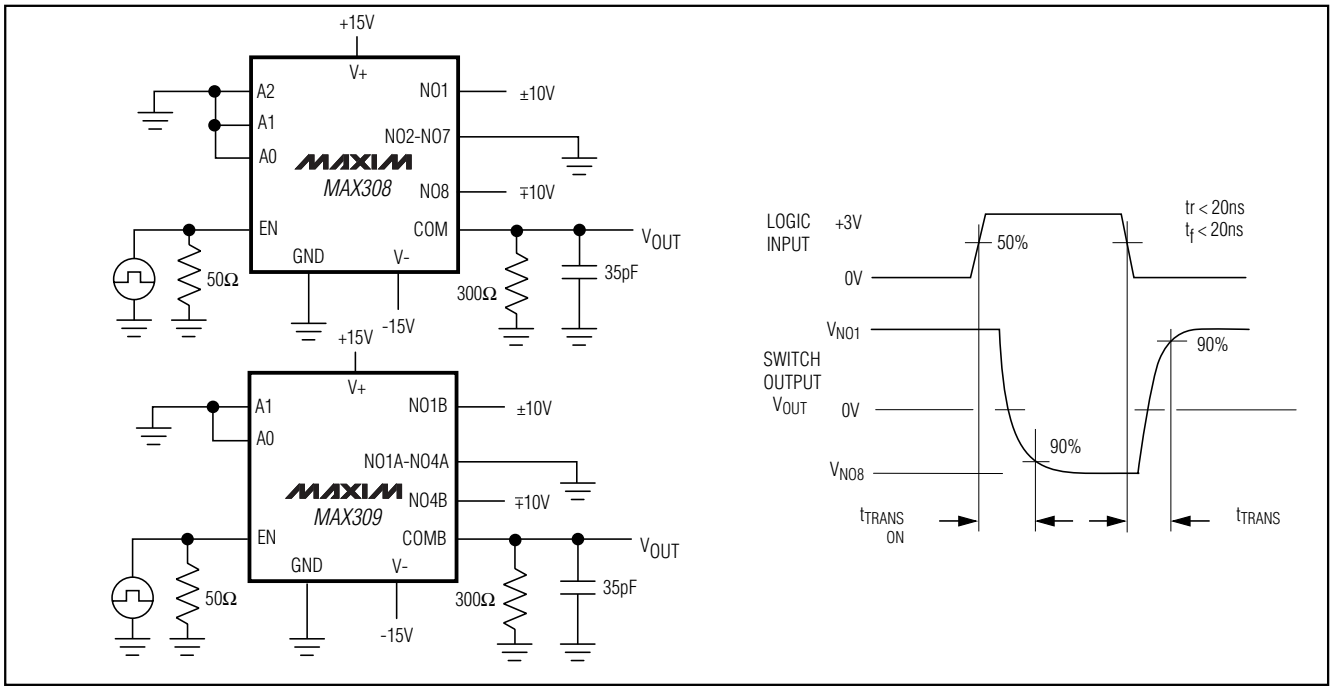


Figure 2. Transition Time

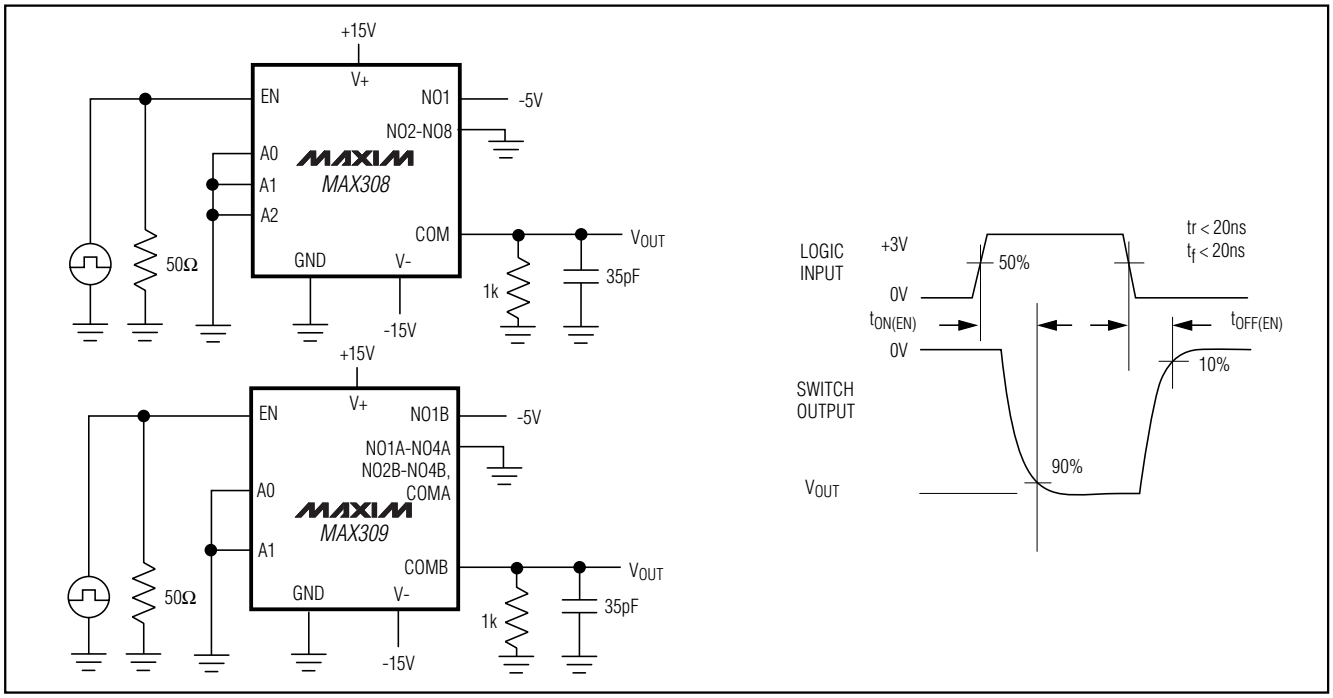


Figure 3. Enable Switching Time

Precision, 8-Channel/Dual 4-Channel, High-Performance, CMOS Analog Multiplexers

Test Circuits/Timing Diagrams (continued)

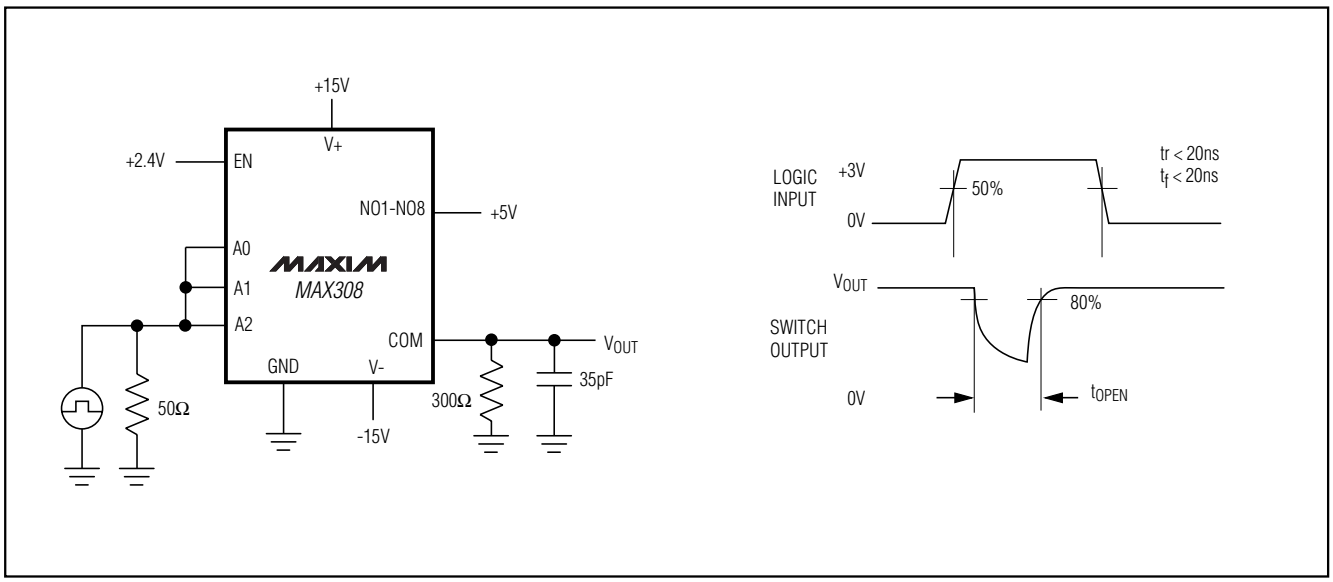


Figure 4. Break-Before-Make Interval

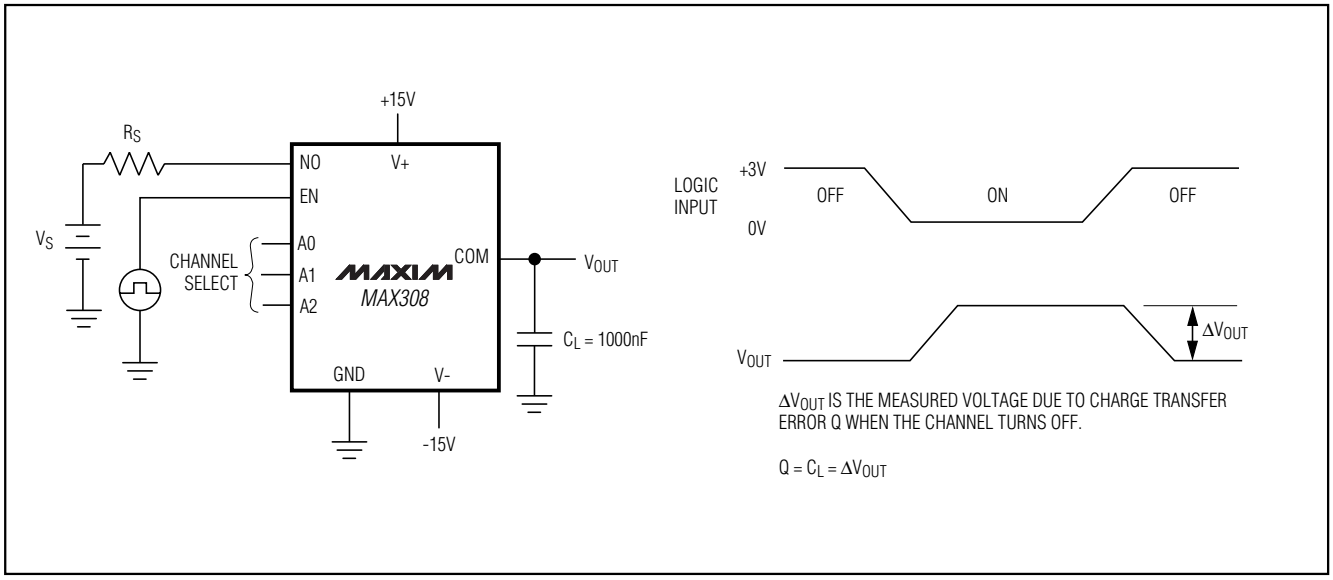


Figure 5. Charge Injection

Test Circuits/Timing Diagrams (continued)

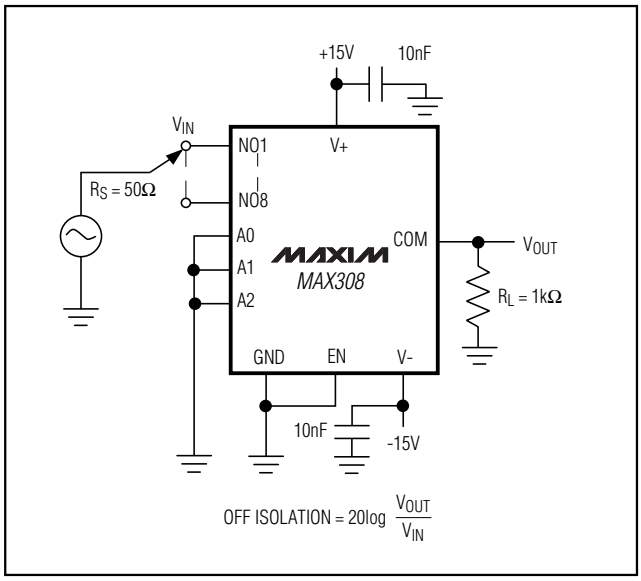


Figure 6. Off Isolation

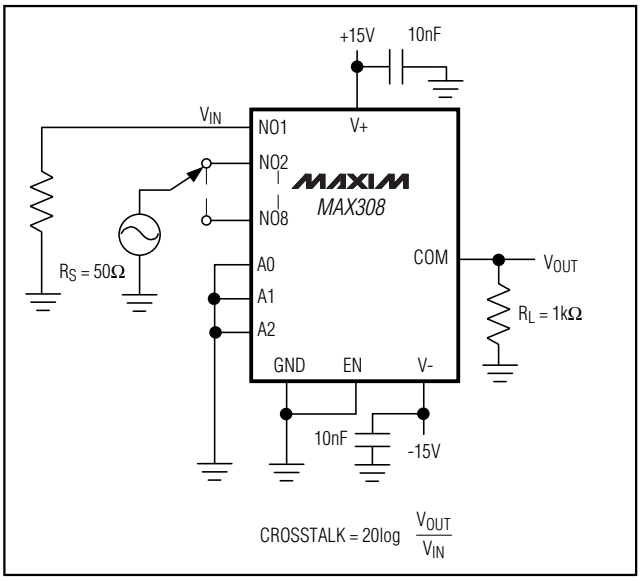


Figure 7. Crosstalk

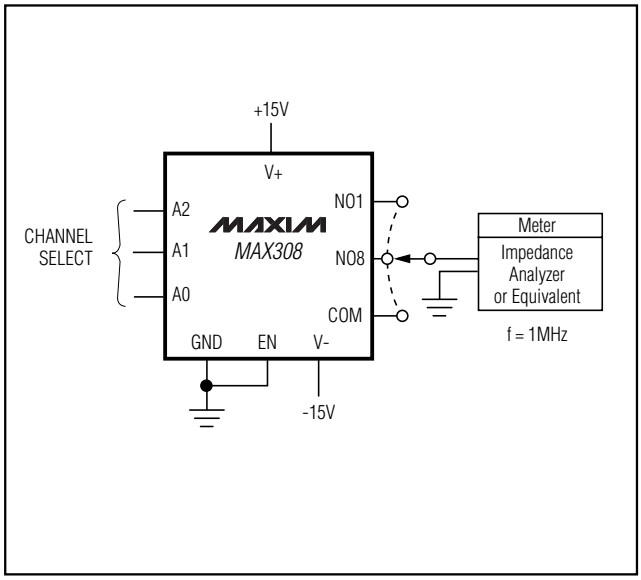
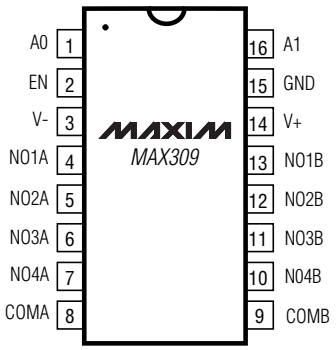


Figure 8. NO/COM Capacitance

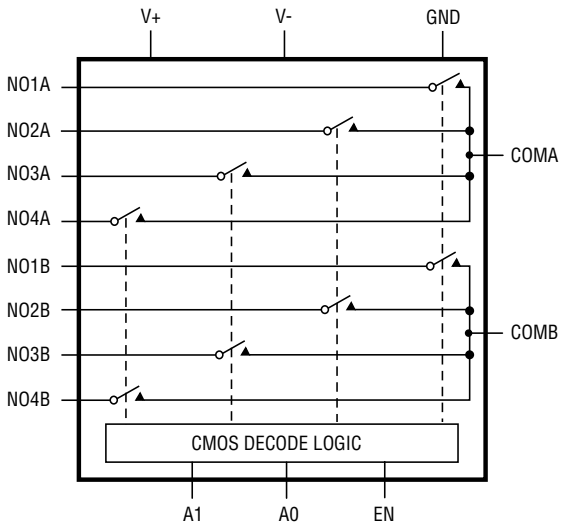
Precision, 8-Channel/Dual 4-Channel, High-Performance, CMOS Analog Multiplexers

Pin Configurations/Functional Diagrams/Truth Tables (continued)

TOP VIEW



DIP/SO/TSSOP



MAX309 4-CHANNEL DIFFERENTIAL MULTIPLEXER

A2	A1	A0	EN	ON SWITCH
X	X	X	0	None
0	0	0	1	1
0	0	1	1	2
0	1	0	1	3
0	1	1	1	4
1	0	0	1	5
1	0	1	1	6
1	1	0	1	7
1	1	1	1	8

MAX308

LOGIC "0" $V_{AL} \leq 0.8V$, LOGIC "1" $V_{AH} \geq 2.4V$

A1	A0	EN	ON SWITCH
X	X	0	None
0	0	1	1
0	1	1	2
1	0	1	3
1	1	1	4

MAX309

LOGIC "0" $V_{AL} \leq 0.8V$, LOGIC "1" $V_{AH} \geq 2.4V$

_Ordering Information (continued)

PART	TEMP RANGE	PIN-PACKAGE
MAX309CPE	0°C to +70°C	16 Plastic DIP
MAX309CSE	0°C to +70°C	16 Narrow SO
MAX309CUE	0°C to +70°C	16 TSSOP
MAX309C/D	0°C to +70°C	Dice*
MAX309EPE	-40°C to +85°C	16 Plastic DIP
MAX309ESE	-40°C to +85°C	16 Narrow SO
MAX309EJE	-40°C to +85°C	16 CERDIP
MAX309EUE	-40°C to +85°C	16 TSSOP
MAX309MJE	-55°C to +125°C	16 CERDIP**

*Contact factory for dice specifications.
**Contact factory for availability.

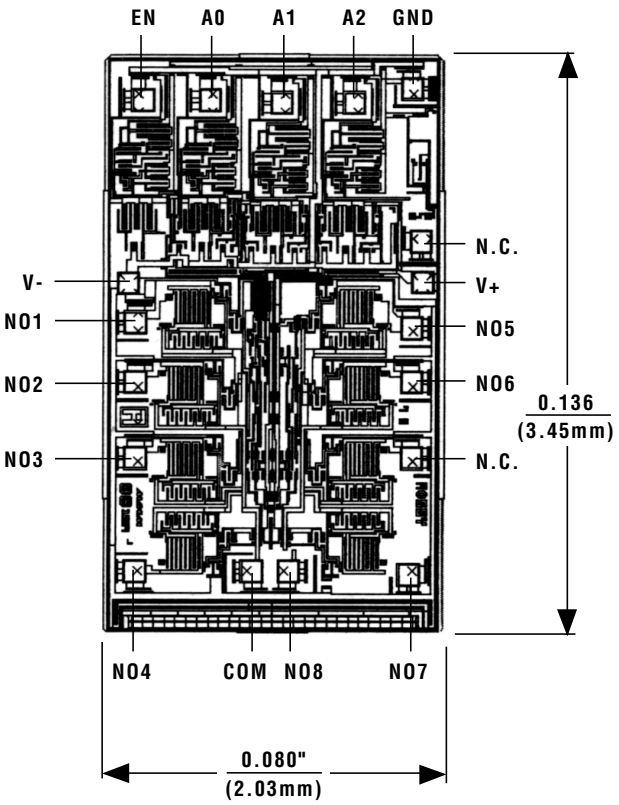
_____Package Information

For the latest package outline information, go to **www.maxim-ic.com/packages**.

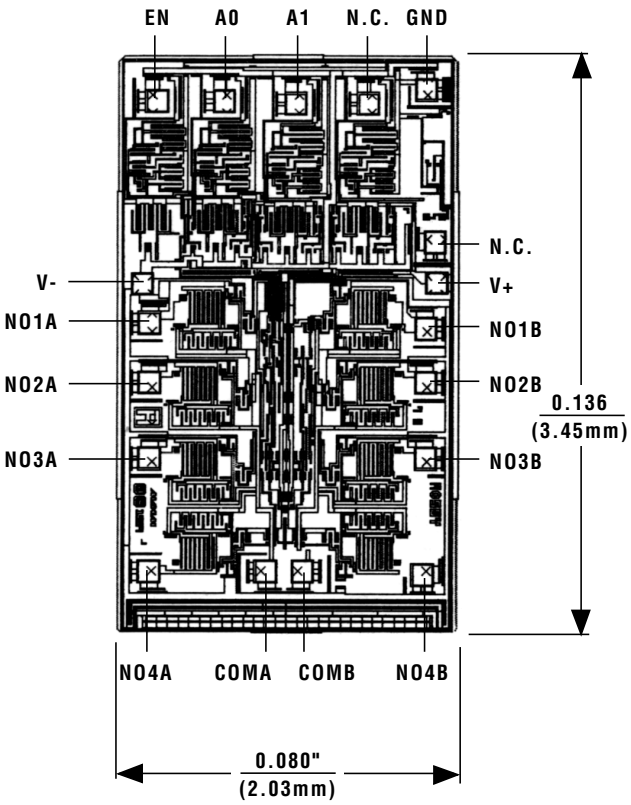
Precision, 8-Channel/Dual 4-Channel, High-Performance, CMOS Analog Multiplexers

Chip Topographies

MAX308



MAX309



N.C. = NO INTERNAL CONNECTION

TRANSISTOR COUNT: 122
SUBSTRATE CONNECTED TO V+

TRANSISTOR COUNT: 122
SUBSTRATE CONNECTED TO V+