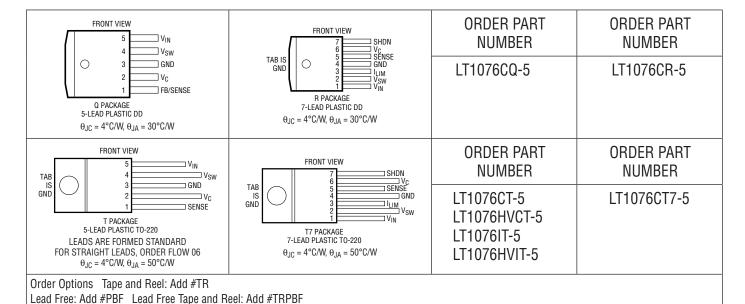
ABSOLUTE MAXIMUM RATINGS (Note 1)

Input Voltage	
LT1076-5	45V
LT1076HV-5	64V
Switch Voltage with Respect to Input Voltage	
LT1076-5	64V
LT1076HV-5	75V
Switch Voltage with Respect to Ground Pin	
(V _{SW} Negative)	
LT1076-5 (Note 6)	35V
LT1076HV-5 (Note 6)	
, ,	

Sense Pin Voltage2V	/, 10V		
Maximum Operating Ambient Temperature Range			
LT1076C-5, LT1076HVC-50°C to	70°C		
LT1076I-5, LT1076HVI-540°C to	85°C		
Maximum Operating Junction Temperature Range			
LT1076C-5, LT1076HVC-50°C to 1	25°C		
LT1076I-5, LT1076HVI-540°C to 1	25°C		
Maximum Storage Temperature65°C to 1			
Lead Temperature (Soldering, 10 sec)3	00°C		

PACKAGE/ORDER INFORMATION

Lead Free Part Marking: http://www.linear.com/leadfree/



Consult LTC Marketing for parts specified with wider operating temperature ranges. *The temperature grade is identified by a label on the shipping container.

ELECTRICAL CHARACTERISTICS The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_J = 25^{\circ}C$. $V_{IN} = 25V$, unless otherwise noted.

SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Switch "On" Voltage (Note 2)	I _{SW} = 0.5A I _{SW} = 2A	•			1.2 1.7	V
Switch "Off" Leakage	$V_{IN} = 25V, VSW = 0$ $V_{IN} = 25V, VSW = 0$				150 250	μΑ μΑ

LINEAR TECHNOLOGY

ELECTRICAL CHARACTERISTICS The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_J = 25^{\circ}$ C. $V_{IN} = 25$ V, unless otherwise noted.

SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Current (Note 3)	V_{OUT} = 5.5V, V_{IN} \leq 40V $40V < V_{IN} < 60V$ V_{SHDN} = 0.1V (Device Shutdown) (Note 9)	•		8.5 9.0 140	11 12 300	mA mA μA
Minimum Supply Voltage	Normal Mode Start-Up Mode (Note 4)	•		7.3 3.5	8.0 4.8	V
Switch Current Limit (Note 5)	Limit (Note 5) $I_{LIM} = Open$ $R_{LIM} = 10k \text{ (Note 10)}$ $R_{LIM} = 7k \text{ (Note 10)}$		2	2.6 1.8 1.2	3.2	A A A
Maximum Duty Cycle		•	85	90		%
Switching Frequency	$T_J \le 125$ °C $V_{OUT} = V_{SENSE} = 0V$ (Note 5)	•	90 85	100 20	110 120	kHz kHz kHz
Switching Frequency Line Regulation	$8V \le V_{IN} \le V_{MAX}$ (Note 8)	•		0.03	0.1	%/V
Error Amplifier Voltage Gain (Note 8)	$1V \le V_C \le 4V$			2000		V/V
Error Amplifier Transconductance (Note 8)			3700	5000	8000	μmho
Error Amplifier Source and Sink Current	Source (V _{SENSE} = 4.5V) Sink (V _{SENSE} = 5.5V)		100 0.7	140 1.0	225 1.6	μA mA
Sense Pin Divider Resistance			3	5	8	kΩ
Sense Voltage	V _C = 2V	•	4.85	5	5.15	V
Output Voltage Tolerance	V _{OUT} (Nominal) = 5V All Conditions of Input Voltage, Output Voltage, Temperature and Load Current	•		±0.5 ±1.0	±2 ±3	% %
Output Voltage Line Regulation	$8V \le V_{IN} \le V_{MAX}$ (Note 7)	•		0.005	0.02	%/V
VC Voltage at 0% Duty Cycle	Over Temperature	•		1.5 -4.0		V mV/°C
Multiplier Reference Voltage				24		V
Shutdown Pin Current	$V_{SHDN} = 5V$ $V_{SHDN} \le V_{THRESHOLD} (\cong 2.5V)$		5	10	20 50	μΑ μΑ
Shutdown Thresholds	Switch Duty Cycle = 0 Fully Shut Down		2.2 0.1	2.45 0.30	2.7 0.5	V
Thermal Resistance Junction to Case					4	°C/W

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: To calculate maximum switch "on" voltage at currents between low and high conditions, a linear interpolation may be used.

Note 3: A sense pin voltage (V_{SENSE}) of 5.5V forces the VC pin to its low clamp level and the switch duty cycle to zero. This approximates the zero load condition where duty cycle approaches zero.

Note 4: Total voltage from V_{IN} pin to ground pin must be \geq 8V after start-up for proper regulation. For $T_A < 25^{\circ}C$, limit = 5V.

Note 5: Switch frequency is internally scaled down when the sense pin voltage is less than 2.6V to avoid extremely short switch on times. During

current limit testing, V_{SENSE} is adjusted to give a minimum switch on time of 1ms.

Note 6: Switch to input voltage limitation must also be observed.

Note 7: $V_{MAX} = 40V$ for the LT1076-5 and 60V for the LT1076HV-5.

Note 8: Error amplifier voltage gain and transconductance are specified relative to the internal feedback node. To calculate gain and transconductance from the Sense pin (Output) to the $V_{\mathbb{C}}$ pin, multiply by 0.44.

Note 9: Does not include switch leakage.

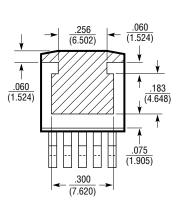
Note 10:
$$I_{LIM} \approx \frac{R_{LIM} - 1k}{5k}$$



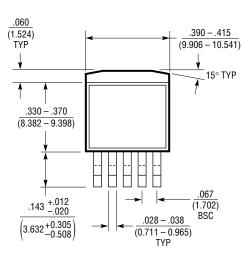
PACKAGE DESCRIPTION

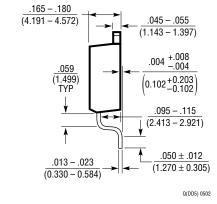
Q Package 5-Lead Plastic DD Pak

(Reference LTC DWG # 05-08-1461)

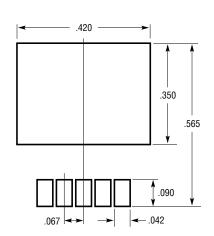


BOTTOM VIEW OF DD PAK HATCHED AREA IS SOLDER PLATED COPPER HEAT SINK







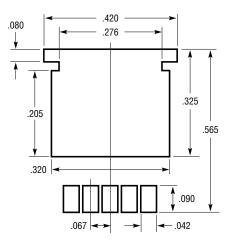


RECOMMENDED SOLDER PAD LAYOUT

NOTE:

1. DIMENSIONS IN INCH/(MILLIMETER)

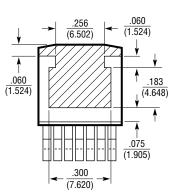
2. DRAWING NOT TO SCALE



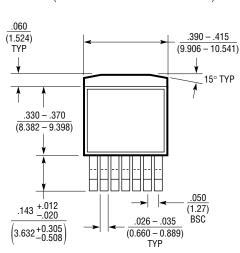
RECOMMENDED SOLDER PAD LAYOUT FOR THICKER SOLDER PASTE APPLICATIONS

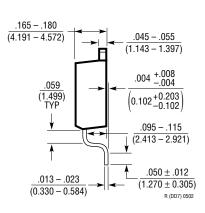
R Package 7-Lead Plastic DD Pak

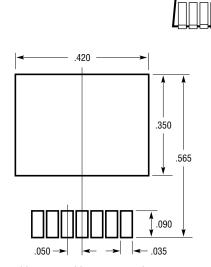
(Reference LTC DWG # 05-08-1462)



BOTTOM VIEW OF DD PAK HATCHED AREA IS SOLDER PLATED COPPER HEAT SINK

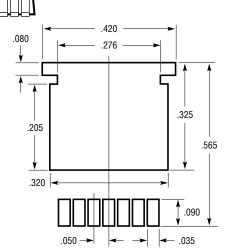






RECOMMENDED SOLDER PAD LAYOUT

- NOTE: 1. DIMENSIONS IN INCH/(MILLIMETER) 2. DRAWING NOT TO SCALE

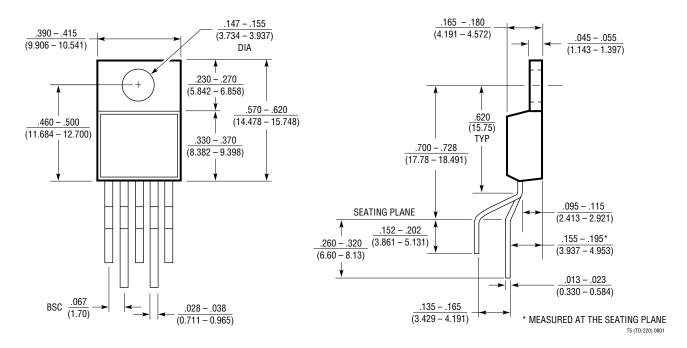


RECOMMENDED SOLDER PAD LAYOUT FOR THICKER SOLDER PASTE APPLICATIONS

PACKAGE DESCRIPTION

T Package 5-Lead Plastic TO-220 (Standard)

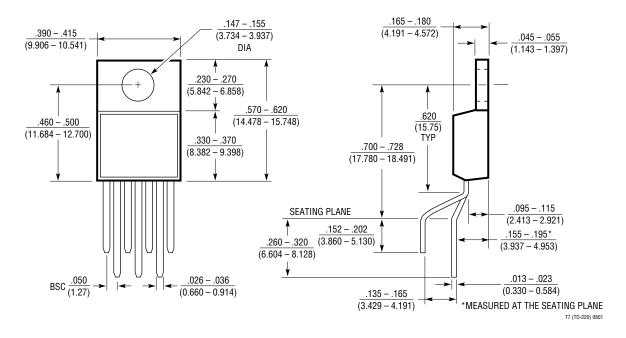
(Reference LTC DWG # 05-08-1421)



PACKAGE DESCRIPTION

T7 Package 7-Lead Plastic T0-220 (Standard)

(Reference LTC DWG # 05-08-1422)



LT1076-5

RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1074/HV	4.4A (I _{OUT}), 100kHz High Efficiency Step-Down DC/DC Converter	V _{IN} : 7.3V to 45V/64V, V _{OUT(MIN)} : 2.21V, I _Q : 8.5mA, I _{SHDN} : 10μA, DD5/7, TO-2205/7
LT3430	60V, 2.75A (I _{OUT}), 200kHz High Efficiency Step-Down DC/DC Converter	V _{IN} : 5.5V to 60V, V _{OUT(MIN)} : 1.20V, I _Q : 2.5mA, I _{SHDN} : 25μA, TSSOP16E
LT1956	60V, 1.2A (I _{OUT}), 500kHz High Efficiency Step-Down DC/DC Converter	V _{IN} : 5.5V to 60V, V _{OUT(MIN)} : 1.20V, I _Q : 2.5mA, I _{SHDN} : 25μA, TSSOP16E