

# LA6585T

Continued from preceding page.

Parameter	Symbol	Conditions	Ratings	Unit
Allowable power dissipation	$P_d \text{ max}$	When mounted on a circuit board *1	400	mW
Operating temperature	$T_{opr}$		-30 to +90	°C
Storage temperature	$T_{stg}$		-55 to +150	°C

\*1 Specified circuit board :  $114.3 \times 76.1 \times 1.6\text{mm}^3$ , glass epoxy.

## Recommended Operating Conditions at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	$V_{CC}$		2.2 to 14.0	V
Common-phase input voltage range of hall input	$V_{ICM}$		0 to $V_{CC} - 1.5$	V

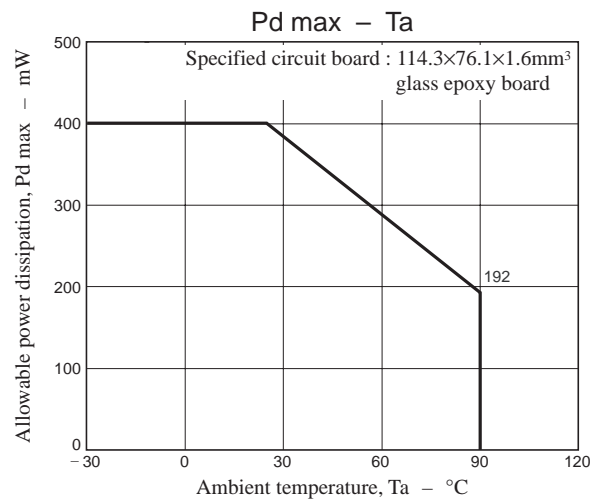
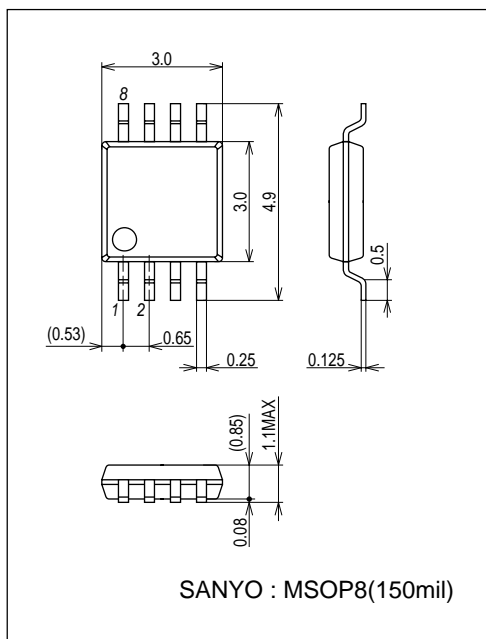
## Electrical Characteristics at $T_a = 25^\circ\text{C}$ , $V_{CC} = 12\text{V}$ , Unless otherwise specified.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Circuit current	$I_{CC1}$	Drive mode (CT = low)	3	6	9	mA
	$I_{CC2}$	Lock protection mode (CT = high)	2.5	5	7.5	mA
Lock detection capacitor charge current	$I_{CT1}$		0.9	1.2	1.5	$\mu\text{A}$
Capacitor discharge current	$I_{CT2}$		0.10	0.18	0.25	$\mu\text{A}$
Capacitor charge/discharge current ratio	$R_{CT}$	$R_{CD} = I_{CT1}/I_{CT2}$	5	6.5	8	
CT charge voltage	$V_{CT1}$		1.3	1.5	1.7	V
CT discharge voltage	$V_{CT2}$		0.3	0.5	0.7	V
OUT output low saturation voltage	$V_{OL}$	$I_O = 200\text{mA}$		0.25	0.45	V
OUT output high saturation voltage	$V_{OH}$	$I_O = 200\text{mA}$		0.95	1.2	V
Hall input sensitivity	$V_{HN}$	Zero peak value (including offset and hysteresis)		7	15	mV
FG output pin low-level voltage	$V_{FG}$	$I_{FG} = 5\text{mA}$		0.15	0.3	V
FG output pin leakage current	$I_{FGL}$	$V_{FG} = 15\text{V}$		1	30	$\mu\text{A}$

## Package Dimensions

unit : mm (typ)

3245B

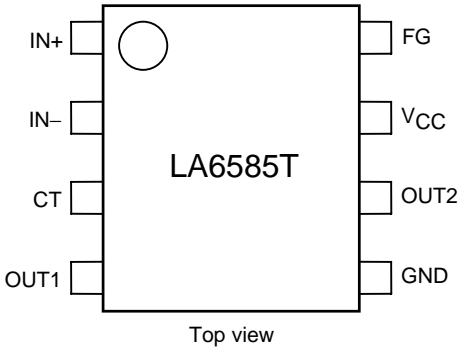


# LA6585T

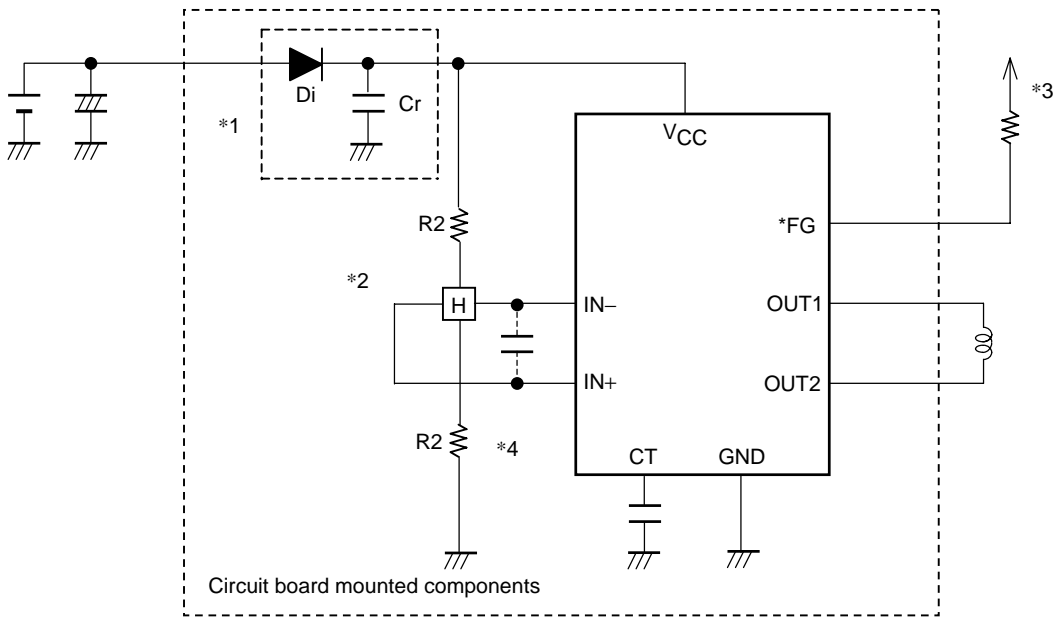
## Truth Table

IN-	IN+	CT	OUT1	OUT2	FG	Mode
High	Low	Low	High	Low	Low	During rotation
Low	High		Low	High	High	
–	–	High	Off	Off	–	Lock protection

## Pin Assignment

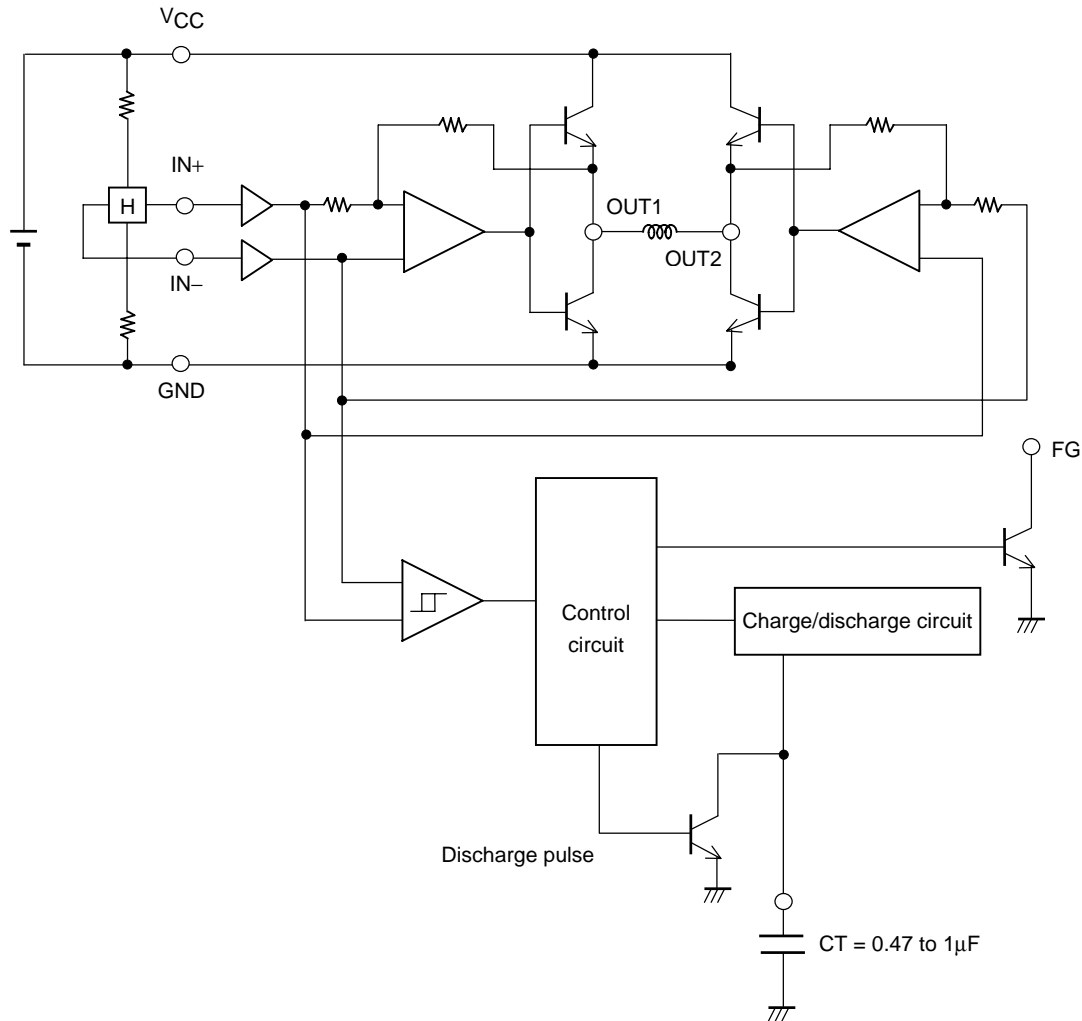


## Application Circuit Example



- \*1. If the diode Di (which protects the IC destruction by reverse connection) is used, it is necessary to insert the capacitor Cr and provide a regenerative current route. Similarly, if there is no nearby capacitor on the fan power supply line, Cr will also be necessary to improve reliability.
- \*2. If the Hall sensor bias is taken from  $V_{CC}$ , a  $1/2 V_{CC}$  bias, as shown in the figure, must be used. Linear drive is implemented by amplifying the Hall sensor output and applying voltage control to the coil. If the Hall effect sensor provides a strong output, the startup characteristics and efficiency will be good, then even quieter operation will be achieved by adjusting the Hall effect sensor.
- \*3. This pin must be left open if unused.
- \*4. If the line from the Hall sensor output to the Hall sensor input of IC are long, noise may enter the system from that line. If that becomes a problem, insert a capacitor as shown in the figure.

## Internal Equivalent Circuits



- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of May, 2007. Specifications and information herein are subject to change without notice.