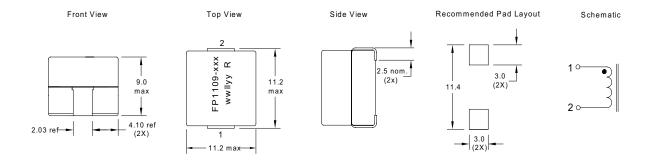
	Product Specifications						
Part Number <sup>7</sup>	OCL1 ± 20% (nH)	FLL <sup>2</sup> Min. (nH)	I <sub>rms</sub> ³ (Amps)	I <sub>sat</sub> 1 <sup>4</sup> @ 25°C (Amps)	I <sub>sat</sub> 2 <sup>5</sup> @ 125°C (Amps)	DCR (mΩ) @ 20°C	K-factor6
FP1109-R20-R	205	122		69	52		233
FP1109-R23-R	247	147		55	41		233
FP1109-R27-R	270	160	1	51	38		233
FP1109-R33-R	311	185	35	44	33	0.42 ±10%	233
FP1109-R47-R	463	275		27	20		233
FP1109-R58-R	548	325	]	22.5	17		233
FP1109-1R0-R	950	565	]	11.5	8.5		233

- 1 Open Circuit Inductance (OCL) Test Parameters: 100kHz,  $0.10V_{\mbox{rms}}$ ,  $0.0\mbox{Adc}$
- $^{\circ}$  Full Load Inductance (FLL) Test Parameters: 100kHz, 0.1V  $_{\rm rms},$   $\rm I_{\rm sat}^{-1}$
- 3 I<sub>rms</sub>: DC current for an approximate temperature rise of 40°C without core loss. Derating is necessary for AC currents. PCB pad layout, trace thickness and width, air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 125°C under worst case operating conditions verified in the end application.
- 4  $I_{\text{Sat}}$ 1: Peak current for approximately 30% rolloff at +25°C.
- 5 I<sub>sat</sub>2: Peak current for approximately 30% rolloff at +125°C.
- 6 K-factor: Used to determine B<sub>p-p</sub> for core loss (see graph). B<sub>p-p</sub> = K \* L \* △I \* 10<sup>-3</sup>, B<sub>p-p</sub> : (Gauss), K: (K-factor from table), L: (inductance in nH), △I (peak-to-peak ripple current in amps).
- 7 Part Number Definition: FP1109-xxx-R
  - FP1109 = Product code and size
  - xxx= Inductance value in µH, R = decimal point. If no "R" is present, then third character = # of zeros
  - "-R" suffix = RoHS compliant

### **Dimensions- mm**



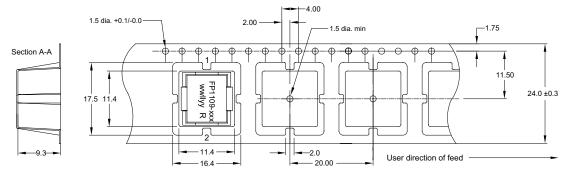
Part Marking: FP1109

 $xxx = \text{Inductance value in } \mu \text{H. (R = Decimal point). If no "R" is present, then last character is \# of zeros } \\$ 

wwllyy = Date code

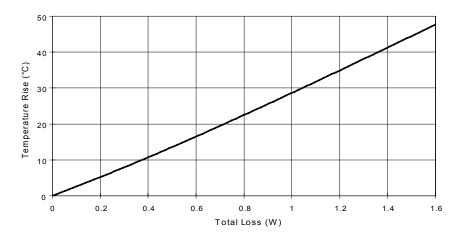
R = Revision level

### Packaging information - mm

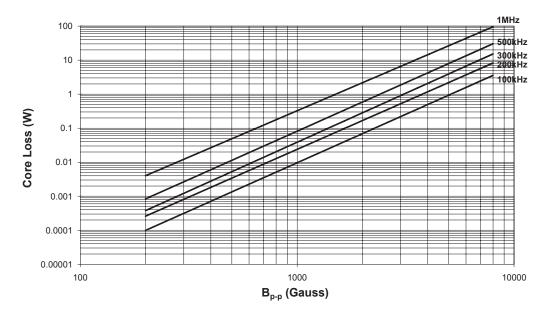


Supplied in tape-and-reel packaging, 350 parts per reel, 13" diameter reel.

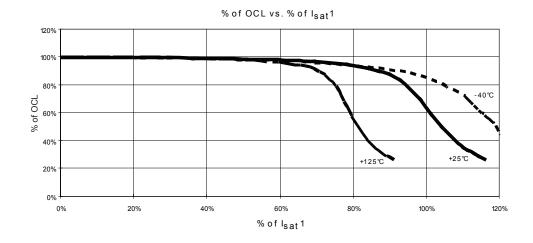
# Temperature rise vs total loss



## Core loss vs Bp-p



## **Inductance characteristics**



### **Solder Reflow Profile**

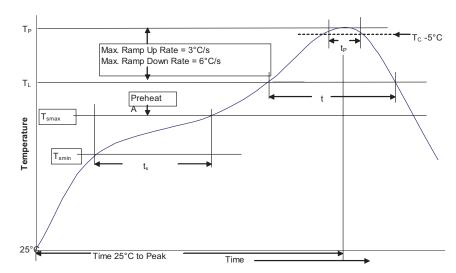


Table 1 - Standard SnPb Solder (T<sub>c</sub>)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5mm	235°C	220°C
≥2.5mm	220°C	220°C

Table 2 - Lead (Pb) Free Solder (Tc)

	Volume	Volume	Volume
Package	mm³	mm³	mm <sup>3</sup>
Thickness	<350	350 - 2000	>2000
<1.6mm	260°C	260°C	260°C
1.6 - 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

#### Reference JDEC J-STD-020

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder	
Preheat and Soak	• Temperature min. (T <sub>smin</sub> )	100°C	150°C	
	Temperature max. (T <sub>smax</sub> )	150°C	200°C	
	• Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60-120 Seconds	60-120 Seconds	
Average ramp up rat	te T <sub>smax</sub> to T <sub>p</sub>	3°C/ Second Max.	3°C/ Second Max.	
Liquidous temperature (TL)		183°C	217°C	
Time at liquidous (t <sub>L</sub> )		60-150 Seconds	60-150 Seconds	
Peak package body	temperature (T <sub>P</sub> )*	Table 1	Table 2	
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$		20 Seconds**	30 Seconds**	
Average ramp-down rate (T <sub>p</sub> to T <sub>smax</sub> )		6°C/ Second Max.	6°C/ Second Max.	
Time 25°C to Peak Temperature		6 Minutes Max.	8 Minutes Max.	

 $<sup>^{\</sup>star}$  Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

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<sup>\*\*</sup> Tolerance for time at peak profile temperature  $(t_p)$  is defined as a supplier minimum and a user maximum.