## **Electrical Characteristics** ( $T_A = 25$ °C Unless otherwise specified.)

### **Individual Component Characteristics**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit	
INPUT			!				
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 20mA	_	1.2	1.4	V	
C <sub>t</sub>	Terminal Capacitance	V = 0, f = 1kHz	_	50	250	pF	
OUTPUT	ОИТРИТ						
I <sub>CEO</sub>	Collector Dark Current	V <sub>CE</sub> = 10V, I <sub>F</sub> = 0	_	_	1	μA	
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 0.1 \text{mA}, I_F = 0$	35	_	_	V	
BV <sub>ECO</sub>	Emitter-Collector Breakdown Voltage	$I_E = 10\mu A, I_F = 0$	6	_	_	V	

## Transfer Characteristics ( $T_A = 25$ °C Unless otherwise specified.)

Symbol	DC Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
I <sub>C</sub>	Collector Current	$I_F = 1 \text{mA}, V_{CE} = 2V$	6	_	75	mA
CTR	Current Transfer Ratio <sup>(1)</sup>		600	_	7,500	%
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_F = 20$ mA, $I_C = 5$ mA	-	0.8	1	V
f <sub>C</sub>	Cut-Off Frequency	$V_{CE} = 5V$ , $I_{C} = 2mA$ , $R_{L} = 100\Omega$ , -3dB	1	6	_	KHz
t <sub>r</sub>	Response Time (Rise)	$V_{CE} = 2 \text{ V}, I_{C} = 10 \text{mA}, R_{L} = 100 \Omega$	_	60	300	μs
t <sub>f</sub>	Response Time (Fall)		_	53	250	μs

#### **Isolation Characteristics**

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Units
V <sub>ISO</sub>	Input-Output Isolation Voltage	$f = 60Hz$ , $t = 1$ min, $I_{I-O} \le 2\mu A$	5000	_	_	Vac(rms)
R <sub>iso</sub>	Isolation Resistance	DC500V 40~60% R.H.	5x10 <sup>10</sup>	1x10 <sup>11</sup>	_	Ω
C <sub>f</sub>	Floating Capacitance	V = 0, f = 1MHz	_	0.6	1	pF

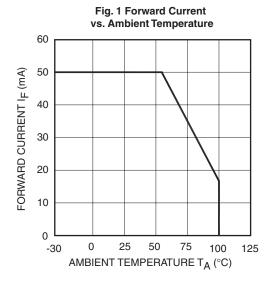
#### Note:

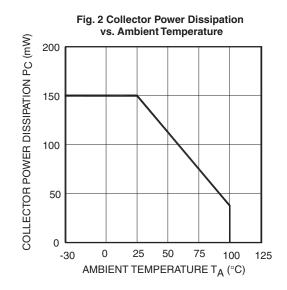
1. Current Transfer Ratio (CTR) =  $I_C/I_F x$  100%.

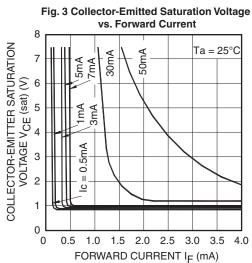
**2** www.fairchildsemi.com

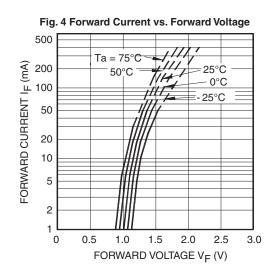
FOD815 Series Rev. 1.0.4

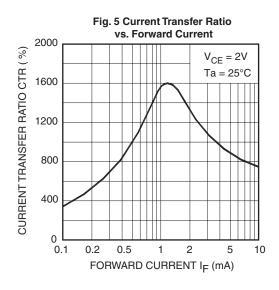
## Typical Electrical/Optical Characteristic Curves ( $T_A = 25$ °C Unless otherwise specified.)

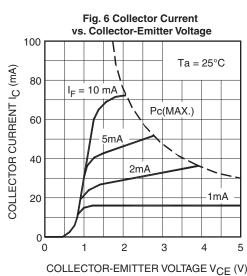












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## Typical Electrical/Optical Characteristic Curves ( $T_A = 25$ °C Unless otherwise specified.)

Fig. 7. Relative Current Transfer Ratio vs. Ambient Temperature

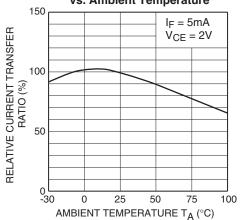


Fig. 8 Collector-Emitter Saturation Voltage vs. Ambient Temperature

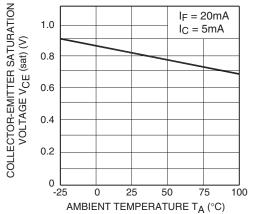


Fig. 9 Collector Dark Current vs. Ambient Temperature

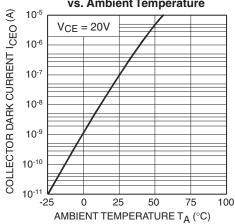


Fig. 10. Response Time vs. Load Resistance

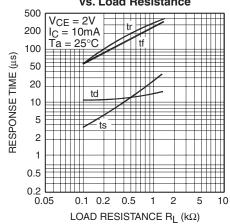
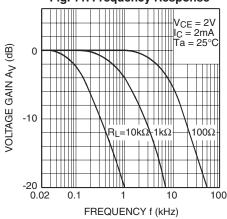
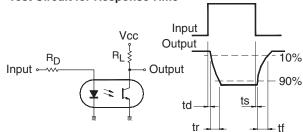


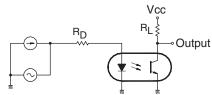
Fig. 11. Frequency Response



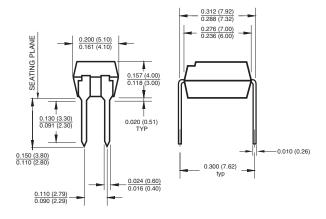
**Test Circuit for Response Time** 



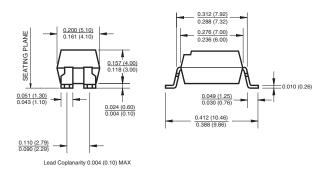
#### **Test Circuit for Frequency Response**



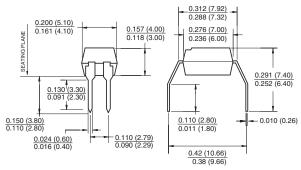
# Package Dimensions (Through Hole)



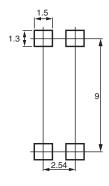
## Package Dimensions (Surface Mount)



# Package Dimensions (0.4" Lead Spacing)



# Footprint Dimensions (Surface Mount))



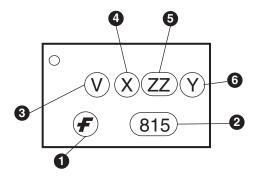
#### Note:

All dimensions are in inches (millimeters)

## **Ordering Information**

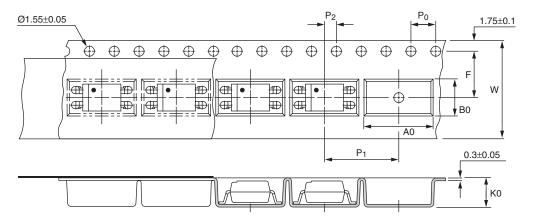
Option	Order Entry Identifier	Description
S	.S	Surface Mount Lead Bend
SD	.SD	Surface Mount; Tape and reel
W	.W	0.4" Lead Spacing
300	.300	VDE Approved
300W	.300W	VDE Approved, 0.4" Lead Spacing
3S	.3\$	VDE Approved, Surface Mount
3SD	.3SD	VDE Approved, Surface Mount, Tape & Reel

## **Marking Information**



Definiti	Definitions		
1	Fairchild logo		
2	Device number		
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)		
4	One digit year code		
5	Two digit work week ranging from '01' to '53'		
6	Assembly package code		

## **Carrier Tape Specifications**

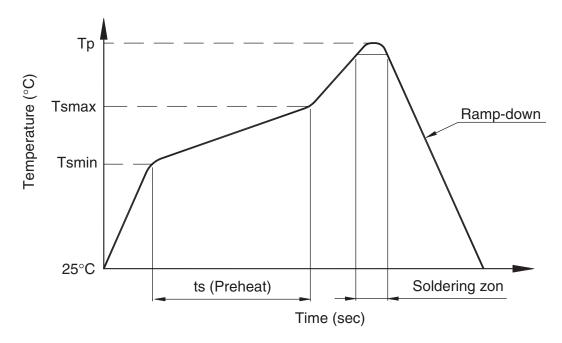


#### Note:

All dimensions are in millimeters.

Description	Symbol	Dimensions in mm (inches)
Tape wide	W	16 ± 0.3 (.63)
Pitch of sprocket holes	P <sub>0</sub>	4 ± 0.1 (.15)
Distance of compartment	F P <sub>2</sub>	7.5 ± 0.1 (.295) 2 ± 0.1 (.079)
Distance of compartment to compartment	P <sub>1</sub>	12 ± 0.1 (.472)
Compartment	A0	10.45 ± 0.1 (.411)
	В0	5.30 ± 0.1 (.209)
	K0	4.25 ± 0.1 (.167)

### **Lead Free Recommended IR Reflow Condition**



Profile Feature	Pb-Sn solder assembly	Lead Free assembly	
Preheat condition (Tsmin-Tsmax / ts)	100°C ~ 150°C 60 ~ 120 sec	150°C ~ 200°C 60 ~120 sec	
Melt soldering zone	183°C 60 ~ 120 sec	217°C 30 ~ 90 sec	
Peak temperature (Tp)	240 +0/-5°C	260 +0/-5°C	
Ramp-down rate	6°C/sec max.	6°C/sec max.	

### **Recommended Wave Soldering condition**

Profile Feature	For all solder assembly	
Peak temperature (Tp)	Max 260°C for 10 sec	

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ActiveArray™	FASTr™	LittleFET™	PowerSaver™	SuperSOT™-3
Bottomless™	FPS™	MICROCOUPLER™	PowerTrench <sup>®</sup>	SuperSOT™-6
Build it Now™	FRFET™	MicroFET™	QFET <sup>®</sup>	SuperSOT™-8
CoolFET™	GlobalOptoisolator™	MicroPak™	QS™	SyncFET™
CROSSVOLT™	GTO™	MICROWIRE™	QT Optoelectronics™	TCM™
DOME™	HiSeC™	MSX™	Quiet Series™	TinyLogic <sup>®</sup>
EcoSPARK™	$I^2C^{TM}$	MSXPro™	RapidConfigure™	TINYOPTO™
E <sup>2</sup> CMOS™	i-Lo™	OCXTM	RapidConnect™	TruTranslation™
EnSigna™	ImpliedDisconnect™	OCXPro™	μSerDes™	UHC™
FACT™	IntelliMAX™	OPTOLOGIC <sup>®</sup>	ScalarPump™	UniFET™
FACT Quiet Series <sup>T</sup>	M	OPTOPLANAR™	SILENT SWITCHER®	UltraFET <sup>®</sup>
Across the board. A	round the world.™	PACMAN™	SMART START™	VCX™
The Power Franchise <sup>®</sup>		POP™	SPM™	Wire™
Programmable Active Droop™		Power247™	Stealth™	

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- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

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