

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	40	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 5)	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	l _D	17.6 12.5	Α
Continuous Drain Current, V _{GS} = 10V (Note 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	76 54	Α
Maximum Continuous Body Diode Forward Current (Note 6)		Is	60	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	100	Α
Avalanche Current, L=0.3mH		I _{AS}	20	Α
Avalanche Energy, L=0.3mH		E _{AS}	60	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	3.1	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	47	°C/W
Total Power Dissipation (Note 6)	P _D	59	W
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	2.5	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

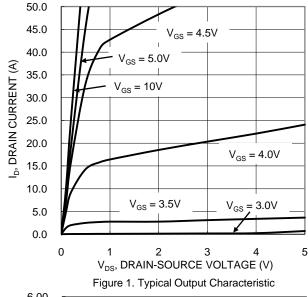
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	-	-	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	-	1	1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I_{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	2	-	4	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	-	-	6	mΩ	$V_{GS} = 10V, I_D = 20A$	
Diode Forward Voltage	V_{SD}	-	1	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	-	2082	-		V_{DS} = 25V, V_{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	-	790	-	pF		
Reverse Transfer Capacitance	C _{rss}	-	113	-			
Gate Resistance	R_g	1	0.46	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_{g}	-	41.9	-		V _{DS} = 30V, I _D = 20A, V _{GS} = 10V	
Gate-Source Charge	Q _{gs}	-	10	-	nC		
Gate-Drain Charge	Q_{gd}	-	11.5	-			
Turn-On Delay Time	t _{D(ON)}	-	7	-		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 20A, R_{G} = 3\Omega$	
Turn-On Rise Time	t _R	-	11.5	-	20		
Turn-Off Delay Time	t _{D(OFF)}	-	15.6	-	ns		
Turn-Off Fall Time	t _F	-	8.8	-			
Body Diode Reverse Recovery Time	t _{RR}	-	29.9	-	ns		
Body Diode Reverse Recovery Charge	Q_{RR}	-	23	-	nC	$I_F = 20A$, di/dt = 100A/ μ s	

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).7. Short duration pulse test used to minimize self-heating effect.8. Guaranteed by design. Not subject to product testing.







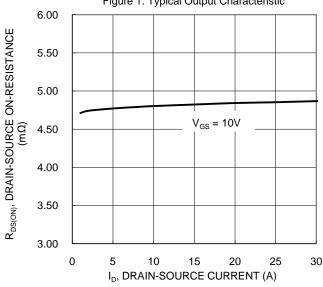


Figure 3. Typical On-Resistance vs Drain Current and Gate Voltage

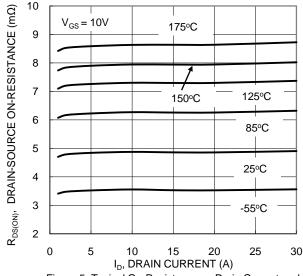
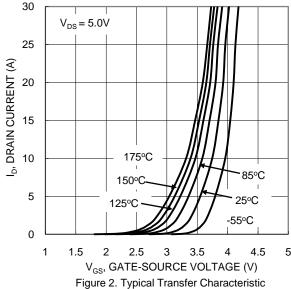
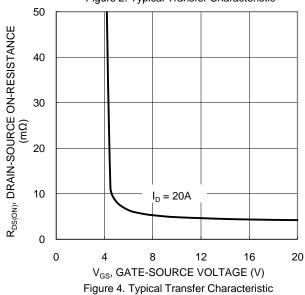


Figure 5. Typical On-Resistance vs Drain Current and Temperature





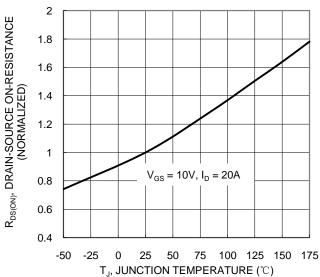
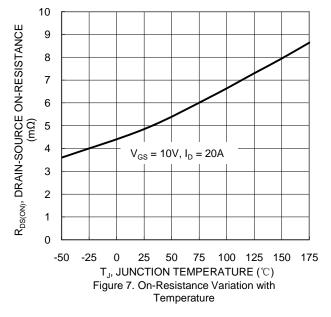
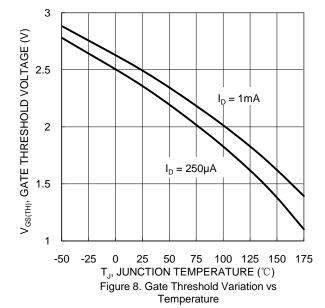


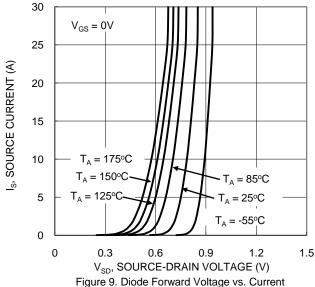
Figure 6. On-Resistance Variation with Temperature

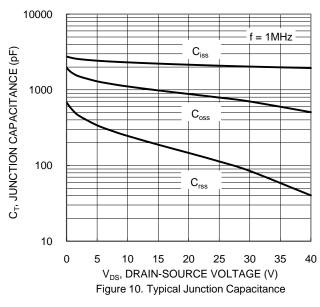


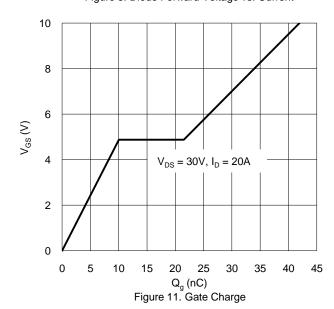


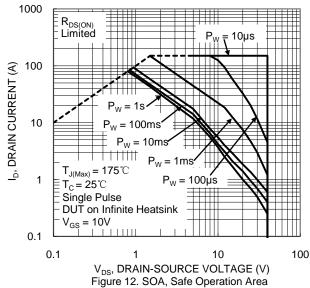




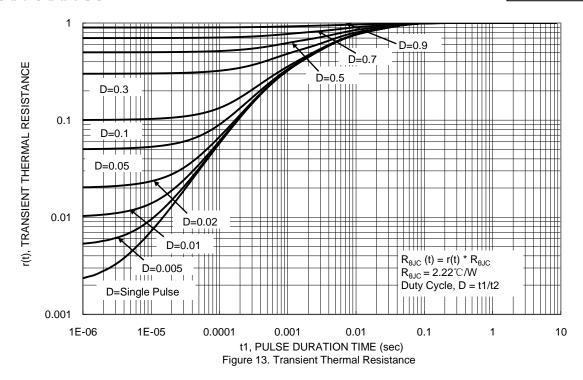










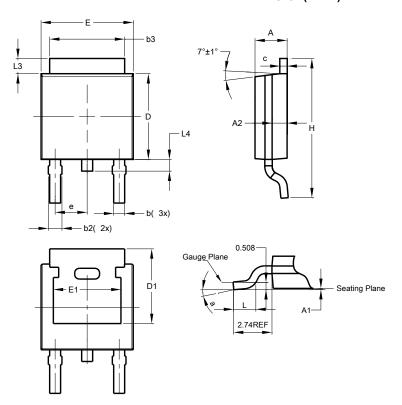




Package Outline Dimensions

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TO252 (DPAK)

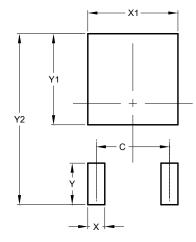


TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
Г	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)		
C	4.572		
Х	1.060		
X1	5.632		
Υ	2.600		
Y1	5.700		
Y2	10.700		



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