



### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Cha	aracteristic		Symbol	Value	Unit	
Continuous Drain current $V_{GS} = 10V$ $(Note 3)$ $T_A = 70^{\circ}C (Note 3)$ $(Note 2)$ $I_D$ $-16.1$ $-12.9$ $-10.6$ Pulsed Drain current $V_{GS} = 10V$ $(Note 4)$ $I_{DM}$ $-41.9$ AContinuous Source current (Body diode) $(Note 3)$ $I_S$ $-12.6$ A	Drain-Source voltage			V <sub>DSS</sub>	-30	V	
Continuous Drain current $V_{GS} = 10V$ $T_{A}=70^{\circ}C$ (Note 3) (Note 2) $I_D$ $-12.9$ $-10.6$ APulsed Drain current $V_{GS}=10V$ (Note 4) $I_{DM}$ $-41.9$ AContinuous Source current (Body diode)(Note 3) $I_S$ $-12.6$ A	Gate-Source voltage			V <sub>GS</sub>	±20	V	
Note 2Image: Note 2Pulsed Drain current $V_{GS}$ = 10V(Note 4) $I_{DM}$ -10.6Continuous Source current (Body diode)(Note 3) $I_S$ -12.6ADulad Course summer (Dack diode)(Note 3) $I_S$ -12.6A			(Note 3)	I <sub>D</sub>	-16.1		
Pulsed Drain current $V_{GS}$ = 10V (Note 4) $I_{DM}$ -41.9 A   Continuous Source current (Body diode) (Note 3) $I_S$ -12.6 A	Continuous Drain current	$V_{GS} = 10V$	T <sub>A</sub> =70°C (Note 3)		-12.9	A	
Continuous Source current (Body diode) (Note 3) Is -12.6 A			(Note 2)		-10.6		
	Pulsed Drain current V <sub>GS</sub> = 10V (Note 4)		(Note 4)	I <sub>DM</sub>	-41.9	A	
Pulsed Source current (Body diode) (Note 4)	Continuous Source current (Body diode) (Note 3)			Is	-12.6	A	
I uised Source current (body diode) (Note 4) ISM -41.5	Pulsed Source current (Body diode) (Note 4)			I <sub>SM</sub>	-41.9	A	

### Thermal Characteristics @TA = 25°C unless otherwise specified

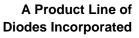
Characteristic		Symbol	Value	Unit
	(Note 2)		4.3 34.5	<b>*</b>
Power dissipation Linear derating factor	(Note 3)	PD	10.0 80.0	₩ mW/°C
	(Note 5)		2.15 17.2	
	(Note 2)		29.0	
Thermal Resistance, Junction to Ambient	(Note 3)	R <sub>0</sub> JA	12.5	2011/
	(Note 5)		58.0	°C/W
Thermal Resistance, Junction to Lead (Note 6)		$R_{ heta JL}$	1.02	
Operating and storage temperature range	TJ, TSTG	-55 to 150	°C	

2. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition. Notes:

3. Same as note 2, except the device is pulsed with D = 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature. 5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is

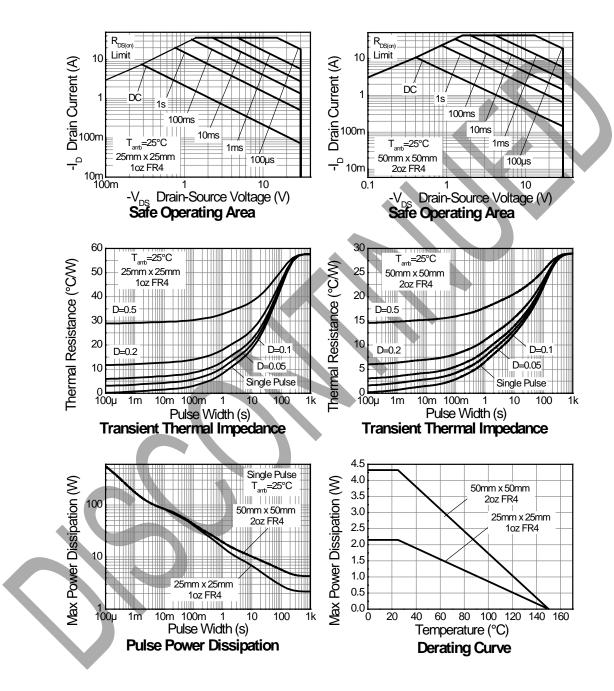
- measured when operating in a steady-state condition.
- 6. Thermal resistance from junction to solder-point (at the end of the drain lead).







## **Thermal Characteristics**







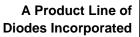
DMP3025LK3

Characteristic		Min	Тур	Max	Unit	Tes	t Condition
OFF CHARACTERISTICS	Symbol		51		1		
Drain-Source Breakdown Voltage		-30			V	I <sub>D</sub> = -250μA, <sup>1</sup>	√ <sub>GS</sub> = 0V
Zero Gate Voltage Drain Current				-0.5	μA	V <sub>DS</sub> = -30V, V	<sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	V <sub>GS</sub> = ±20V, \	/ <sub>DS</sub> = 0V
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.0		-3.0	V	I <sub>D</sub> = -250μA, \	/ <sub>DS</sub> = V <sub>GS</sub>
Static Drain-Source On-Resistance (Note 7)	P		_	0.025		Vgs= -10V, Ic	)= -7.1A
	R <sub>DS (ON)</sub>			0.041		V <sub>GS</sub> = -4.5V, I	<sub>D</sub> = -5.5A
Forward Transconductance (Notes 7 & 8)	<b>g</b> fs		18.6		S	V <sub>DS</sub> = -15V, I <sub>D</sub>	∋= -7.1A
Diode Forward Voltage (Note 7)	V <sub>SD</sub>		-0.80	-1.2	V	I <sub>S</sub> = -1.7A, V <sub>GS</sub> = 0V	
Reverse recovery time (Note 8)			16.2	_	ns	1 2.24 di/dt=1004/up	
Reverse recovery charge (Note 8)	Q <sub>rr</sub>		10		nC	–I <sub>S</sub> = -2.2A, di/dt= 100A/μs	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>		1678	-	pF	−V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V - f= 1MHz	
Output Capacitance	C <sub>oss</sub>		303		рF		
Reverse Transfer Capacitance	C <sub>rss</sub>		178	1	pF		
Total Gate Charge	Qg		16.5		nC	V <sub>GS</sub> = -4.5V	
Total Gate Charge	Qg		31.6		nC	V <sub>DS</sub> = -15V	
Gate-Source Charge	Q <sub>gs</sub>		4.3		nC	V <sub>GS</sub> = -10V	I <sub>D</sub> = -7.1A
Gate-Drain Charge	Q <sub>gd</sub>	1	6.2		nC		
Turn-On Delay Time (Note 9)	t <sub>D(on)</sub>		3.5		ns		
Turn-On Rise Time (Note 9)	tr	—	4.9	_	ns	V <sub>DD</sub> = -15V, V	<sub>GS</sub> = -10V
Turn-Off Delay Time (Note 9)	t <sub>D(off)</sub>	ł	44		ns	$I_D = -1A, R_G \cong$	6.0Ω
Turn-Off Fall Time (Note 9)	tf		23		ns		

Notes:

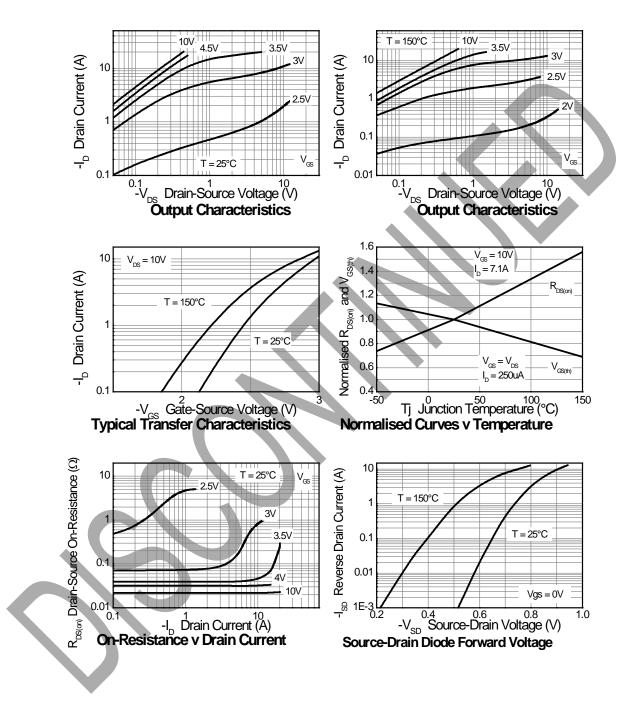
Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%
For design aid only, not subject to production testing.
Switching characteristics are independent of operating junction temperatures.







# **Typical Characteristics**

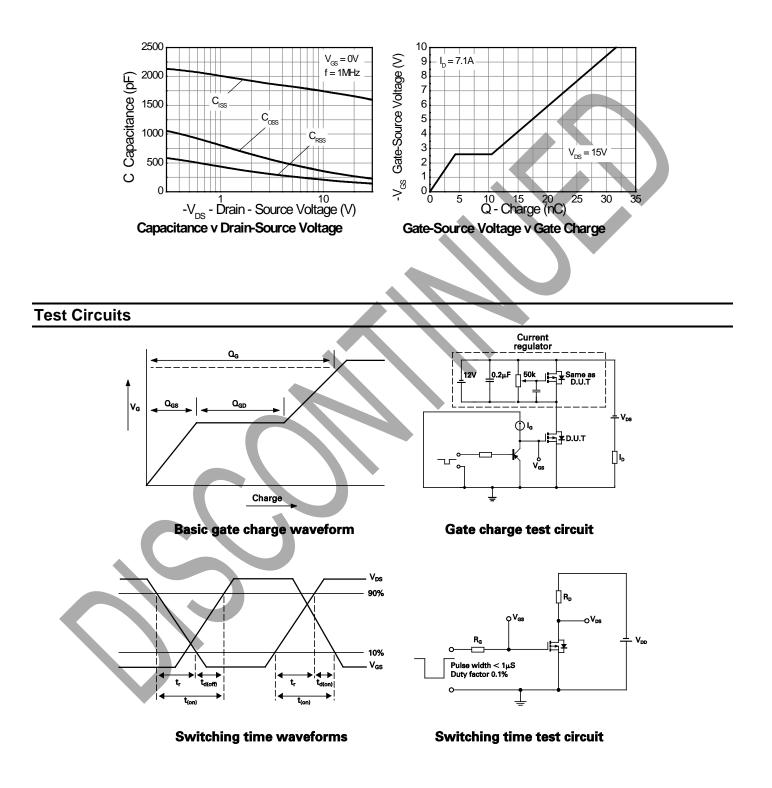






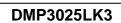


## **Typical Characteristics - continued**

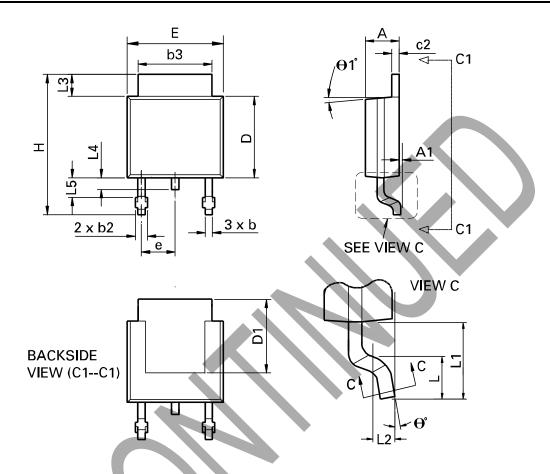








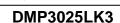
# **Package Outline Dimensions**



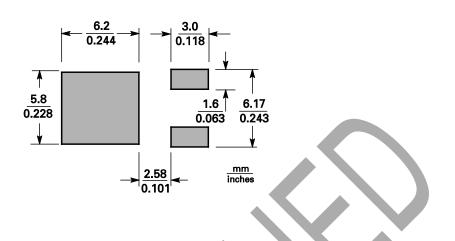
DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min	Max	Min	Max		Min	Max	Min	Max
А	0.086	0.094	2.18	2.39	е	0.090 BSC		2.29 BSC	
A1	-	0.005	-	0.127	н	0.370	0.410	9.40	10.41
b	0.020	0.035	0.508	0.89	L	0.055	0.070	1.40	1.78
b2	0.030	0.045	0.762	1.14	L1	0.108 REF		2.74 REF	
b3	0.205	0.215	5.21	5.46	L2	0.020 BSC		0.508 BSC	
с	0.018	0.024	0.457	0.61	L3	0.035	0.065	0.89	1.65
c2	0.018	0.023	0.457	0.584	L4	0.025	0.040	0.635	1.016
D	0.213	0.245	5.41	6.22	L5	0.045	0.060	1.14	1.52
D1	0.205	-	5.21	-	•1°	0°	10°	0°	10°
E	0.250	0.265	6.35	6.73	•	0°	15°	0°	15°
E1	0.170	-	4.32	-	-	-	-	-	-







## **Suggested Pad Layout**



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