

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current (Note 6) V _{GS} = -4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-3.8 -3.0	А
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	-1.3	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-20	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)		P_{D}	0.8	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	163	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	114	C/VV	
Total Power Dissipation (Note 6)		P_{D}	1.3	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	94	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	R _{0JA}	66	C/VV	
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	°C	

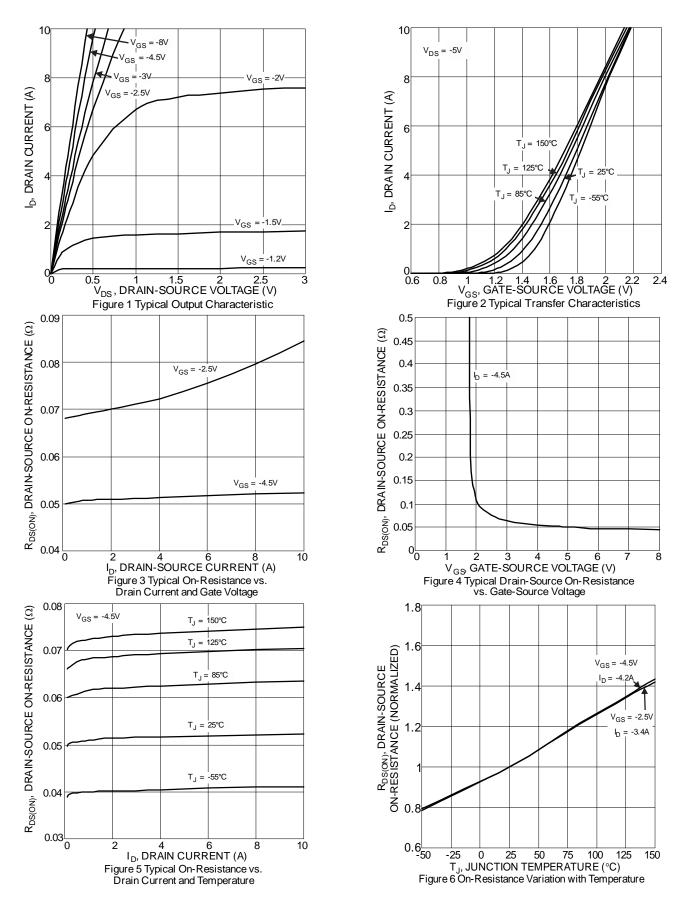
Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}			-1.0	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.4	I	-1.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
			51	62		VGS = -4.5V, ID = -4.2A
Static Drain-Source On-Resistance	R _{DS(ON)}	_	71	90	mΩ	Vgs = -2.5V, $ID = -3.4A$
			116	150		VGS = -1.8V, ID = -2.0A
Diode Forward Voltage	V_{SD}		-0.7	-1.1	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	487	_	pF	V 99V V 9V
Output Capacitance	Coss	_	60		pF	$V_{DS} = -20V, V_{GS} = 0V,$ 1 = 1.0MHz
Reverse Transfer Capacitance	C _{rss}		53	_	pF	1 = 1.0lvinz
Gate Resistance	R_{G}		39	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Q_{G}	I	6.3	_	nC	4.51/.)/
Gate-Source Charge	Q _{GS}	_	0.7	_	nC	$V_{GS} = -4.5V, V_{DS} = -4V,$
Gate-Drain Charge	Q_{GD}		1.4	_	nC	$I_D = -3.5A$
Turn-On Delay Time	t _{D(ON)}		5.3	_	ns	$V_{DS} = -4V, V_{GS} = -4.5V,$ $I_{D} = -1.0A, R_{G} = 6\Omega$
Turn-On Rise Time	t _R	_	15.7	_	ns	
Turn-Off Delay Time	t _{D(OFF)}		38.5	_	ns	
Turn-Off Fall Time	t _F		23.2	_	ns	
Body Diode Reverse Recovery Time	t _{RR}		7.5	_	ns	$I_S = -2.0A$, di/dt = -100A/ μ s
Body Diode Reverse Recovery Charge	Q_{RR}	_	1.9	_	nC	$I_S = -2.0A$, di/dt = -100A/ μ s

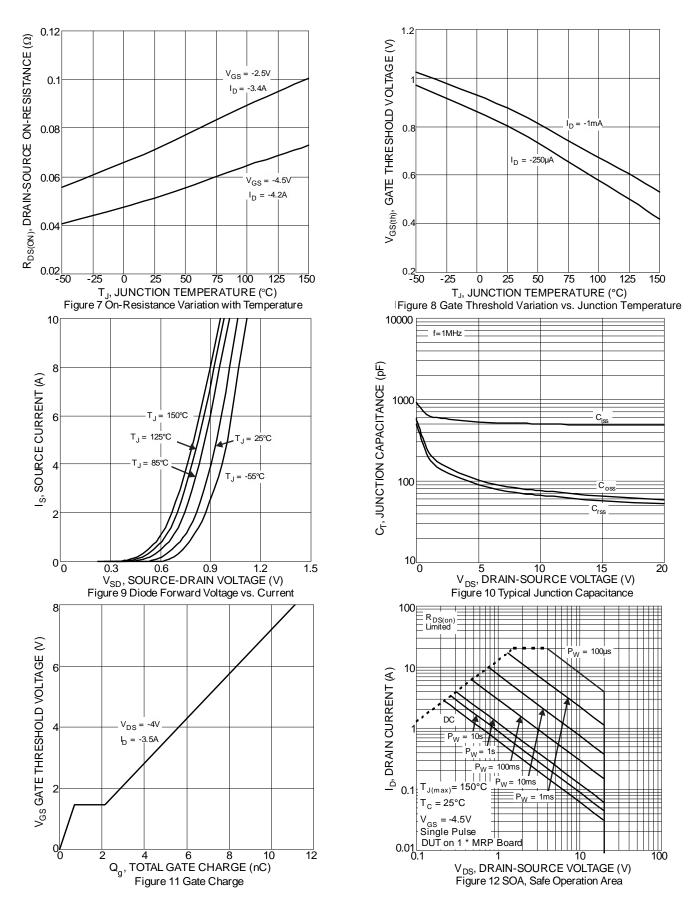
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Solution in the district of States (2004). 202 copper, with 1 linch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

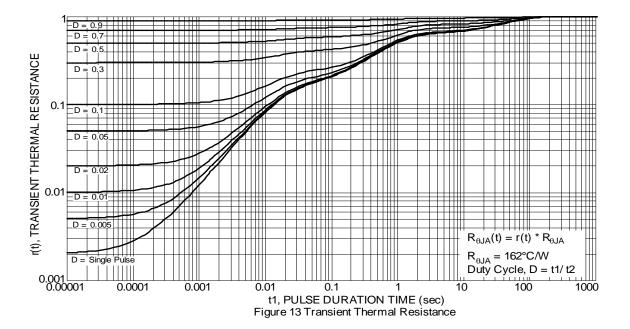








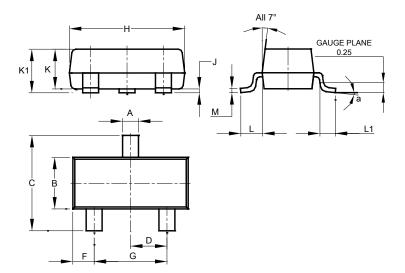






Package Outline Dimensions

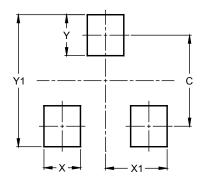
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K 1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	29



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