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DM74AS32 Quad 2-Input OR Gate

General Description

This device contains four independent gates, each of which performs the logic OR function.

- Features
- Switching specifications at 50 pF
 Switching specifications guaranteed over full
- temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky, low power Schottky, and advanced low power Schottky TTL counterpart

April 1984

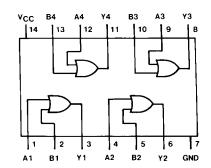
Revised March 2000

Improved AC performance over Schottky, low power Schottky and advanced low power Schottky counterparts

Ordering Code:

Order Number	Package Number	Package Description
DM74AS32M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74AS32SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
DM74AS32N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Connection Diagram



Function Table

 $\bm{Y}=\bm{A}+\bm{B}$

Inp	Inputs		
Α	В	Y	
L	L	L	
L	н	н	
н	L	н	
н	н	Н	

H = HIGH Logic Level L = LOW Logic Level

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Absolute Maximum Ratings(Note 1)

7V
7V
$0^{\circ}C$ to $+70^{\circ}C$
65°C to +150°C
84.0°C/W
114.0°C/W

Note 1: The Absolute Maximum Ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The Recommended Operating Conditions table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
√ _{IH}	HIGH Level Input Voltage	2			V
√ _{IL}	LOW Level Input Voltage			0.8	V
он	HIGH Level Output Current			-2	mA
OL	LOW Level Output Current			20	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

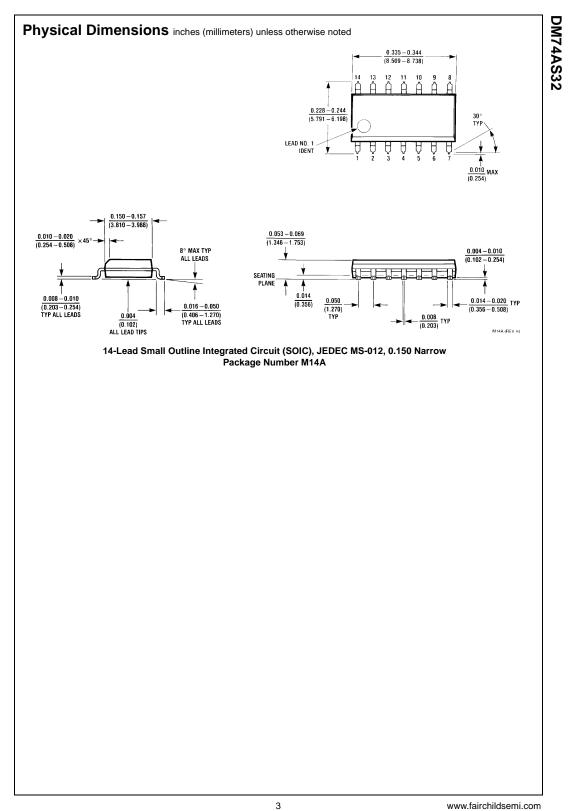
over recommended operating free air temperature range. All typical values are measured at V_{CC} = 5V, T_A = 25°C.

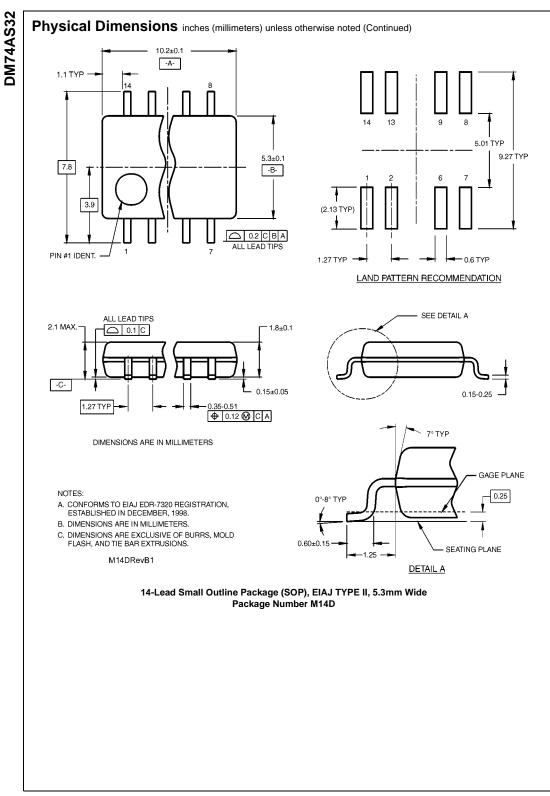
Symbol	Parameter	Condition	s	Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V, I_I = -18 \text{ mA}$				-1.2	V
V _{OH}	HIGH Level	$V_{CC} = 4.5V$ to 5.5V		V 2		v	
	Output Voltage	$I_{OH} = -2 \text{ mA}$		V _{CC} – 2		v	v
V _{OL}	LOW Level	$V_{CC} = 4.5V$			0.35	0.5 \	V
	Output Voltage	$I_{OL} = 20 \text{ mA}$			0.35		v
I _I	Input Current at Max Input Voltage	$V_{CC}=5.5V,V_{IH}=7V$				0.1	mA
IIH	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$				20	μA
IIL	LOW Level Input Current	$V_{CC} = 5.5 V, V_{IL} = 0.4 V$				-0.5	mA
I _O	Output Drive Current	$V_{CC} = 5.5 V, V_{O} = 2.25 V$		-30		-112	mA
I _{CC}	Supply Current	$V_{CC} = 5.5V$	Outputs HIGH		7.3	12	mA
			Outputs LOW		16.5	26.6	mA

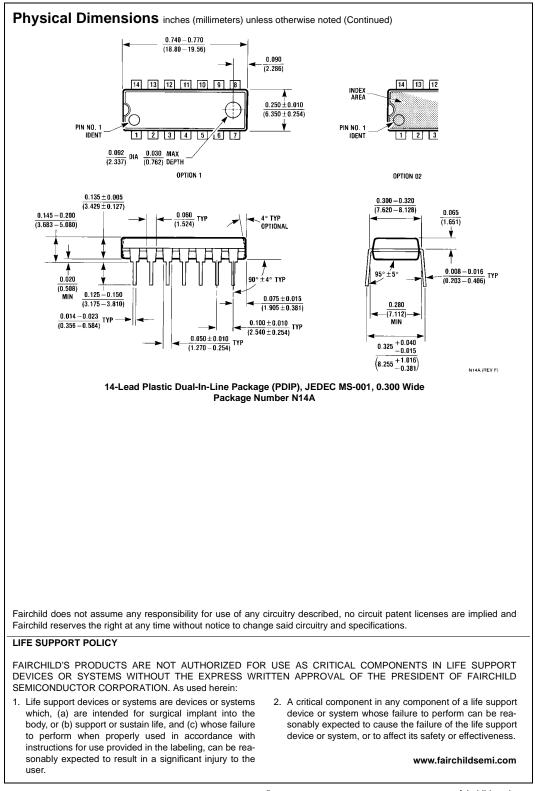
Switching Characteristics

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	V _{CC} = 4.5V to 5.5V	1	5.8	ns
	LOW-to-HIGH Level Output	$R_L = 500\Omega$			
t _{PHL}	Propagation Delay Time	C _L = 50 pF	1	5.8	ns
	HIGH-to-LOW Level Output				

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DM74AS32 Quad 2-Input OR Gate

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