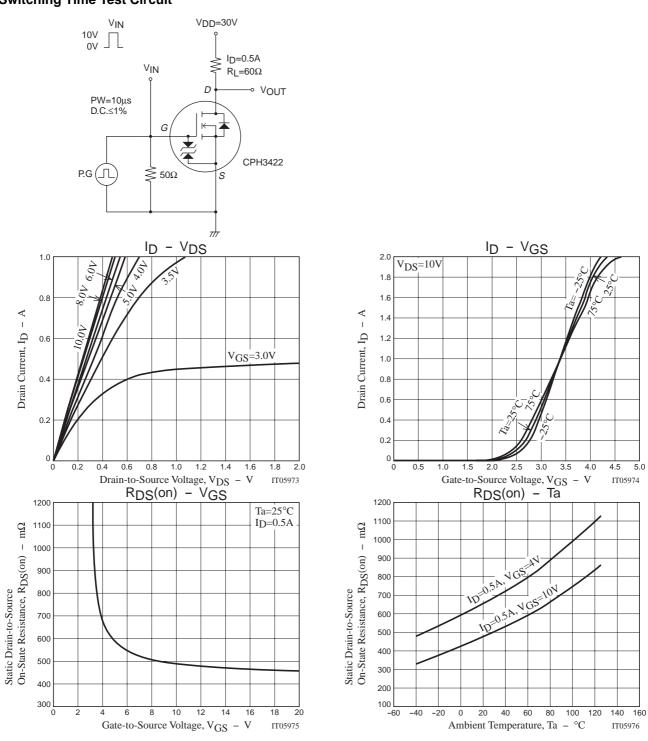
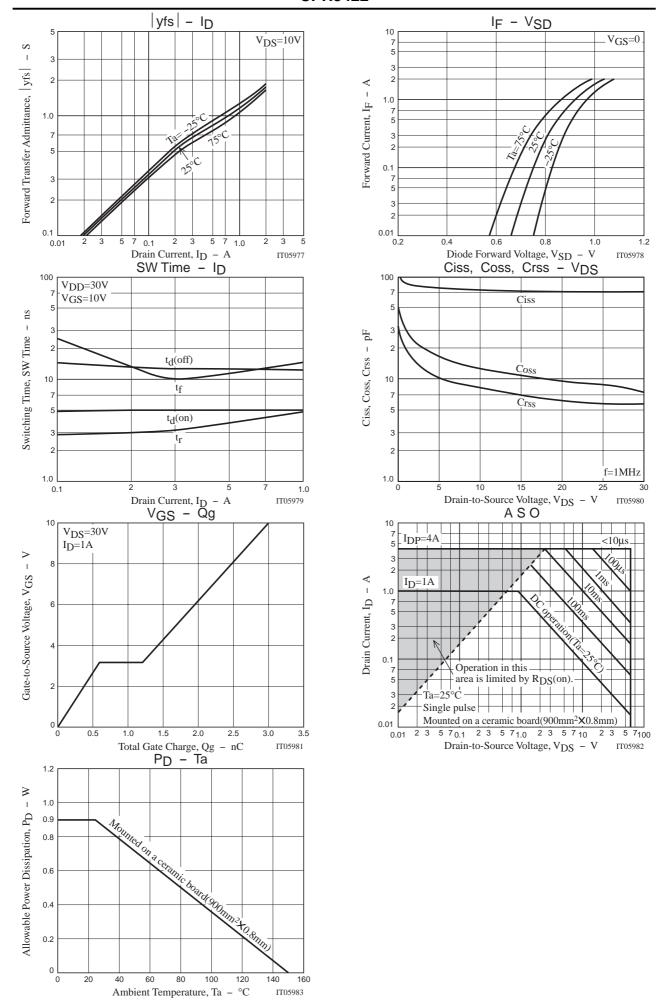
Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Input Capacitance	Ciss	V <sub>DS</sub> =20V, f=1MHz		70		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		9.0		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =20V, f=1MHz		6.5		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		5		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		4		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		12		ns
Fall Time	tf	See specified Test Circuit.		12		ns
Total Gate Charge	Qg	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =1A		3.0		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =1A		0.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =1A		0.6		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0		0.9	1.2	V

## **Switching Time Test Circuit**



## **CPH3422**



Note on usage: Since the CPH3422 is designed for high-speed switching applications, please avoid using this device in the vicinity of highly charged objects.

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