Symbol	Parameter	Value	Unit
P_{tot}	Total Dissipation at T_C = 25 °C	70	W
T_{stg}	Storage Temperature	-65 to 150	°C
T_J	Max. Operating Junction Temperature	150	°C

Table 3: Thermal Data

$R_{thj\text{-}case}$	Thermal Resistance Junction-Case	Max	1.78	°C/W
$R_{thj\text{-}amb}$	Thermal Resistance Junction-Ambient	Max	62.5	°C/W

Table 4: Electrical Characteristics (T_{case} = 25 °C unless otherwise specified)

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
I_{CES}	Collector Cut-off Current (V_{BE} = 0 V)	V_{CE} = 700 V				100	µA
		V_{CE} =700 V	T_j = 125 °C			500	µA
I_{CEO}	Collector Cut-off Current (I_B = 0)	V_{CE} = 400 V				250	µA
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage (I_C = 0)	I_E = 10 mA		9		18	V
$V_{CEO(sus)}$*	Collector-Emitter Sustaining Voltage (I_B = 0)	I_C = 100 mA	L = 25 mH	400			V
$V_{CE(sat)}$*	Collector-Emitter Saturation Voltage	I_C = 0.5 A	I_B = 0.1 A			0.7	V
		I_C = 1 A	I_B = 0.2 A			1	V
		I_C = 2.5 A	I_B = 0.5 A			1.5	V
		I_C = 4 A	I_B = 1 A		0.5		V
$V_{BE(sat)}$*	Base-Emitter Saturation Voltage	I_C = 0.5 A	I_B = 0.1 A			1.1	V
		I_C = 1 A	I_B = 0.2 A			1.2	V
		I_C = 2.5 A	I_B = 0.5 A			1.3	V
h_{FE}*	DC Current Gain	I_C = 10 mA	V_{CE} = 5 V	10			
		I_C = 2 A	V_{CE} = 5 V	12		32	
	RESISTIVE LOAD	V_{CC} =200 V	I_C = 2 A				
t_s	Storage Time	I_{B1} = 0.4 A	$V_{BE(off)}$ = -5 V		0.6		µs
t_f	Fall Time	R_{BB} = 0 Ω	L = 200 µH		0.1		µs
		(see figure 15)					
	INDUCTIVE LOAD	V_{CC} =250 V	I_C = 2 A				
t_s	Storage Time	I_{B1} = 0.4 A	I_{B2} = -0.4 A	2		2.9	µs
t_f	Fall Time	Tp = 30 µs	(see figure 14)		0.2		µs

* Pulsed: Pulsed duration = 300 μs, duty cycle ≤ 1.5 %.

.

Figure 3: Safe Operating Area

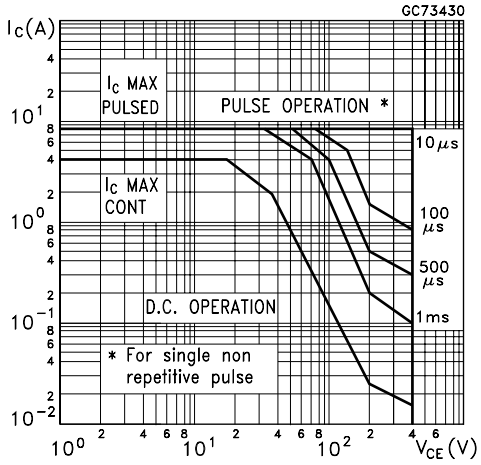


Figure 4: DC Current Gain

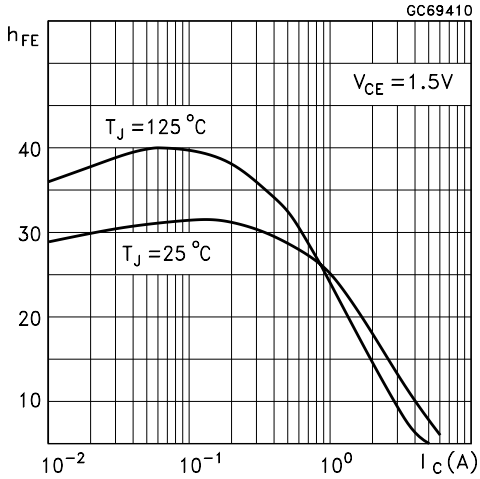


Figure 5: Collector-Emitter Saturation Voltage

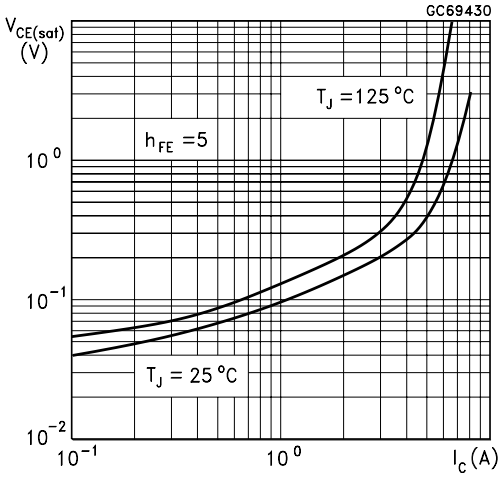


Figure 6: Derating Current

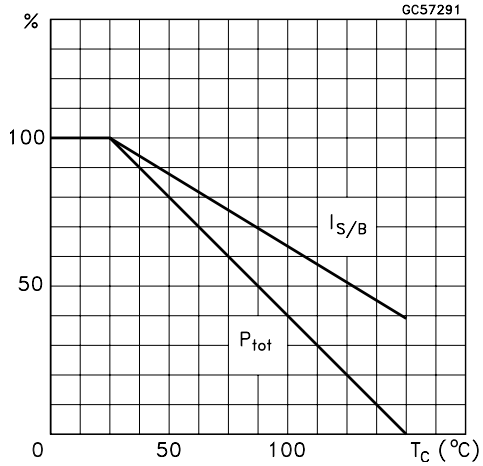


Figure 7: DC Current Gain

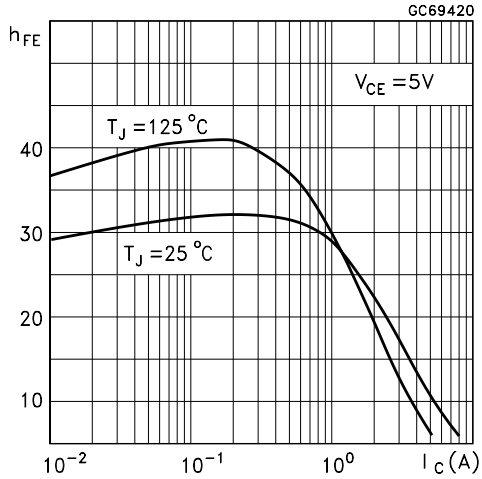


Figure 8: Base-Emitter Saturation Voltage

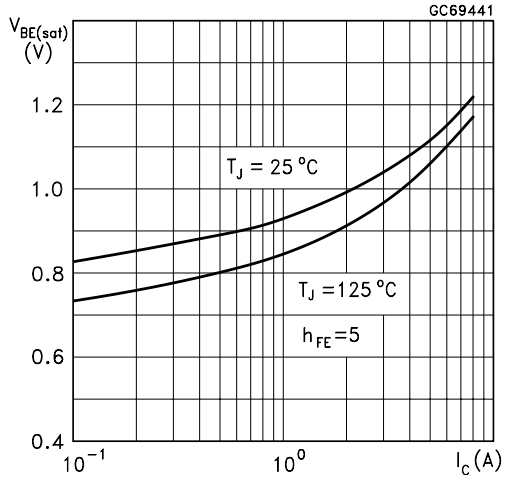


Figure 9: Inductive Load Fall Time

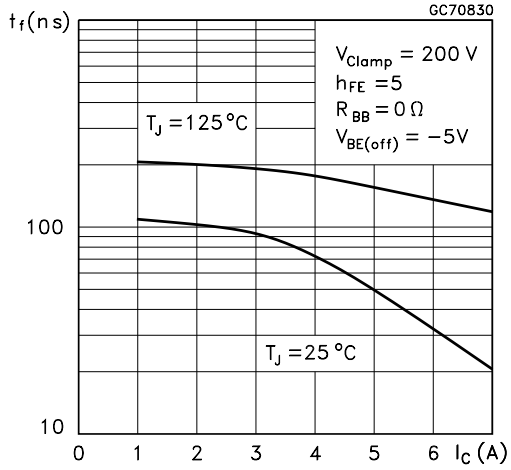


Figure 10: Resistive Load Fall Time

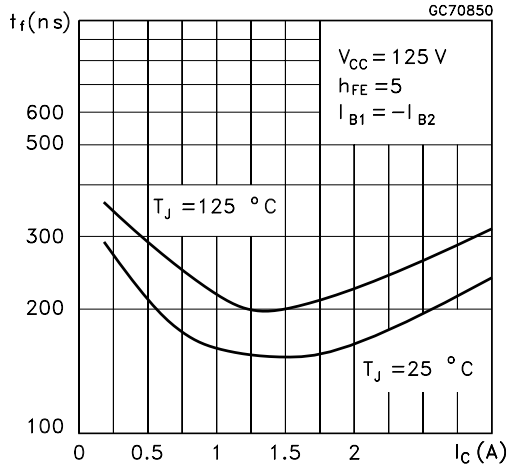


Figure 11: Reverse Biased Operating Area

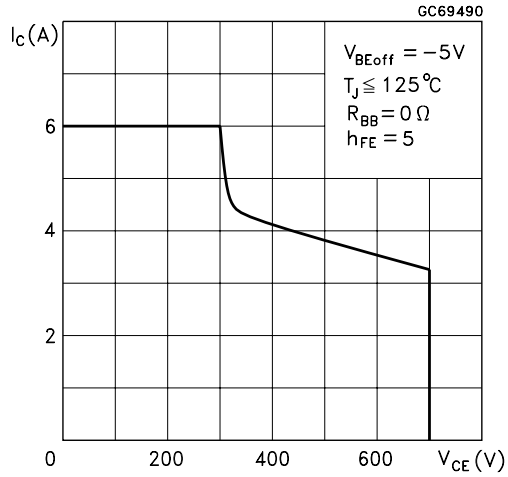


Figure 12: Inductive Load Stoarage Time

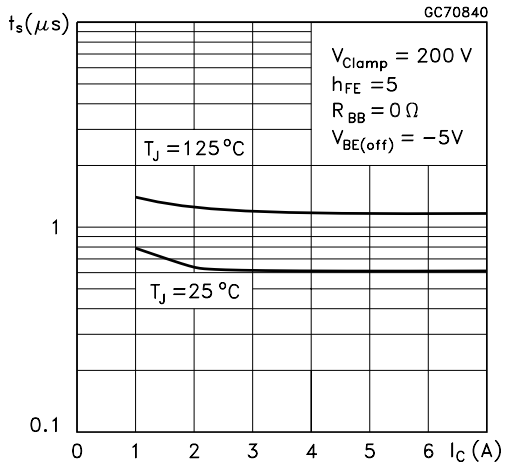


Figure 13: Resistive Load Stoarage Time

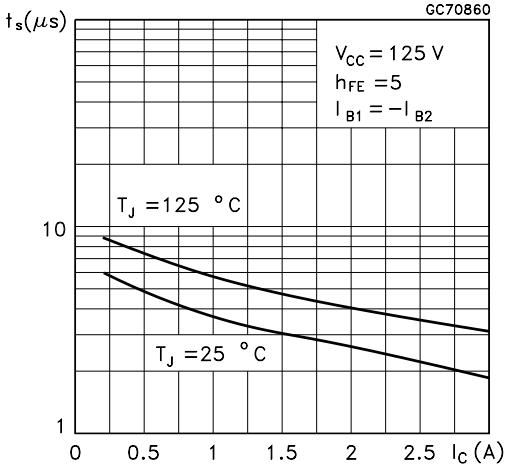


Figure 14: Inductive Load Switching Test Circuit

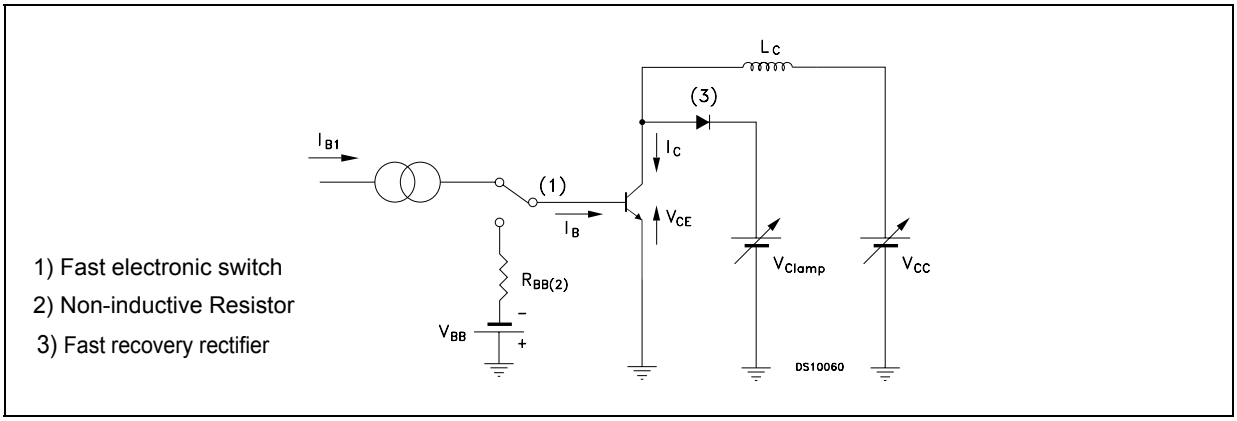
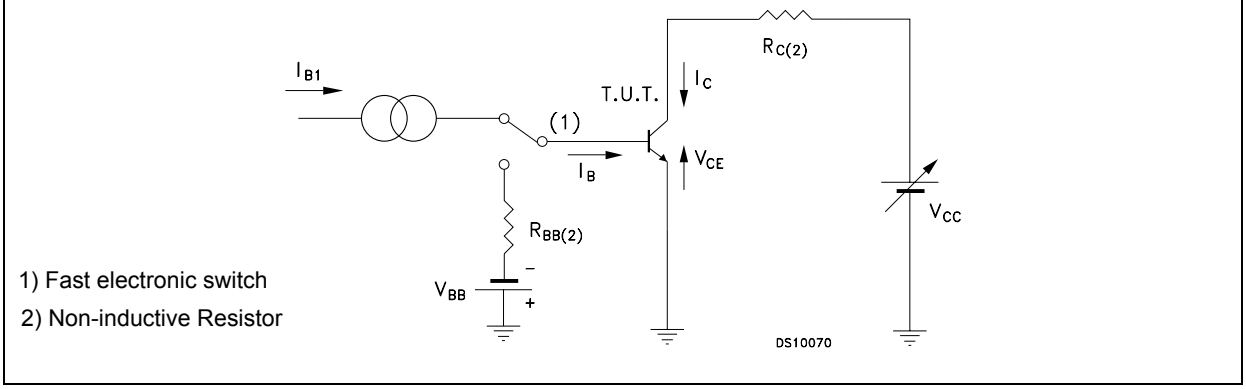


Table 15: Restistive Load Switching Test Circuit



TO-220 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.15		1.70	0.045		0.066
c	0.49		0.70	0.019		0.027
D	15.25		15.75	0.60		0.620
E	10		10.40	0.393		0.409
e	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.052
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
øP	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116

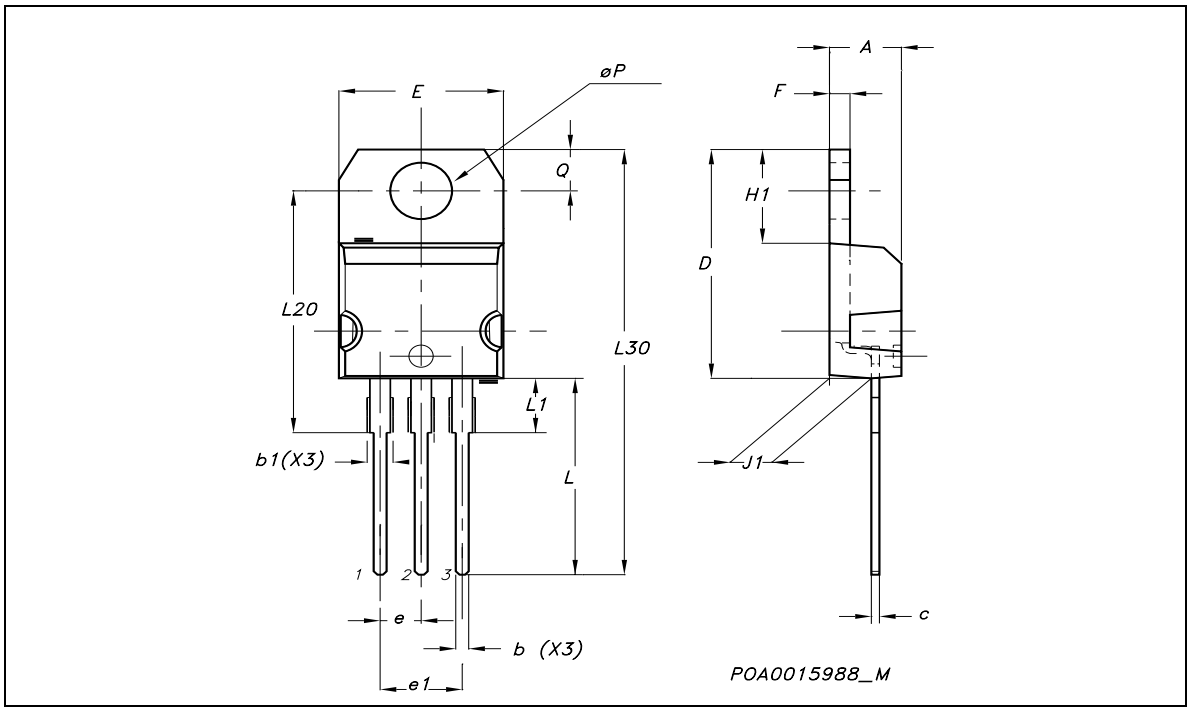


Table 5:

Version	Release Date	Change Designator
01-Oct-2002	1	First Release.
15-Feb-2005	1	Added table 1 on page 1.