

#### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	50	V
Drain-Gate Voltage $R_{GS} \le 20 K\Omega$	V <sub>DGR</sub>	50	V
Gate-Source Voltage Continuous	N/	±20	V
Gate-Source Voltage Non Repetitive, Pulse Width<50µs	V <sub>GSS</sub>	±40	V
Drain Current Continuous	I <sub>D</sub>	200	mA
Pulsed Drain Current (10µs Pulse Duty Cycle = 1%)	I <sub>DM</sub>	1	А

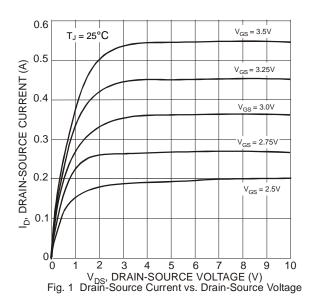
# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

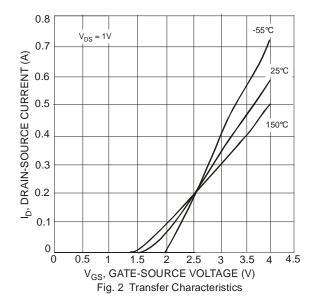
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>0</sub> JA	417	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-55 to +150	°C

#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

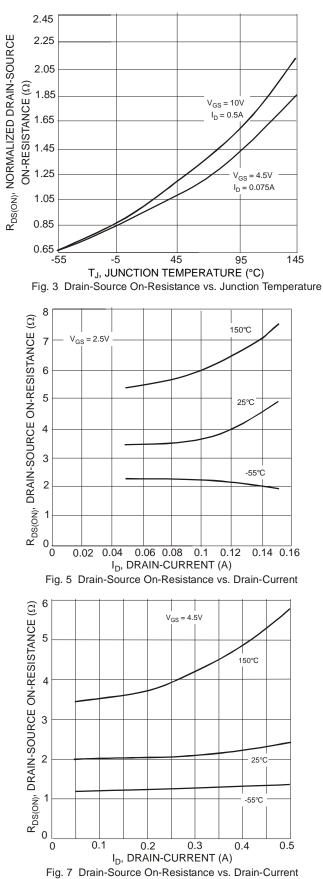
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)						•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	50	75	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS		_	0.5	μΑ	$V_{DS} = 50V, V_{GS} = 0V$	
Gate-Body Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)						·	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5	1.2	1.5	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	1.4	3.5	Ω	$V_{GS} = 10V, I_D = 0.22A$	
Forward Transconductance	<b>g</b> fs	100	_	_	mS	V <sub>DS</sub> = 25V, I <sub>D</sub> = 0.2A, f = 1.0KHz	
DYNAMIC CHARACTERISTICS						·	
Input Capacitance	Ciss		_	50	pF		
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	C <sub>rss</sub>			8.0	pF	1	
SWITCHING CHARACTERISTICS				•		•	
Turn-On Delay Time	t <sub>D(ON)</sub>			20	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>			20	ns	$V_{DD} = 30V, I_D = 0.2A, R_{GEN} = 50\Omega$	

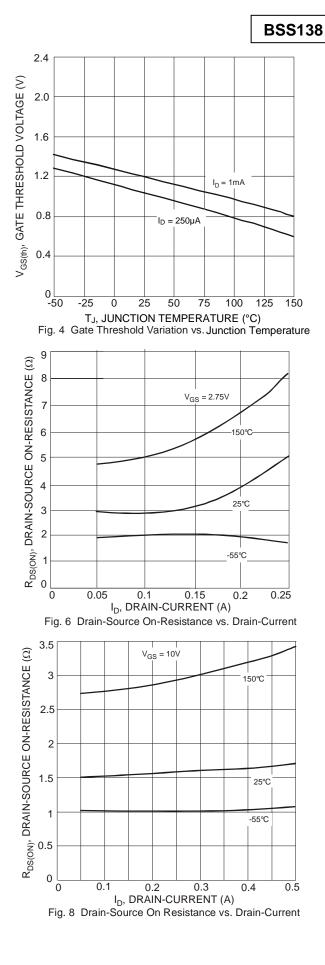
 Device mounted on FR-4 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.
Short duration pulse test used to minimize self-heating effect. Notes:





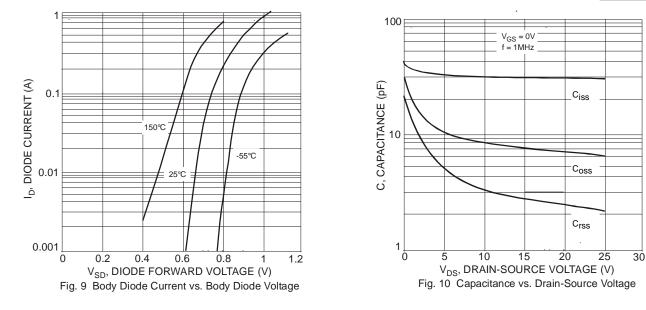






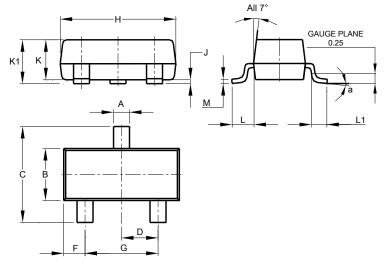


### **BSS138**



## **Package Outline Dimensions**

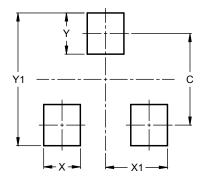
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
C	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
М	0.085	0.150	0.110		
а	0°	8°			
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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