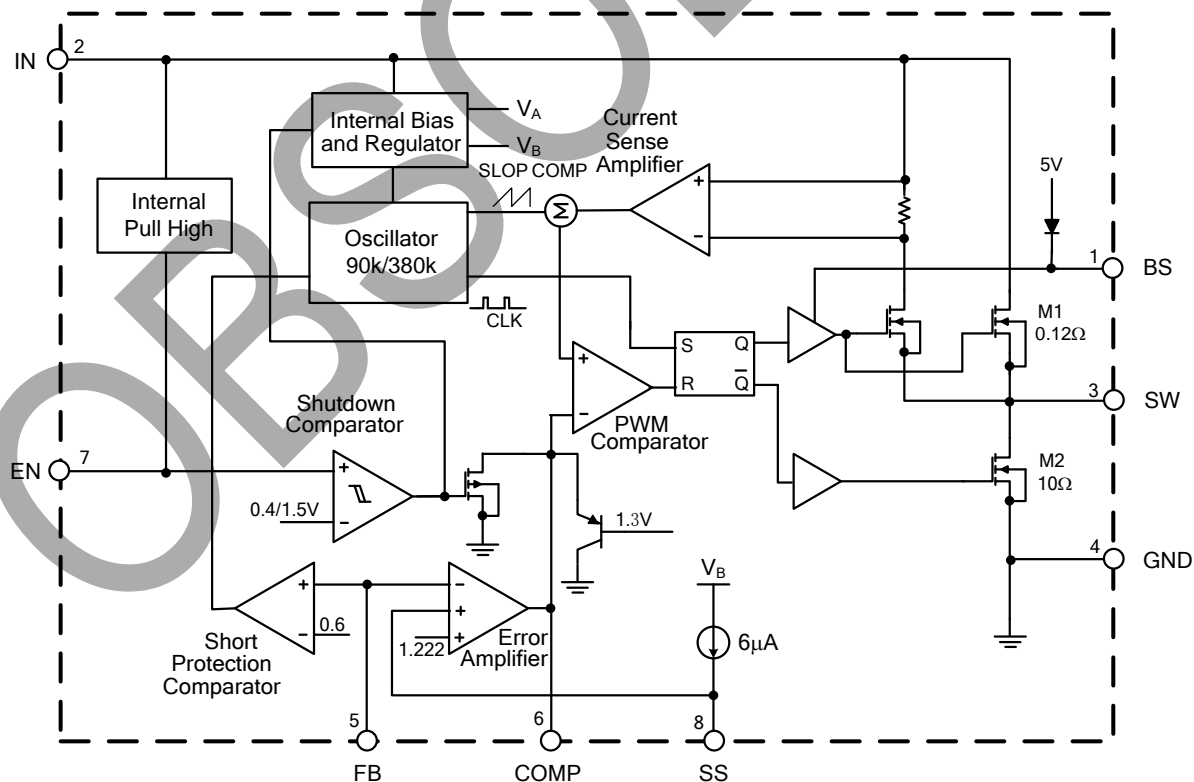


Pin Descriptions

Pin Number	Pin Name	Function
1	BS	Bootstrap pin. A bootstrap capacitor is connected between the BS pin and SW pin. The voltage across the bootstrap capacitor drives the internal high-side NMOS switch
2	IN	Supply input pin. A capacitor should be connected between the IN pin and GND pin to keep the DC input voltage constant
3	SW	Power switch output pin. This pin is connected to the inductor and bootstrap capacitor
4	GND	Ground pin
5	FB	Feedback pin. This pin is connected to an external resistor divider to program the system output voltage. When the FB pin voltage exceeds 20% of the nominal regulation value of 1.222V, the over voltage protection is triggered. When the FB pin voltage is below 0.6V, the oscillator frequency is lowered to realize short circuit protection
6	COMP	Compensation pin. This pin is the output of the transconductance error amplifier and the input to the current comparator. It is used to compensate the control loop. Connect a series RC network from this pin to GND. In some cases, an additional capacitor from this pin to GND pin is required
7	EN	Control input pin. Forcing this pin above 1.5V or set this pin floating enables the IC. Forcing this pin below 0.5V shuts down the IC. When the IC is in shutdown mode, all functions are disabled to decrease the supply current below 1μA
8	SS	Soft-start control input. SS controls the soft-start period. Connect a capacitor from SS to GND to set the soft-start period. A 0.1μF capacitor sets the soft-start period to 10ms. To disable the soft-start feature, leave SS disconnected

Functional Block Diagram



Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Value	Unit
V_{IN}	IN Pin Voltage	-0.3 to 20	V
V_{EN}	EN Pin Voltage	-0.3 to V_{IN}	V
V_{SW}	SW Pin Voltage	21	V
V_{BS}	BS Pin Voltage	-0.3 to $V_{SW}+6$	V
V_{FB}	FB Pin Voltage	-0.3 to 6	V
V_{COMP}	COMP Pin Voltage	-0.3 to 6	V
V_{SS}	SS Pin Voltage	-0.3 to 6	V
θ_{JA}	Thermal Resistance	105	°C/W
T_J	Operating Junction Temperature	+150	°C
T_{STG}	Storage Temperature	-65 to +150	°C
T_{LEAD}	Lead Temperature (Soldering, 10sec)	+260	°C
—	ESD (Machine Model)	200	V
—	ESD (Human Body Model)	2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{IN}	Input Voltage	4.75	18	V
$I_{OUT} (Max)$	Maximum Output Current	3	—	A
T_A	Operating Ambient Temperature	-40	+85	°C

Electrical Characteristics ($V_{IN}=V_{EN}=12V$, $V_{OUT}=3.3V$, $T_A=+25^\circ C$, unless otherwise specified. And those in boldface type apply over the full operating temperature range ($T_A=-40^\circ C$ to $+85^\circ C$).)

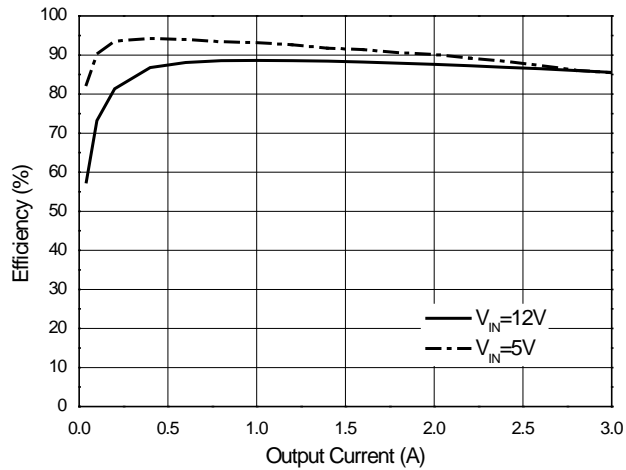
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{IN}	Input Voltage	—	4.75	—	18	V
I_Q	Quiescent Current	$V_{EN}=2V$, $V_{FB}=1.35V$	—	1.0	1.5	mA
I_{SHDN}	Shutdown Supply Current	$V_{EN}=0V$	—	1	10	μA
V_{FB}	Feedback Voltage	—	1.185	1.222	1.258	V
V_{FBOV}	Feedback Over Voltage Threshold	—	—	1.48	—	V
V_{FB_SCP}	Feedback SCP Voltage Threshold	—	—	0.6	—	V
I_{FB}	Feedback Bias Current	$V_{FB}=1V$	-0.1	—	0.1	μA
$R_{DS(ON)H}$	Upper Switch On-resistance	$I_{SW}=0.5A$	—	0.12	—	Ω
$R_{DS(ON)L}$	Lower Switch On-resistance	$I_{SW}=0.05A$	—	10	—	Ω
I_{LEAKH}	High-side Switch Leakage Current	$V_{IN}=18V$, $V_{EN}=0V$, $V_{SW}=0V$	—	0.1	10	μA
I_{LIMH}	High-side Switch Current Limit	—	4	5.2	—	A
I_{LIML}	Low-side Switch Current Limit	From Drain to Source	—	0.15	—	A
V_{ENH}	EN Pin Threshold	—	1.5	—	—	V
V_{ENL}		—	—	—	0.5	V
I_{EN_PH}	EN Pull-up Current	$V_{EN}=0V$	—	1.0	—	μA
V_{UVLO}	Input UVLO Threshold	V_{IN} Rising	3.55	3.85	4.15	V
V_{HYS}	Input UVLO Hysteresis	—	—	0.3	—	V
f_{OSC1}	Oscillator Frequency	—	—	380	—	kHz
f_{OSC2}	Short Circuit Oscillator Frequency	—	—	80	—	kHz
D_{MAX}	Maximum Duty Cycle	$V_{FB}=1.0V$	—	90	—	%
D_{MIN}	Minimum Duty Cycle	$V_{FB}=1.35V$	—	—	0	%
A_{EA}	Error Amplifier Voltage Gain (Note 3)	—	—	400	—	V/V
G_{EA}	Error Amplifier Transconductance	—	700	950	1200	$\mu A/V$
G_{CS}	COMP to Current Sense Transconductance	—	—	5.4	—	A/V
T_{OTSD}	Thermal Shutdown (Note 3)	—	—	+160	—	$^\circ C$
T_{HYS}	Thermal Shutdown Hysteresis (Note 3)	—	—	+30	—	$^\circ C$
t_{SS}	Soft-start Time (Note 3)	$C4=0.1\mu F$, $I_{OUT}=500mA$	—	10	—	ms
I_{SS}	Soft-start Current	—	—	6	—	μA

Note 2:
$$R_{DS(ON)} = \frac{V_{SW1} - V_{SW2}}{I_{SW1} - I_{SW2}}$$

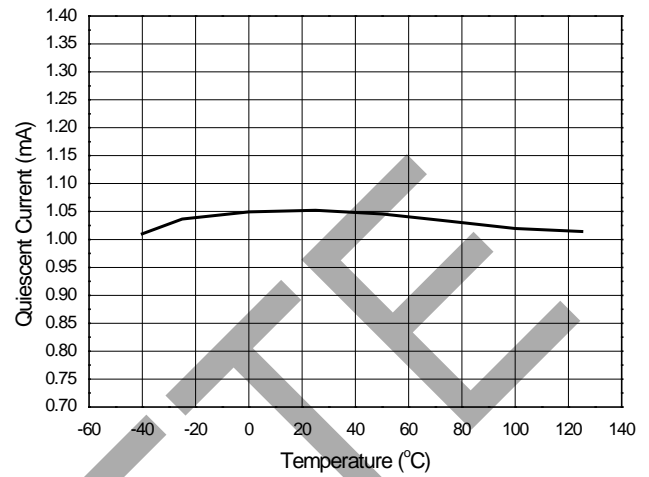
Note 3: Not tested, guaranteed by design.

Performance Characteristics ($T_A=+25^{\circ}\text{C}$, $V_{\text{IN}}=12\text{V}$, $V_{\text{OUT}}=3.3\text{V}$, unless otherwise noted.)

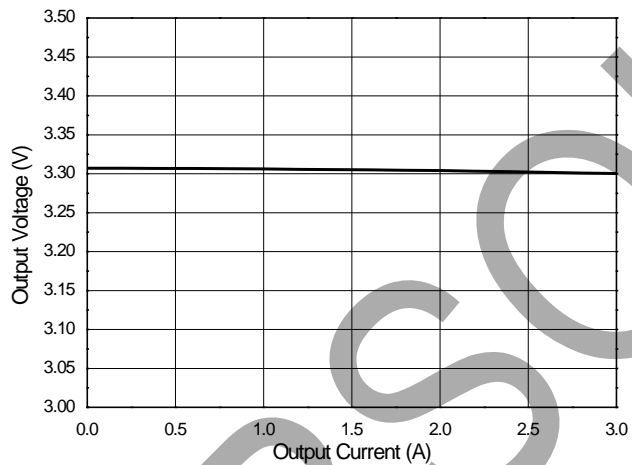
Efficiency vs. Output Current



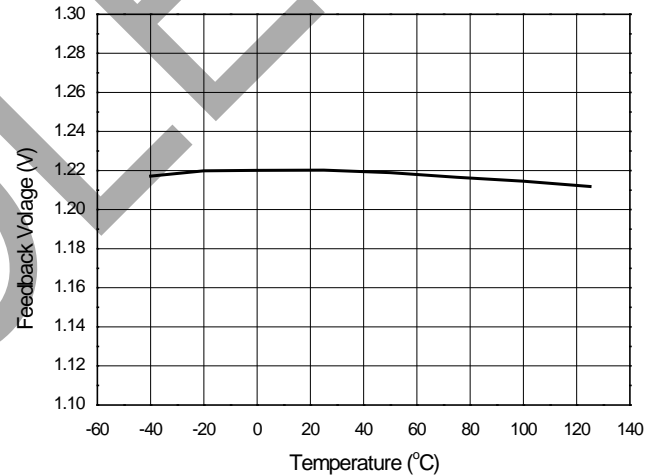
Quiescent Current vs. Temperature



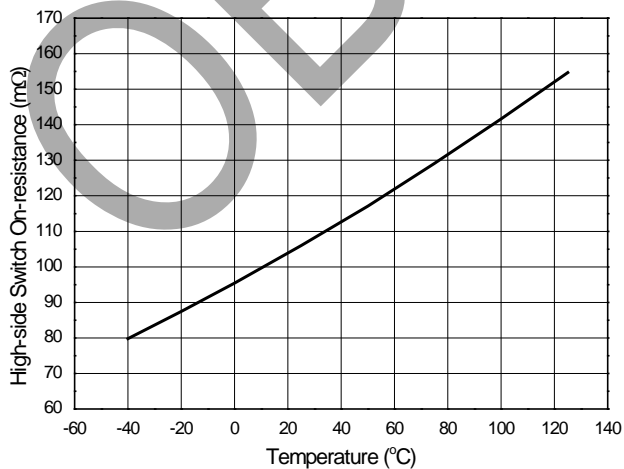
Output Voltage vs. Output Current



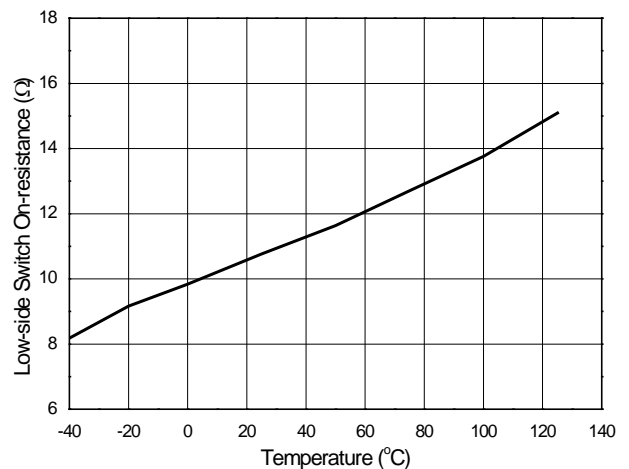
Feedback Voltage vs. Temperature



$R_{\text{DS(on)H}}$ vs. Temperature

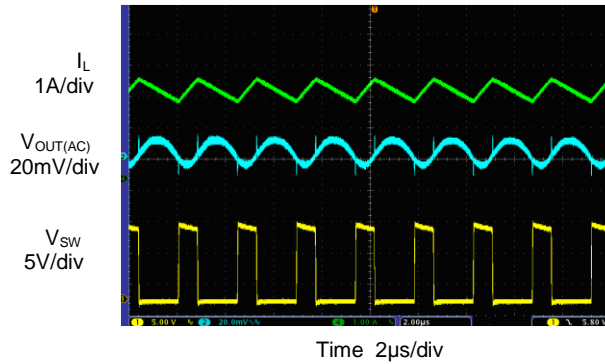


$R_{\text{DS(on)L}}$ vs. Temperature

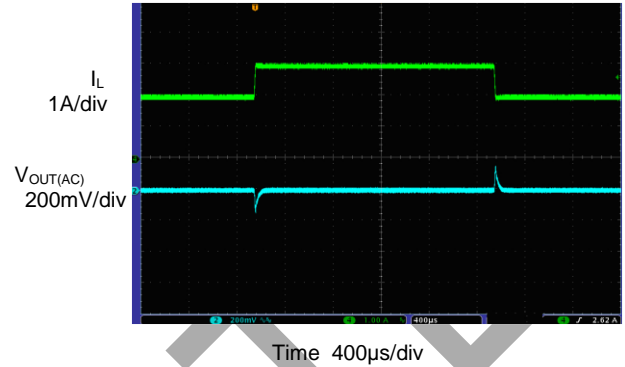


Performance Characteristics (Cont. $T_A=+25^{\circ}\text{C}$, $V_{IN}=12\text{V}$, $V_{OUT}=3.3\text{V}$, unless otherwise noted.)

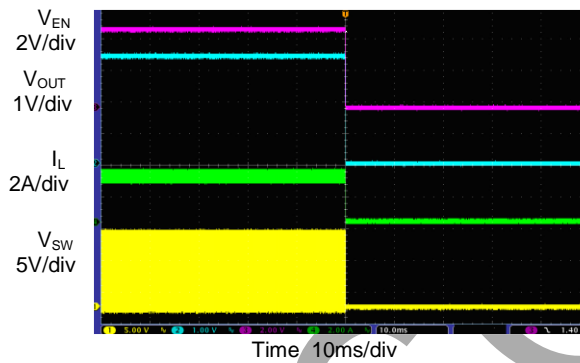
Output Ripple
($V_{IN}=12\text{V}$, $V_{OUT}=3.3\text{V}$, $I_{OUT}=3\text{A}$)



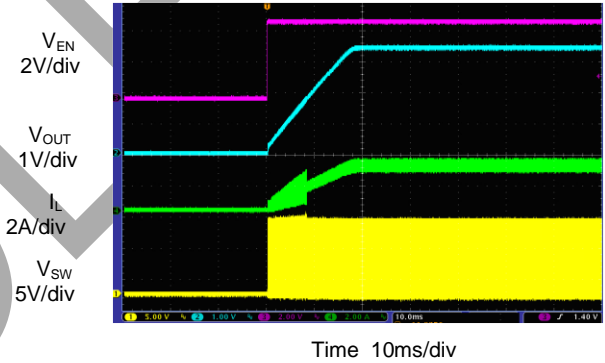
Load Transient
($V_{IN}=12\text{V}$, $V_{OUT}=3.3\text{V}$, $I_{OUT}=2\text{A to } 3\text{A}$)



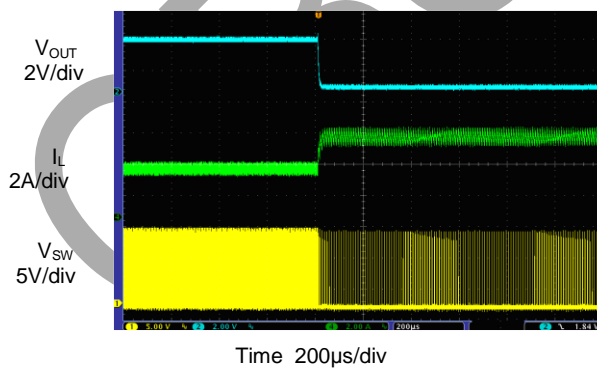
Enable Turn Off
($V_{IN}=12\text{V}$, $V_{OUT}=3.3\text{V}$, $I_{OUT}=3\text{A}$, with Resister load)



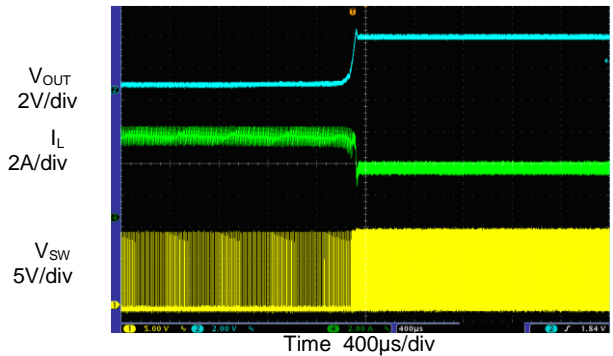
Enable Turn On
($V_{IN}=12\text{V}$, $V_{OUT}=3.3\text{V}$, $I_{OUT}=3\text{A}$, with Resistance Load)



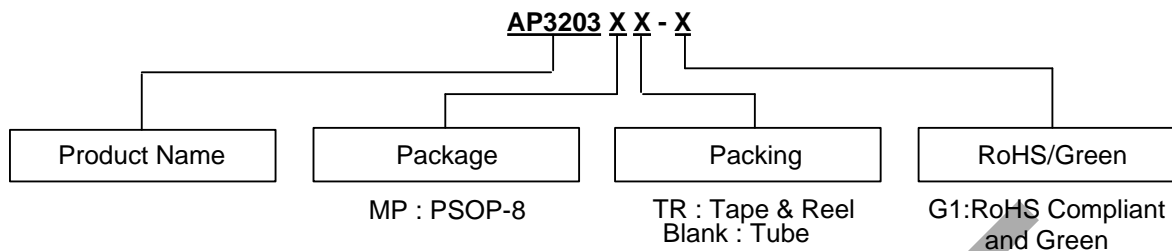
Short Circuit Protection
($V_{IN}=12\text{V}$, $V_{OUT}=3.3\text{V}$, $I_{OUT}=3\text{A}$)



Short Circuit Recovery
($V_{IN}=12\text{V}$, $V_{OUT}=3.3\text{V}$, $I_{OUT}=3\text{A}$)



Ordering Information

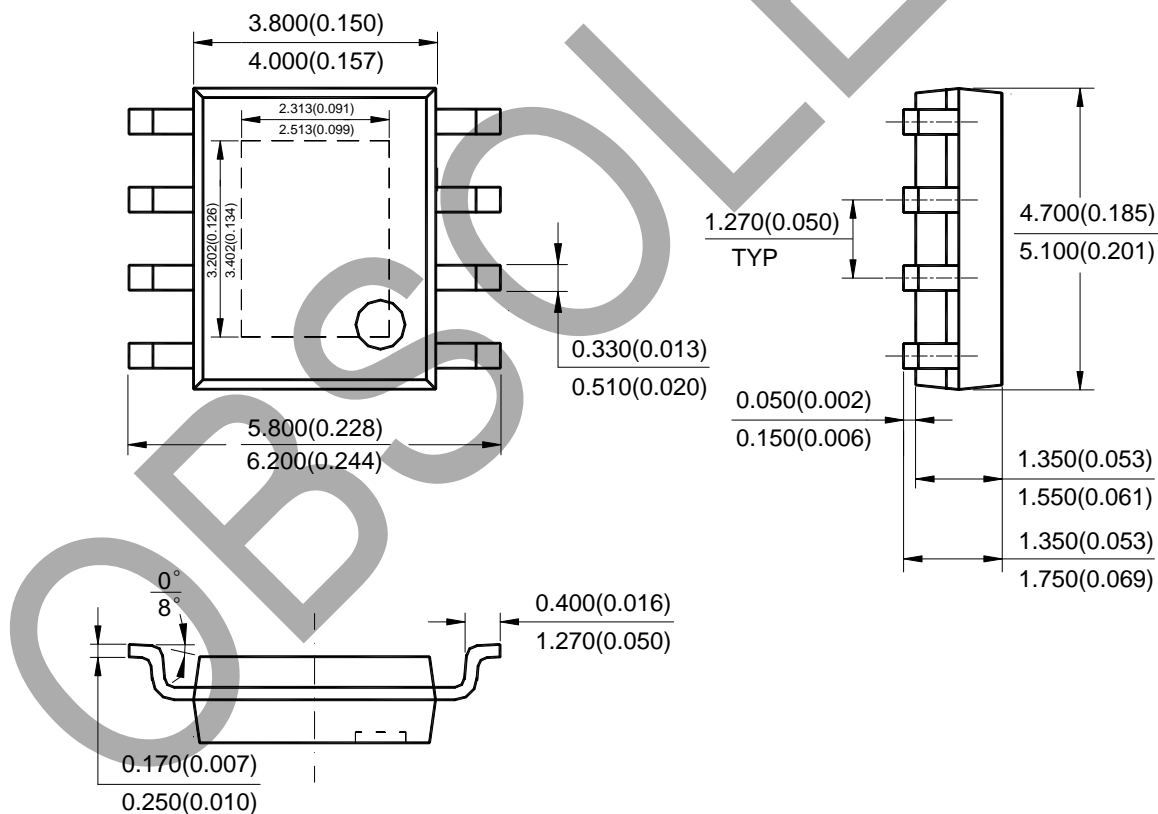


Package	Temperature Range	Part Number	Marking ID	Packing
PSOP-8	-40 to +85°C	AP3203MP-G1	3203MP-G1	Tube
		AP3203MPTR-G1 (Note 4)	3203MP-G1	Tape & Reel

Note 4: EOL = End of life.

Package Outline Dimensions (All dimensions in mm.)

(1) Package Type: PSOP-8



Note: Eject hole, oriented hole and mold mark is optional.

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