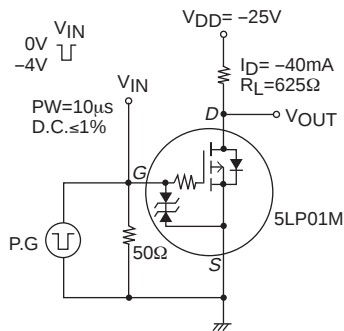


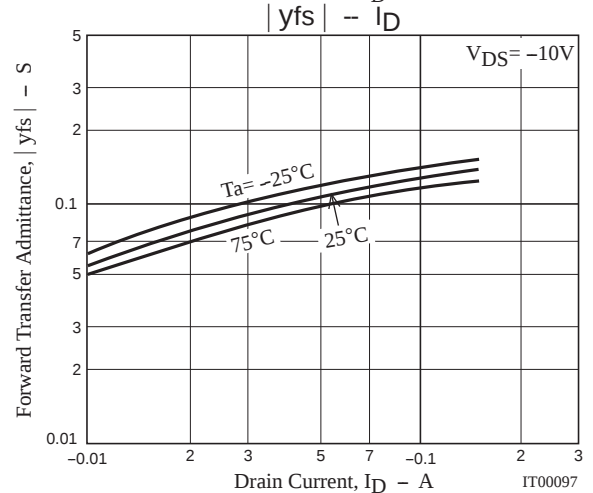
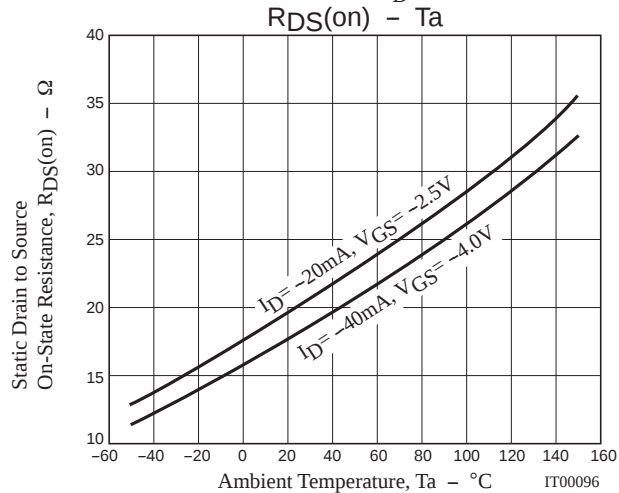
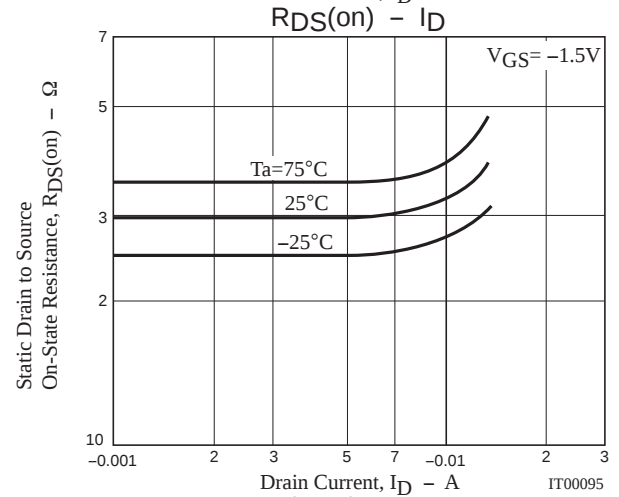
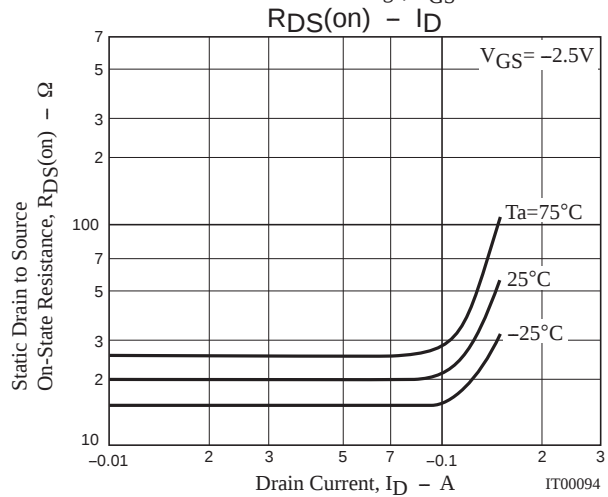
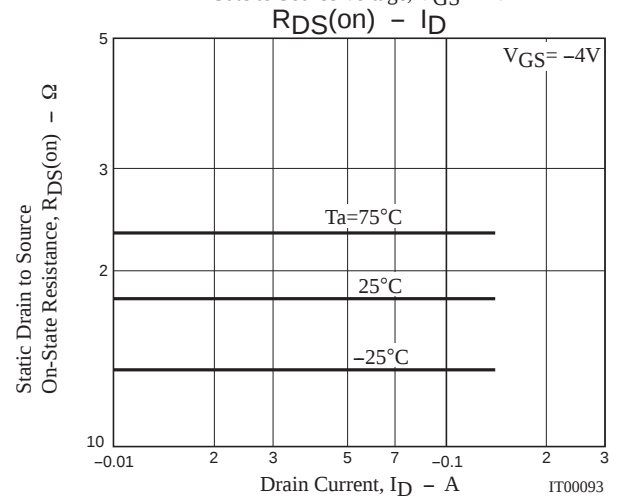
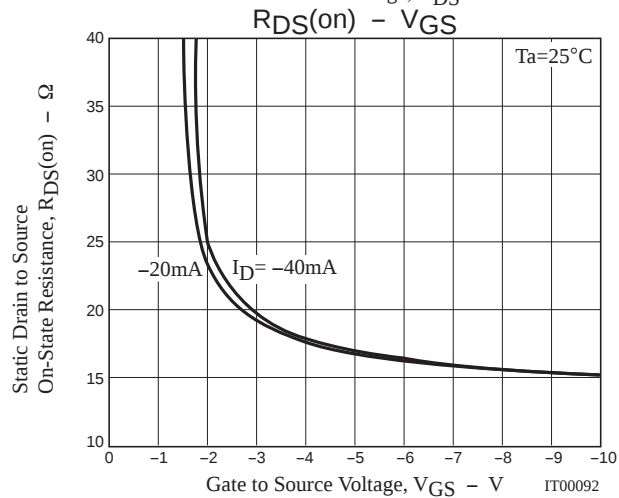
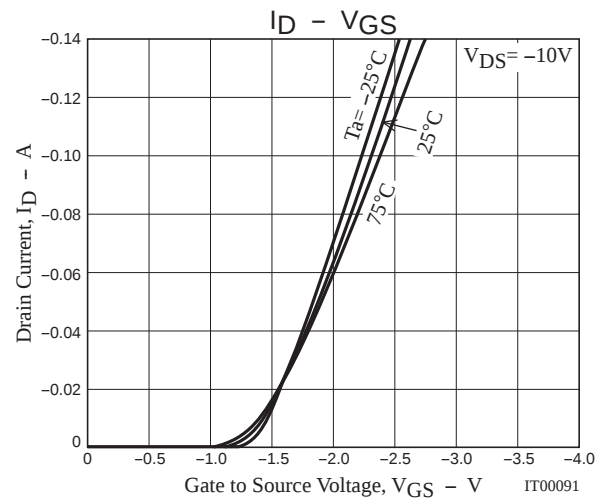
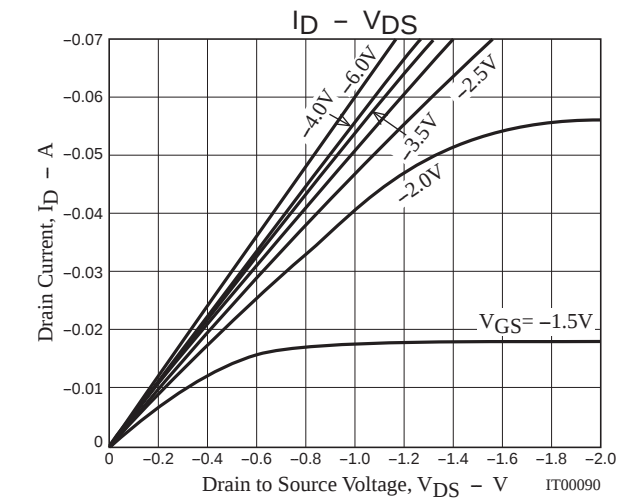
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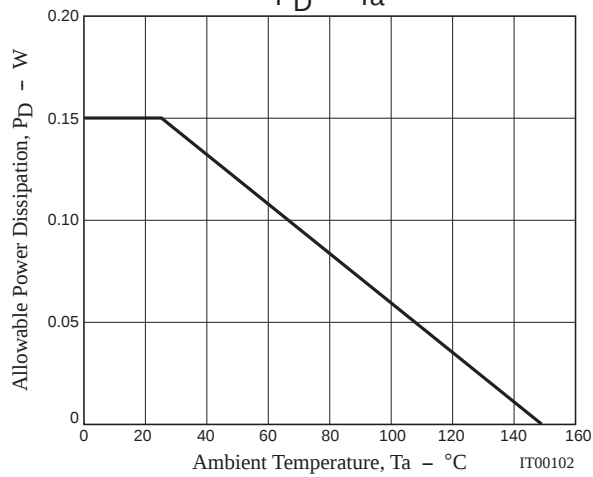
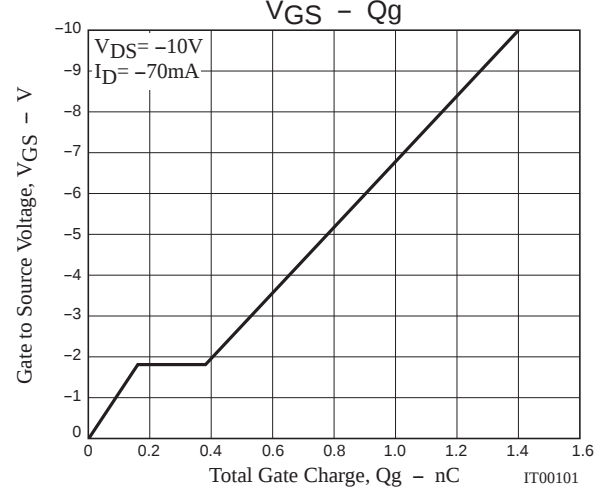
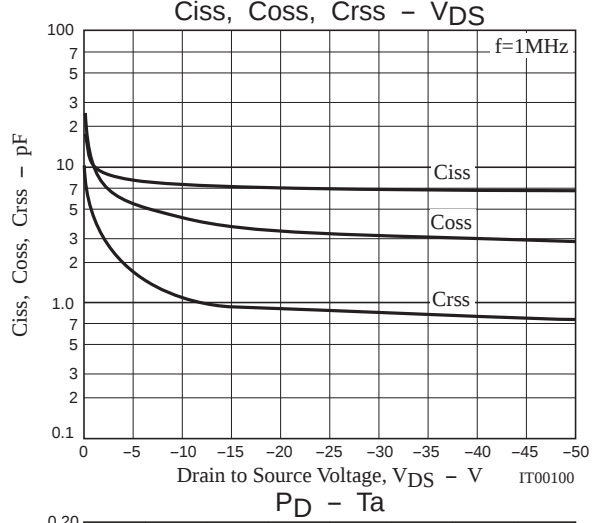
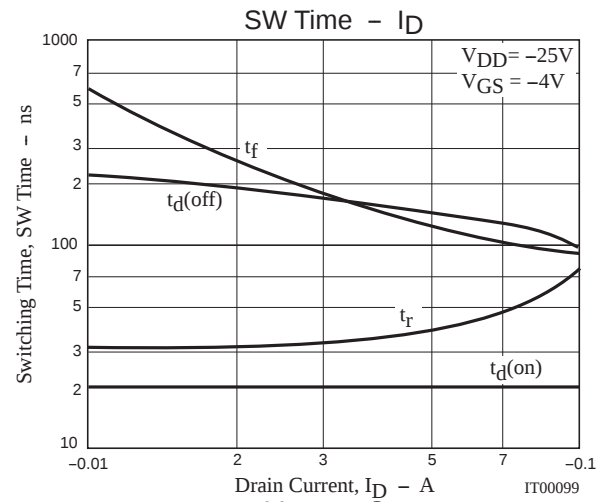
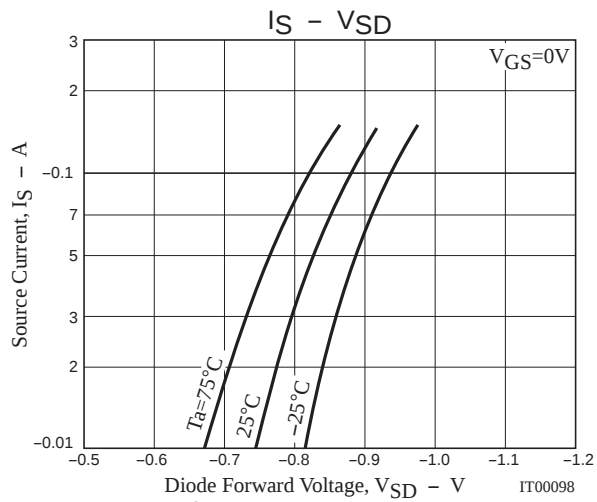
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1\text{mA}$, $V_{GS} = 0\text{V}$	-50			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -50\text{V}$, $V_{GS} = 0\text{V}$			-1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 8\text{V}$, $V_{DS} = 0\text{V}$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{V}$, $I_D = -100\mu\text{A}$	-0.4		-1.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10\text{V}$, $I_D = -40\text{mA}$	70	100		mS
Static Drain to Source On-State Resistance	$R_{DS(on)1}$	$I_D = -40\text{mA}$, $V_{GS} = -4\text{V}$		18	23	Ω
	$R_{DS(on)2}$	$I_D = -20\text{mA}$, $V_{GS} = -2.5\text{V}$		20	28	Ω
	$R_{DS(on)3}$	$I_D = -5\text{mA}$, $V_{GS} = -1.5\text{V}$		30	60	Ω
Input Capacitance	C_{iss}	$V_{DS} = -10\text{V}$, $f = 1\text{MHz}$		7.4		pF
Output Capacitance	C_{oss}			4.2		pF
Reverse Transfer Capacitance	C_{rss}			1.3		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		20		ns
Rise Time	t_r			35		ns
Turn-OFF Delay Time	$t_d(off)$			160		ns
Fall Time	t_f			150		ns
Total Gate Charge	Q_g	$V_{DS} = -10\text{V}$, $V_{GS} = -10\text{V}$, $I_D = -70\text{mA}$		1.40		nC
Gate to Source Charge	Q_{gs}			0.16		nC
Gate to Drain "Miller" Charge	Q_{gd}			0.23		nC
Diode Forward Voltage	V_{SD}	$I_S = -70\text{mA}$, $V_{GS} = 0\text{V}$		-0.85	-1.2	V

Switching Time Test Circuit

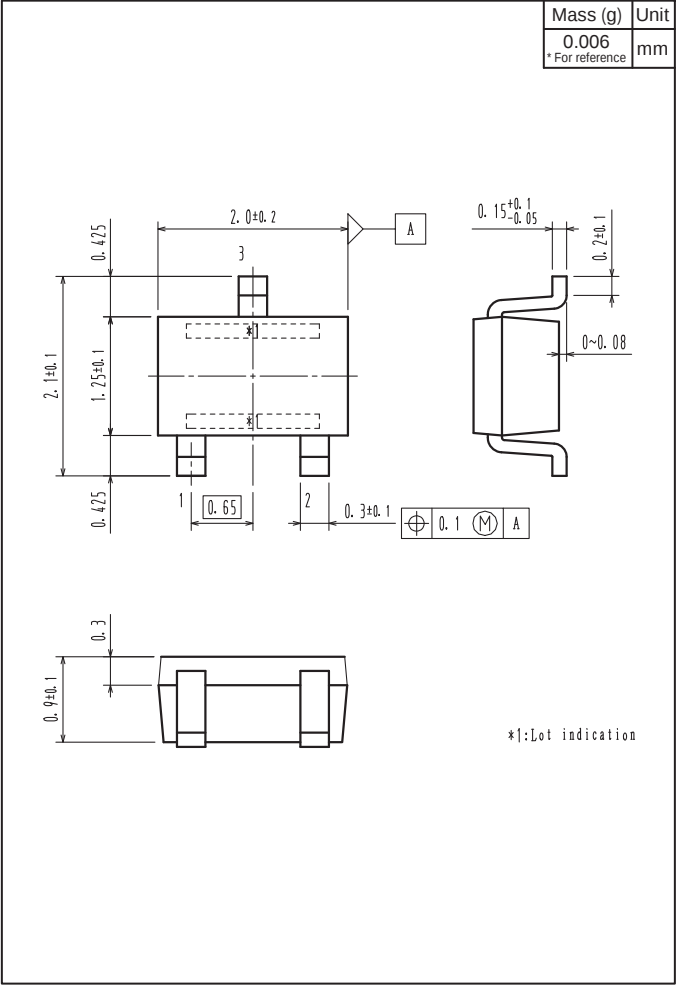




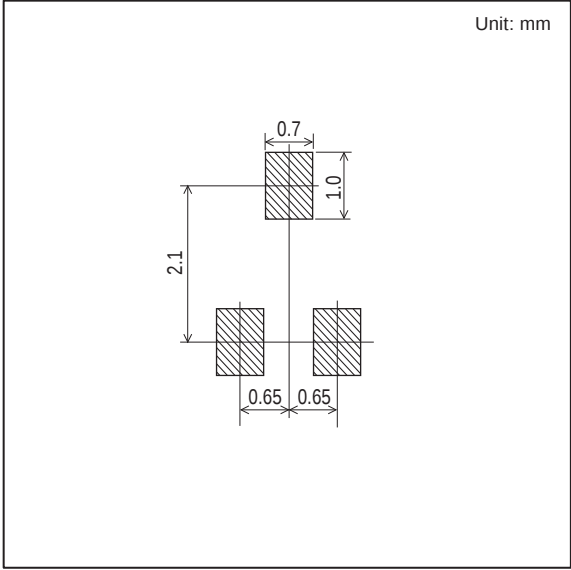


Outline Drawing

5LP01M-TL-E, 5LP01M-TL-H



Land Pattern Example



Note on usage : Since the 5LP01M is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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