Characteristics TN2015H-6FP

1 Characteristics

Table 2. Absolute ratings

Symbol	Parameter			Value	Unit	
I _{T(RMS)}	On-state rms current (180° conduction angle) $T_c = 80$ °C			20	Α	
		T _c = 80 °C	12.7			
I _{T(AV)}	Average on-state current (180° conducti	T _c = 99 °C	10	Α		
		T _c = 112 °C	8			
ı	Non repetitive surge peak on-state current (T _j initial = 25 °C)		t = 8.3 ms	197	А	
I _{TSM}			t = 10 ms	180		
l ² t	I ² t value for fusing (T _j initial = 25 °C)	$t_p = 10 \text{ ms}$	162	A²s		
dI/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100$ ns, $T_i = 25$ °C			100	A/µs	
V _{DRM} , V _{RRM}	Repetitive peak off-state voltage			600	٧	
I _{GM}	Peak gate current $t_p = 20 \mu s$		T _j = 150 °C	4	Α	
P _{G(AV)}	Average gate power dissipation	1	W			
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 150	°C	
TL	Maximum lead temperature for soldering during 10 s			260	°C	
V _{ins}	Insulation rms voltage, 1 minute	2000	V			

Table 3. Electrical characteristics ($T_i = 25$ °C, unless otherwise specified)

Symbol	Test conditions			Value	Unit
1.	$V_D = 12 \text{ V, R}_1 = 33 \Omega$		Тур.	6	mΛ
I _{GT}	V _D = 12 V, K _L = 33 22			15	mA
V _{GT}	$V_D = 12 \text{ V}, R_L = 33 \Omega$			1.3	V
$V_{\sf GD}$	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$ $T_j = 150 \text{ °C}$		Min.	0.2	V
lΗ	I _T = 500 mA, gate open			50	mA
Ι _L	I _G = 1.2 x I _{GT}		Max.	60	mA
dV/dt	$V_D = 402 \text{ V, gate open}$ $T_j = 150 \text{ °C}$		Min.	750	V/µs
t _{gt}	$I_T = 40 \text{ A}, V_D = 600 \text{ V}, I_G = 100 \text{ mA},$ $(dI_G/dt) \text{max} = 0.2 \text{ A/}\mu\text{s}$		Тур	1.9	μs
t _q	$V_D = 402 \text{ V}, V_R = 25 \text{ V}, I_T = 20 \text{ A}, \\ (dI_G/dt) max = 30 \text{ A/}\mu \text{s}, dV_D/dt = 50 \text{ V/}\mu \text{s} $ $T_j = 150 \text{ °C}$		Тур	70	μs

TN2015H-6FP Characteristics

Table 4. Static characteristics

Symbol	Test conditions			Value	Unit
V _{TM}	$I_{TM} = 40 \text{ A}, t_p = 380 \mu\text{s}$	T _j = 25 °C	Max.	1.6	V
V _{t0}	Threshold voltage	T _j = 150 °C	Max.	0.82	V
R _d	Dynamic resistance	T _j = 150 °C	Max.	17.5	mΩ
I _{DRM,} I _{RRM}	$V_D = V_{DRM}, V_R = V_{RRM}$	T _j = 25 °C	- Max.	5	μΑ
		T _j = 150 °C		2	mA

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	4.0	°C/W
R _{th(j-a)}	Junction to ambient (DC)	60	°C/W

Figure 1. Maximum power dissipation versus average on-state current

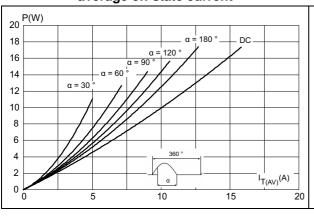


Figure 2. Average and DC on-state current versus case temperature

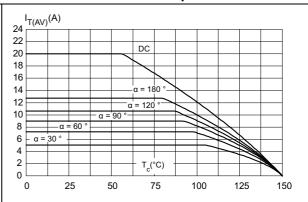
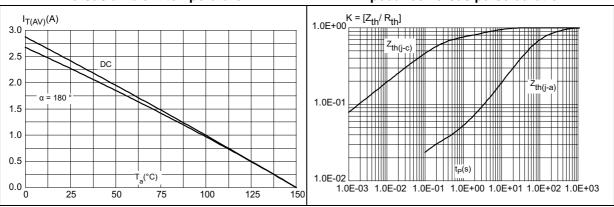


Figure 3. Average and DC on-state current versus ambient temperature

Figure 4. Relative variation of thermal impedance versus pulse duration



Characteristics TN2015H-6FP

Figure 5. Relative variation of gate triggering current and gate voltage versus junction temperature (typical values)

Figure 6. Relative variation of holding current and latching current versus junction temperature (typical values)

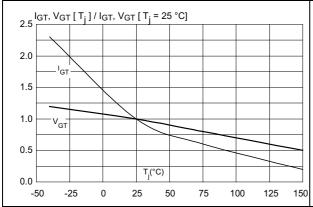
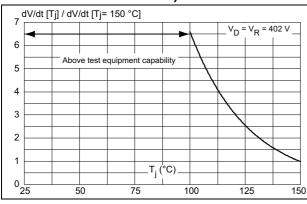


Figure 7. Relative variation of static dV/dt immunity versus junction temperature (typical values)

Figure 8. Surge peak on-state current versus number of cycles



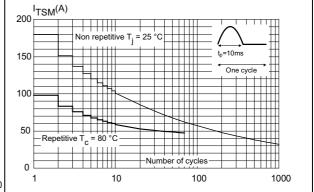
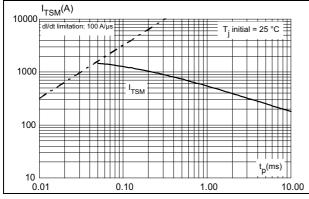
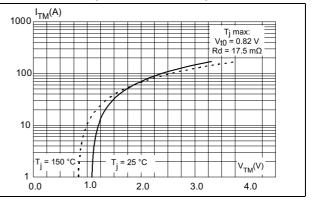


Figure 9. Non-repetitive surge peak on-state current for a sinusoidal pulse (tp < 10 ms)

Figure 10. On-state characteristics (maximum values)

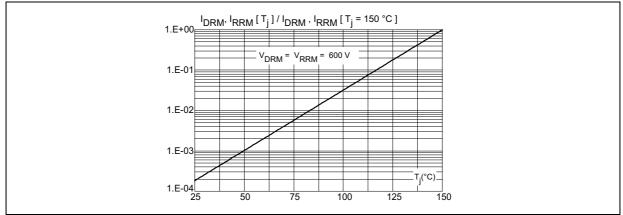




4/9 DocID027549 Rev 1

TN2015H-6FP Characteristics

Figure 11. Relative variation of leakage current versus junction temperature (tp < 10 ms)



Package information TN2015H-6FP

2 Package information

- Epoxy meets UL94, V0
- Lead-free package
- Halogen free molding compound
- Recommended torque: 0.4 to 0.6 N·m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Н Dia L6 L2 L7 L3 L5 D F1 L4 F2 F G1 G

Figure 12. TO-220FPAB dimension definitions

6/9 DocID027549 Rev 1

TN2015H-6FP Package information

Table 6. TO-220FPAB dimensions

	Dimensions					
Ref.	Millimeters		Inches			
	Min.	Max.	Min.	Max.		
А	4.4	4.6	0.173	0.181		
В	2.5	2.7	0.098	0.106		
D	2.5	2.75	0.098	0.108		
E	0.45	0.70	0.018	0.027		
F	0.75	1	0.030	0.039		
F1	1.15	1.70	0.045	0.067		
F2	1.15	1.70	0.045	0.067		
G	4.95	5.20	0.195	0.205		
G1	2.4	2.7	0.094	0.106		
Н	10	10.4	0.393	0.409		
L2 16 Typ.		Гур.	0.63	Тур.		
L3	28.6	30.6	1.126	1.205		
L4	9.8	10.6	0.386	0.417		
L5	2.9	3.6	0.114	0.142		
L6	L6 15.9		0.626	0.646		
L7	9.00	9.30	0.354	0.366		
Dia. 3.00		3.20	0.118	0.126		



Ordering information TN2015H-6FP

3 Ordering information

Figure 13. Ordering information scheme

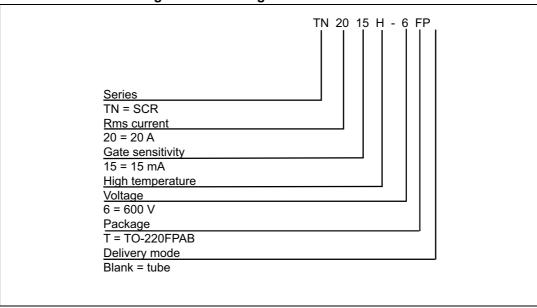


Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
TN2015H-6FP	TN2015H6	TO-220FPAB	2.0 g	50	Tube

4 Revision history

Table 8. Document revision history

, and of 200 and 101 a			
Date Revision		Changes	
25-Feb-2015	1	Initial release.	

IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2015 STMicroelectronics - All rights reserved



DocID027549 Rev 1

9/9