FUNCTIONAL BLOCKS

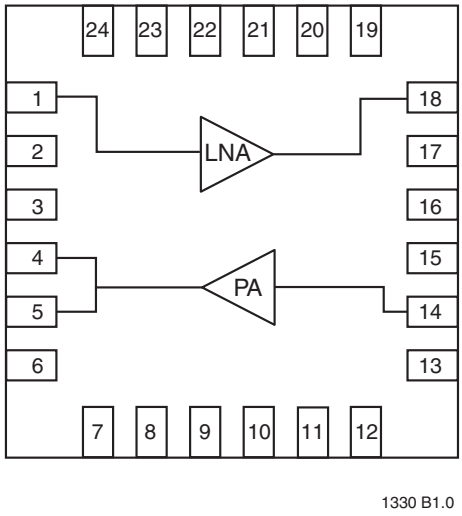


FIGURE 1: Functional Block Diagram

PIN ASSIGNMENTS

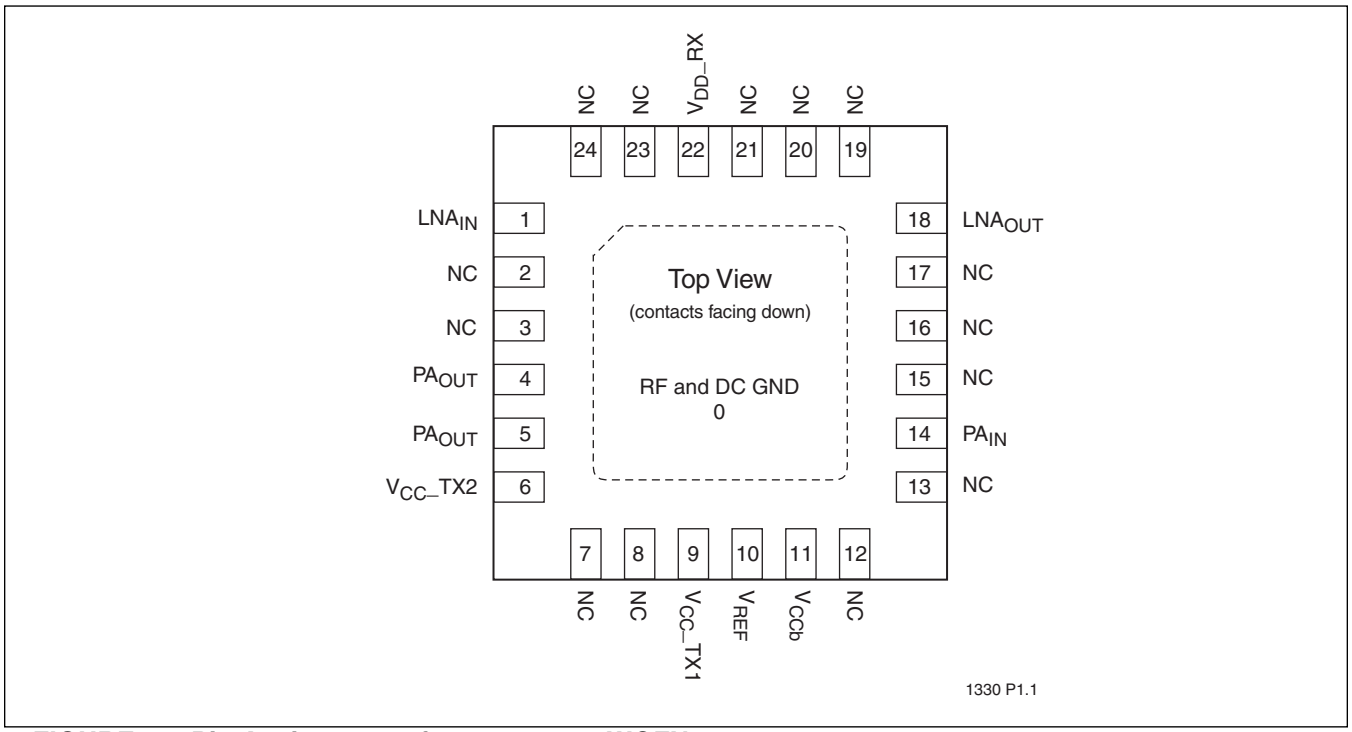


FIGURE 2: Pin Assignments for 24-contact WQFN

PIN DESCRIPTIONS

TABLE 1: Pin Description

Symbol	Pin No.	Pin Name	Type[1]	Function
LNA_{IN}	1		I	LNA RF Input
NC	2	No Connection		Unconnected pin
NC	3	No Connection		Unconnected pin
PA_{OUT}	4		O	PA RF output
PA_{OUT}	5		O	PA RF output
V_{CC}_TX2	6	Power Supply	PWR	PA power supply, 2nd stage
NC	7	No Connection		Unconnected pin
NC	8	No Connection		Unconnected pin
V_{CC}_TX1	9	Power Supply	PWR	PA power supply,1st stage
V_{REF}	10		PWR	PA-enable and current control
V_{CCb}	11	Power Supply	PWR	PA power supply, bias circuit
NC	12	No Connection		Unconnected pin
NC	13	No Connection		Unconnected pin
PA_{IN}	14		I	PA RF input
NC	15	No Connection		Unconnected pin
NC	16	No Connection		Unconnected pin
NC	17	No Connection		Unconnected pin
LNA_{OUT}	18		O	LNA RF Output
NC	19	No Connection		Unconnected pin
NC	20	No Connection		Unconnected pin
NC	21	No Connection		Unconnected pin
V_{DD}_RX	22	Power Supply	PWR	LNA power supply
NC	23	No Connection		Unconnected pin
NC	24	No Connection		Unconnected pin

T1.0 1330

1. I=Input, O=Output

ELECTRICAL SPECIFICATIONS

The AC and DC specifications for the power amplifier interface signals. Refer to Table 2 for the DC voltage and current specifications. Refer to Figures 3 through 14 for the RF performance.

Absolute Maximum Stress Ratings (Applied conditions greater than those listed under "Absolute Maximum Stress Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these conditions or conditions greater than those defined in the operational sections of this data sheet is not implied. Exposure to absolute maximum stress rating conditions may affect device reliability.)

Input power to pins 1 (LNA) . . . . . 0 dBm
Input power to pins 14 (PA) . . . . . -5 dBm
Average output power pins 4 and 5 (P_{OUT})[1] . . . . . 24 dBm
Average output power pin 18 (P_{OUT})[1] . . . . . 9 dBm
Supply Voltage at pins 6, 9, and 11 (V_{CC}) . . . . . -0.3V to +3.5V
Supply Voltage at pin 22 (V_{DD}) . . . . . -0.3V to +4.6V
Reference voltage to pin 10 (V_{REF}) . . . . . -0.3V to +3.6V
DC supply current to pin 10 (I_{DD}) . . . . . 14 mA
DC supply current to pin 6, 9, and 11 (I_{CC}) . . . . . 300 mA
Operating Temperature (T_A) . . . . . -40ºC to +85ºC
Storage Temperature (T_{STG}) . . . . . -40ºC to +120ºC
Maximum Junction Temperature (T_J) . . . . . +150ºC
Surface Mount Solder Reflow Temperature . . . . . 260°C for 10 seconds

1. Never measure with CW source. Pulsed single-tone source with <50% duty cycle is recommended. Exceeding the maximum rating of average output power could cause permanent damage to the device.

Operating Range

Range	Ambient Temp	V_{CC} / V_{DD}
Commercial	-0 to 80ºC	2.9–3.5V

TABLE 2: DC Electrical Characteristics

Symbol	Parameter	Min.	Typ	Max.	Unit
V_{CC}	Supply Voltage at pins 6, 9, 11, and 22		3.3	4.2	V
I_{CC}	Supply Current at pin 22		10		mA
	for 802.11g, 22 dBm at pins 6, 9, and 11		210		mA
	for 802.11b, 23.5 dBm at pins 6, 9, and 11		260		mA
I_{CQ}	Idle current for 802.11g to meet EVM<4% @ 20 dBm		75		mA
I_{OFF}	Shut down current		2.5		µA
V_{REF}[1]	Reference Voltage at pin10 with R_{REG} = 0Ω resistor		2.7		V
	Reference Voltage at pin 10 with R_{REG} = 120Ω resistor	2.7	2.9	3.1	V
	Reference Voltage at pin 10 with R_{REG} = 220Ω resistor	2.9	3.1	3.3	V

T2.1 1330

1. VREF and VREG are defined in Figure 15. Three combinations of resistor values and applied voltages of VREG are suggested in Table 2.

TABLE 3: AC Electrical Characteristics for RX Chain

Symbol	Parameter	Min.	Typ	Max.	Unit
F_{L-U}	Frequency range	2400		2550	MHz
G	Small signal gain	10	12		dB
NF	Noise Figure		1.45		dB
IIP3	2.4–2.55 GHz	1	3		dBm

T3.1 1330

TABLE 4: AC Electrical Characteristics for TX Chain

Symbol	Parameter	Min.	Typ	Max.	Unit
F_{L-U}	Frequency range	2400		2485	MHz
P_{OUT}	Output power				
	@ PIN = -6 dBm 11b signals	23			dBm
	@ PIN = -9 dBm 11g signals	20			dBm
G	Small signal gain	28	29	33	dB
G_{VAR1}	Gain variation over band (2400~2485 MHz)			±0.5	dB
G_{VAR2}	Gain ripple over channel (20 MHz)		0.2		dB
ACPR	Meet 11b spectrum mask	23			dBm
	Meet 11g OFDM 54 Mbps spectrum mask	22			dBm
Added EVM	@ 20 dBm output with 11g OFDM 54 Mbps signal		4		%
2f, 3f, 4f, 5f	Harmonics at 22 dBm, without external filters			-40	dBc

T4.1 1330

TYPICAL PERFORMANCE CHARACTERISTICS
Test Conditions: V_{DD} = 3.0V, T_A = 25°C, unless otherwise specified

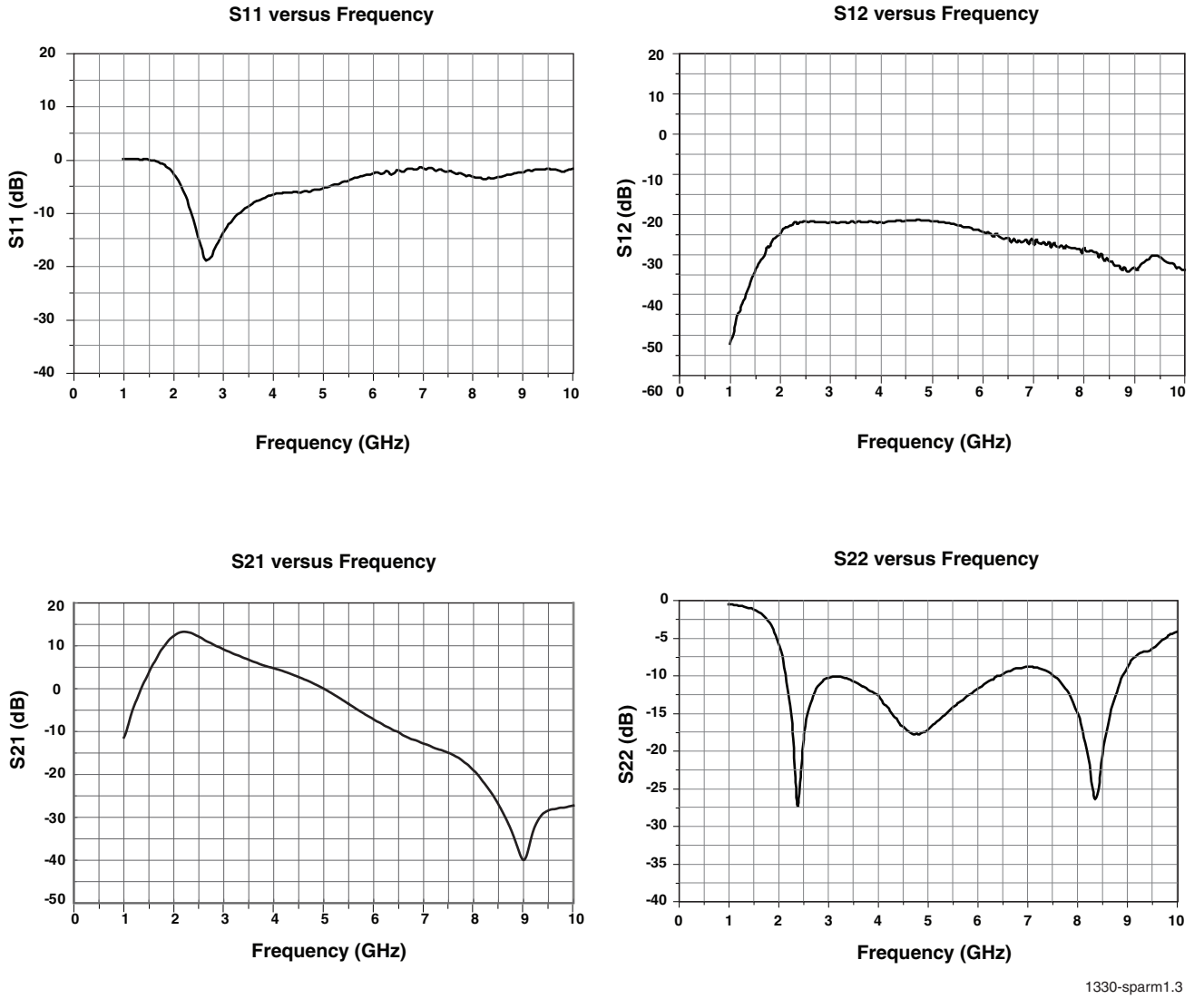


FIGURE 3: S-Parameters, RX Chain

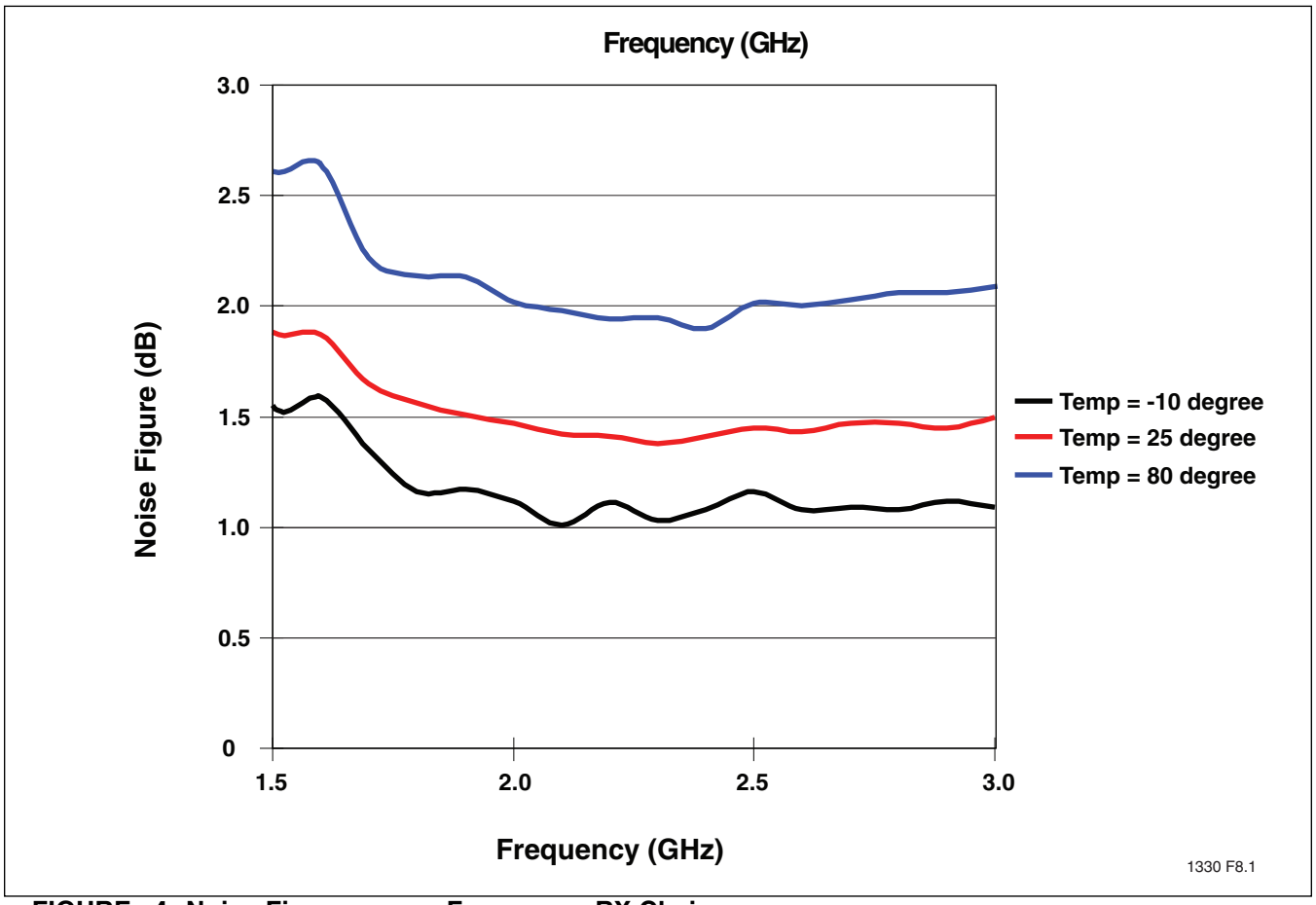


FIGURE 4: Noise Figure versus Frequency, RX Chain

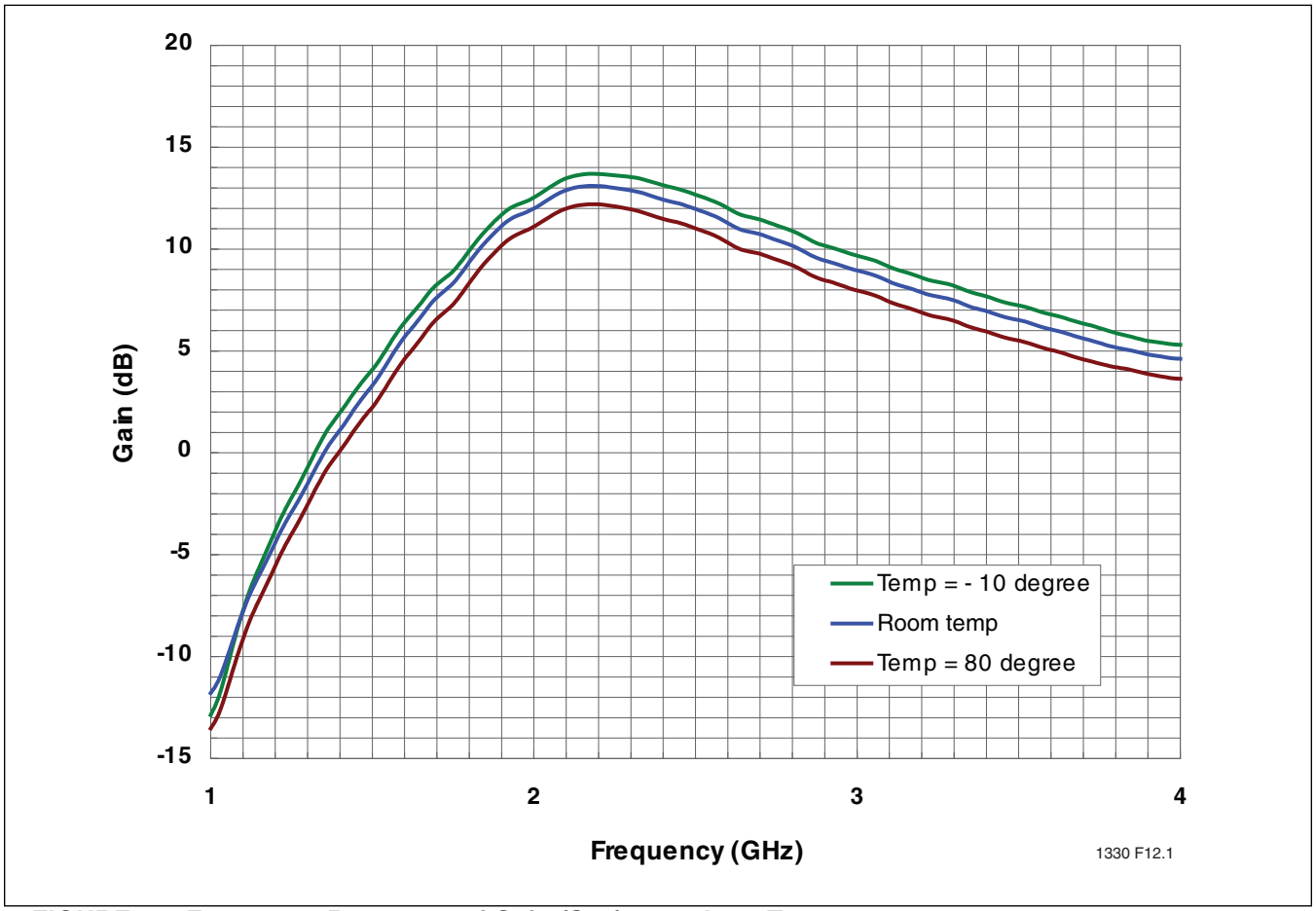


FIGURE 5: Frequency Response of Gain (S21) over three Temperatures

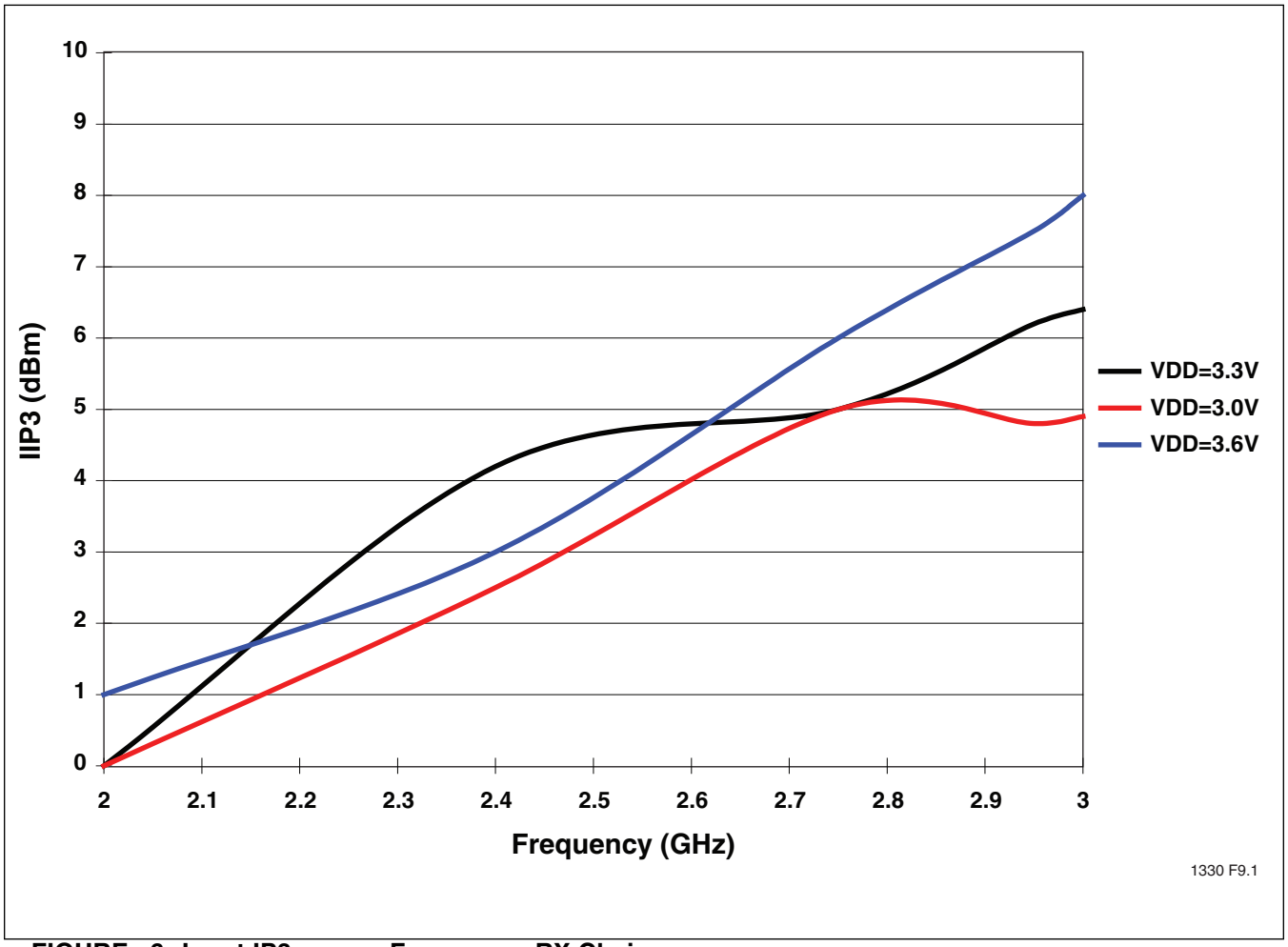


FIGURE 6: Input IP3 versus Frequency, RX Chain

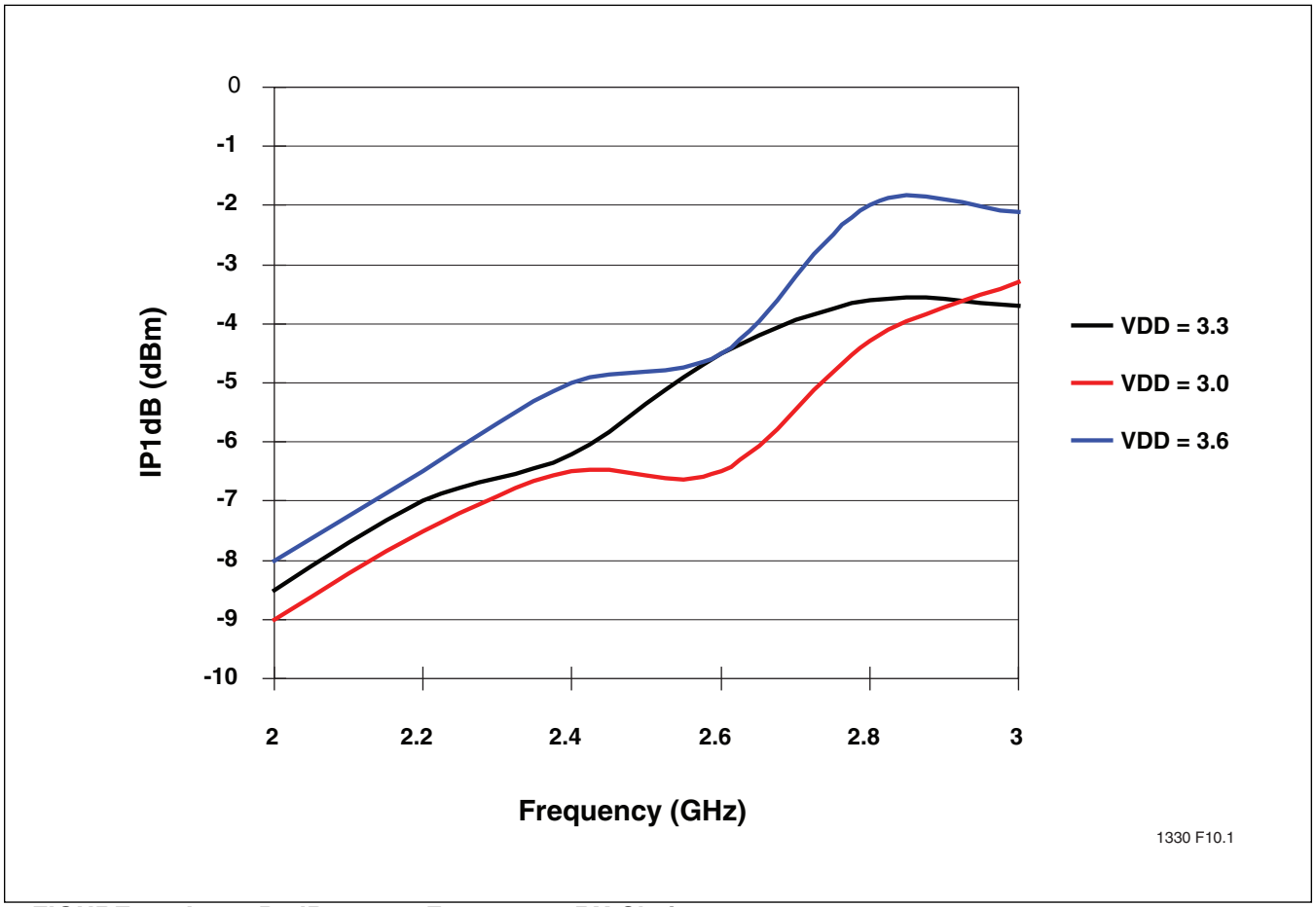


FIGURE 7: Input P1dB versus Frequency, RX Chain

Test Conditions: V_{CC} = 3.3V, T_A = 25°C, unless otherwise specified

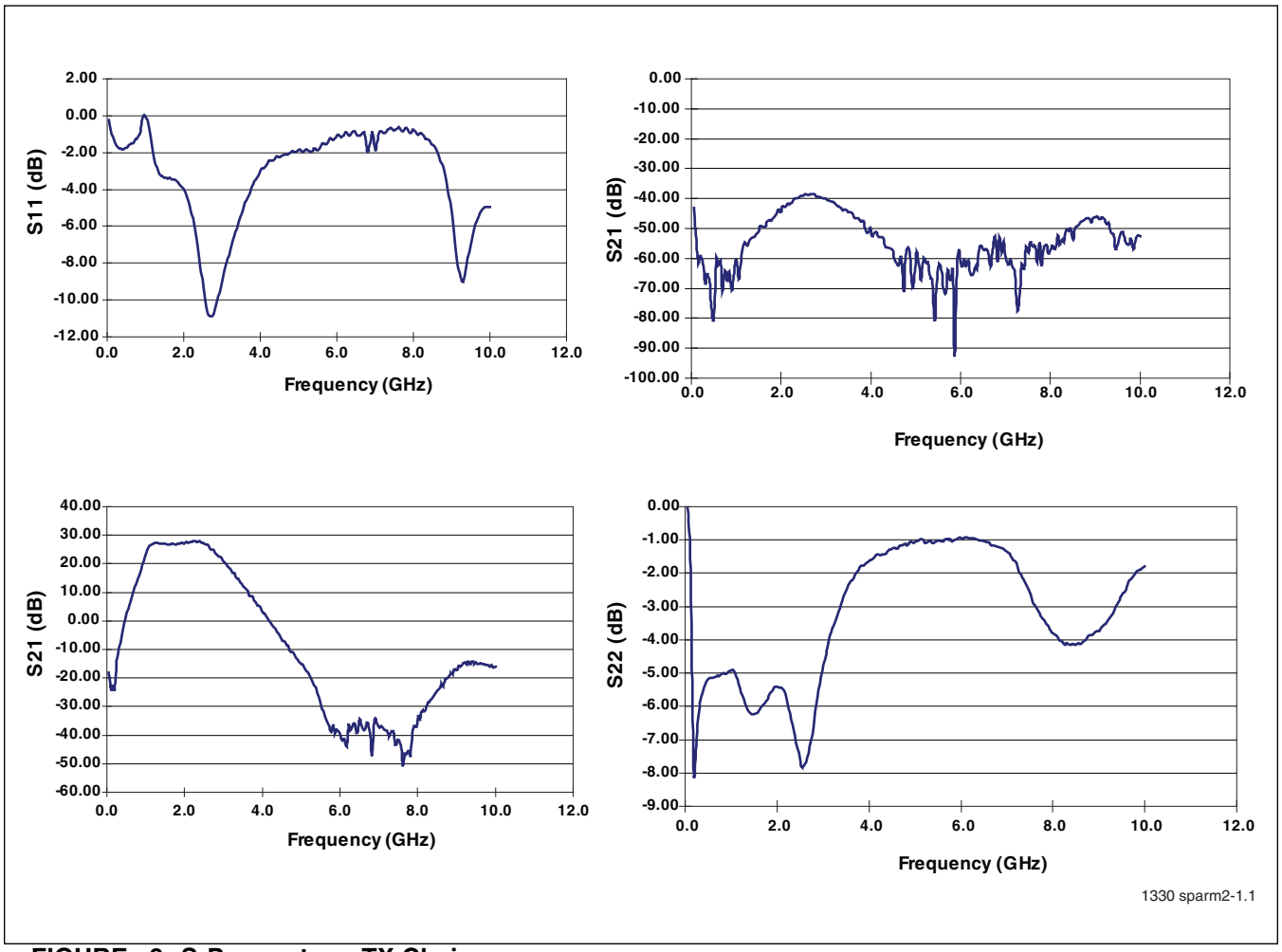


FIGURE 8: S-Parameters, TX Chain

TYPICAL PERFORMANCE CHARACTERISTICS

Test Conditions: f = 2.447 GHz, V_{CC} = 3.3V, V_{REF} = 2.85V at Room Temperature I_{CQ} = 70 mA

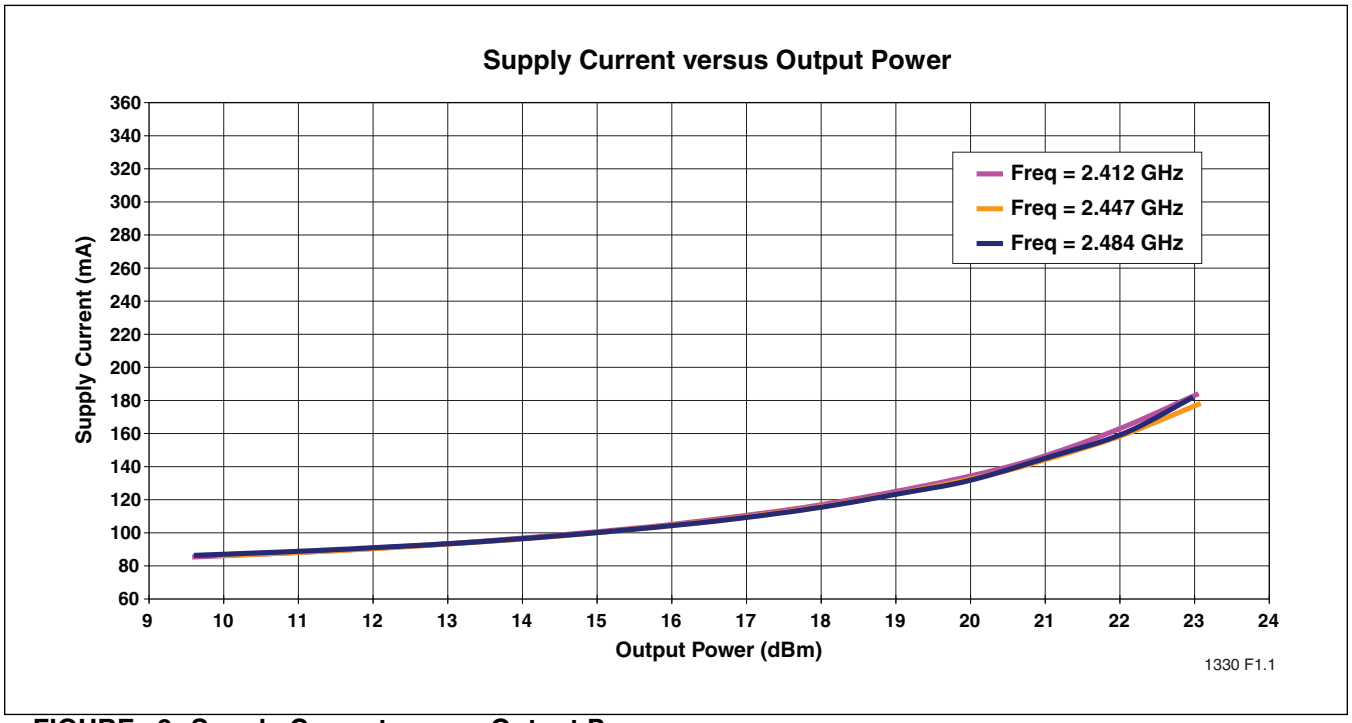


FIGURE 9: Supply Current versus Output Power

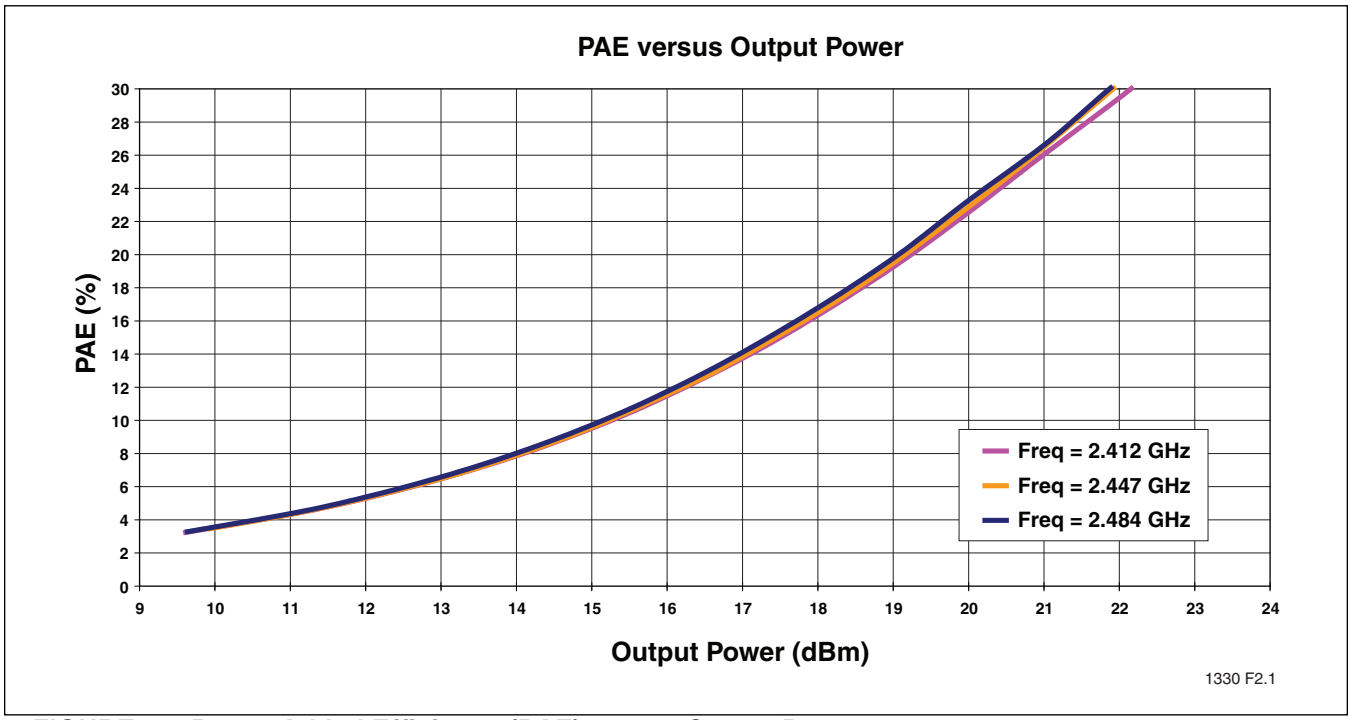


FIGURE 10: Power Added Efficiency (PAE) versus Output Power

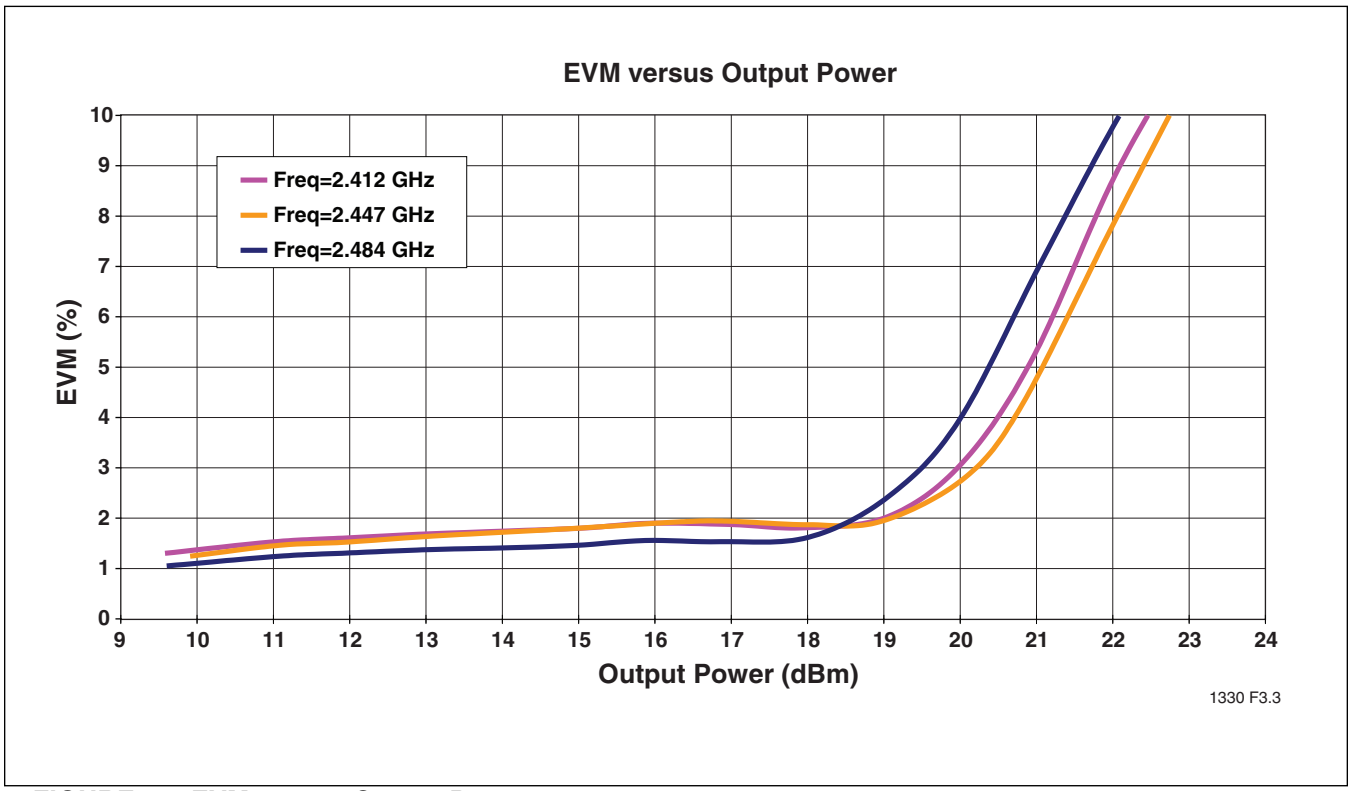


FIGURE 11: EVM versus Output Power

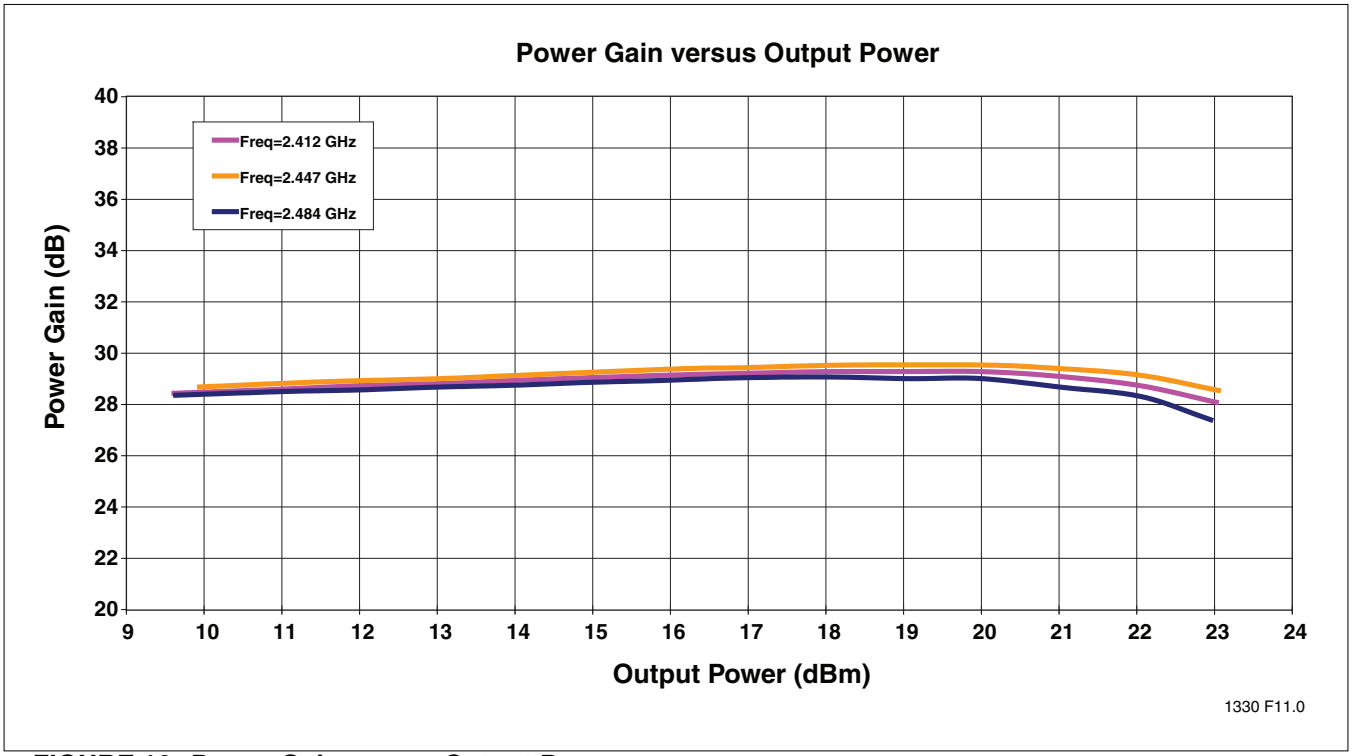


FIGURE 12: Power Gain versus Output Power

TEST CONDITIONS: V_{CC} = 3.3V, T_A = 25°C, 54 MBPS 802.11G OFDM SIGNAL

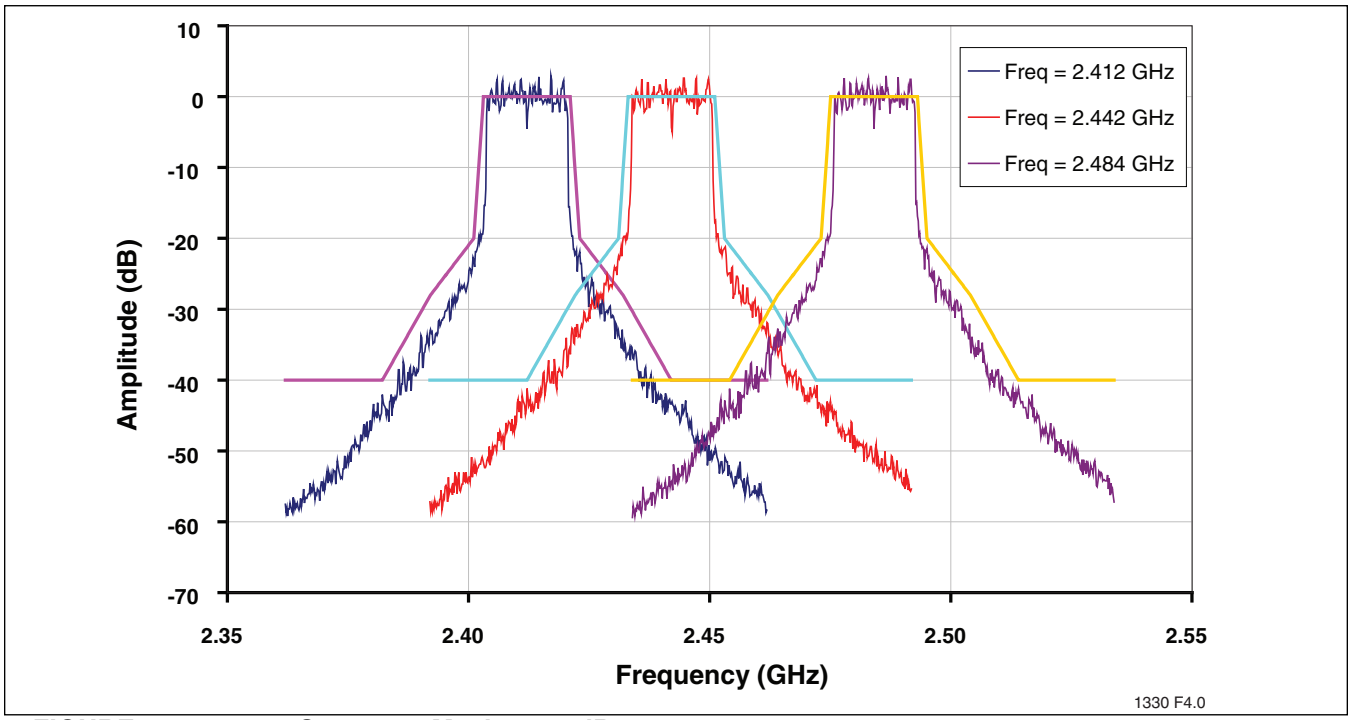


FIGURE 13: 802.11g Spectrum Mask at 23 dBm

Test Conditions: V_{CC} = 3.3V, T_A = 25°C, 1 Mbps 802.11b signal

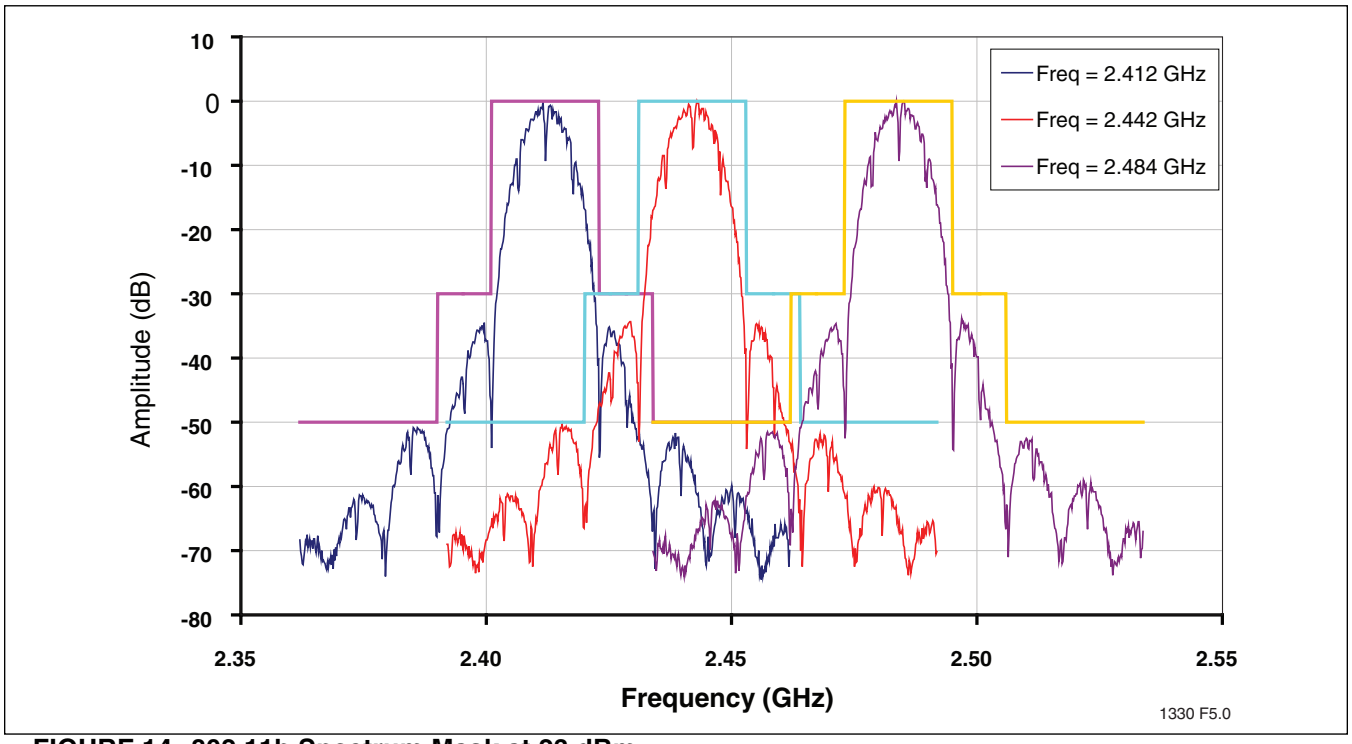


FIGURE 14: 802.11b Spectrum Mask at 23 dBm

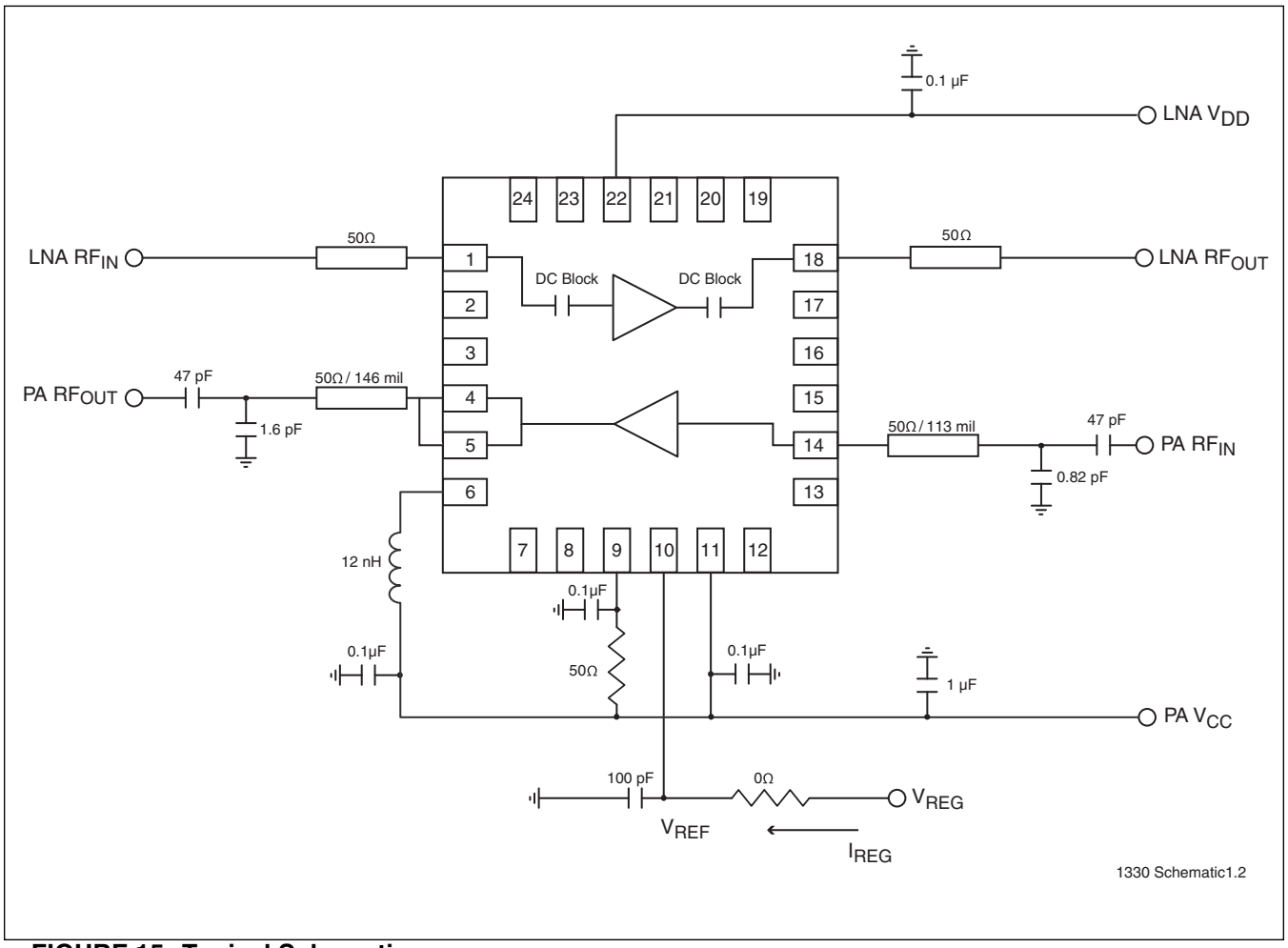
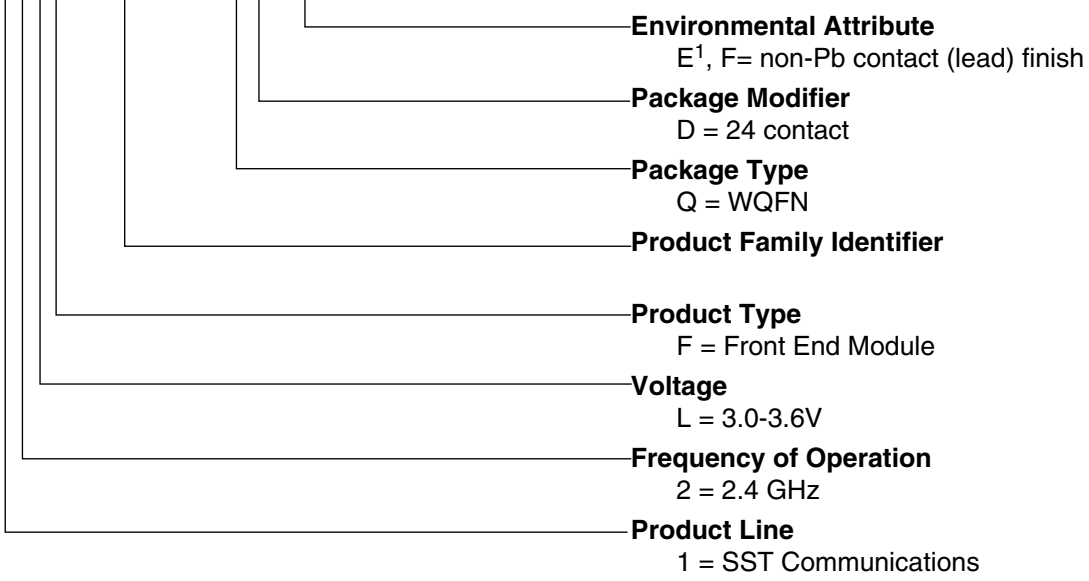


FIGURE 15: Typical Schematic

PRODUCT ORDERING INFORMATION

SST12LF 01 - QD E
SSTXXLF XX - XX X



1. Environmental suffixes “E” and “F” denote non-Pb solder. SST non-Pb solder devices are “RoHS Compliant”.

Valid combinations for SST12LF01

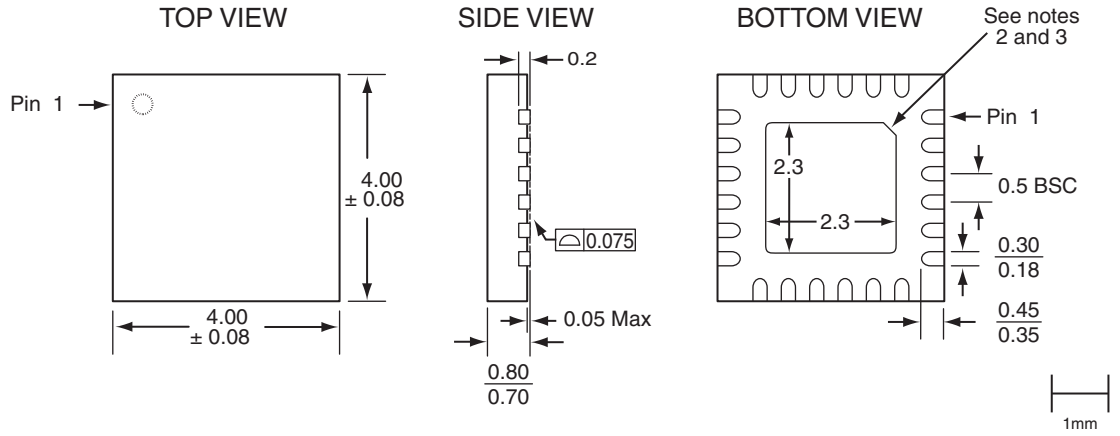
SST12LF01-QDE SST12LF01-QDF

SST12LF01 Evaluation Kits

SST12LF01-QDE-K SST12LF01-QDF-K

Note: Valid combinations are those products in mass production or will be in mass production. Consult your SST sales representative to confirm availability of valid combinations and to determine availability of new combinations.

PACKAGING DIAGRAMS



Note: 1. Complies with JEDEC JEP95 MO-220J, variant WGGD-4 except external paddle dimensions. 24-wqfn-4x4-QD-2.0
2. From the bottom view, the pin 1 indicator ma y be either a 45-degree chamfer or a half-circle notch.
3. The external paddle is electrically connected to the die back-side and possibly to certain V_{SS} leads. This paddle can be soldered to the PC board; it is suggested to connect this paddle to the V_{SS} of the unit. Connection of this paddle to any other voltage potential can result in shorts and/or electrical malfunction of the device.
4. Untoleranced dimensions are nominal target dimensions.
5. All linear dimensions are in millimeters (max/min).

FIGURE 16: 24-contact Very-very-thin Quad Flat No-lead (WQFN)
SST Package Code: QD

TABLE 5: Revision History

Revision	Description	Date
00	• Initial release of data sheet	Sep 2006
01	• Updated pins 9 and 11 in Figure 2 on page 3 • Updated pin 6, 9, and 11 in Table 1 on page 4 • Updated Figure 11 on page 14 • Updated Figure 15 on page 16	Jan 2007
02	• Updated “Product Ordering Information” on page 17	Sep 2007
03	• Revised Product Description on page 1 • Changed signal gain value14 dB globally • Changed low-noise figure to 1.45 dB globally • Edited high temperature stability feature, page 1 • Change low idle current to 75 mA, page 1 • Edited Table 2, DC Electrical Characteristics; Table 3, AC Electrical Characteristics RX Chain; Table 4, AC Electrical Characteristics TX Chain • Replaced Figures 3 through 11 with up-to-date graphs on pages 7 through 13 • Added Figure 5 on page 8 • Added Figure 12 on page 14 • Edited Figure 15 on page 16	Jun 2008
04	• Revised RX chain gain value from 14 to 12 in “Features:” and “Product Description” on page 1 and Table 3 on page 6. • Updated Figures 3 and 5.	Nov 2008
05	• Updated “” on page 19	Feb 2009
06	• Updated document status to “Data Sheet” • Revised IIPE values in Features on page 1 and Table 3 on page 6 • Changed definition of “F” environmental attribute in “Product Ordering Information” on page 17	Nov 2010